



# BTA204-800C

## 3Q Hi-Com Triac

Rev. 3 — 9 May 2011

Product data sheet

## 1. Product profile

### 1.1 General description

Planar passivated high commutation three quadrant triac in a SOT78 (TO-220AB) plastic package intended for use in circuits where high static and dynamic  $dV/dt$  and high  $dI/dt$  can occur. This "series C" triac will commute the full rated RMS current at the maximum rated junction temperature without the aid of a snubber.

### 1.2 Features and benefits

- 3Q technology for improved noise immunity
- High blocking voltage capability
- High commutation capability with maximum false trigger immunity
- High immunity to false turn-on by  $dV/dt$
- Less sensitive gate for high noise immunity
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only

### 1.3 Applications

- General purpose motor control circuits
- Home appliances
- Rectifier-fed DC inductive loads e.g. DC motors and solenoids

### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	-	800	V
$I_{TSM}$	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25\text{ °C}$ ; $t_p = 20\text{ ms}$ ; see <a href="#">Figure 4</a> ; see <a href="#">Figure 5</a>	-	-	25	A
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 107\text{ °C}$ ; see <a href="#">Figure 1</a> ; see <a href="#">Figure 2</a> ; see <a href="#">Figure 3</a>	-	-	4	A

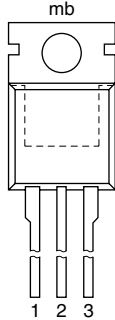
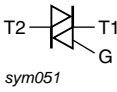


Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ }^\circ\text{C}$ ; see <a href="#">Figure 7</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ }^\circ\text{C}$ ; see <a href="#">Figure 7</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ }^\circ\text{C}$ ; see <a href="#">Figure 7</a>	-	-	35	mA

## 2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		
2	T2	main terminal 2		
3	G	gate		
mb	T2	mounting base; main terminal 2		

**SOT78 (TO-220AB)**

## 3. Ordering information

Table 3. Ordering information

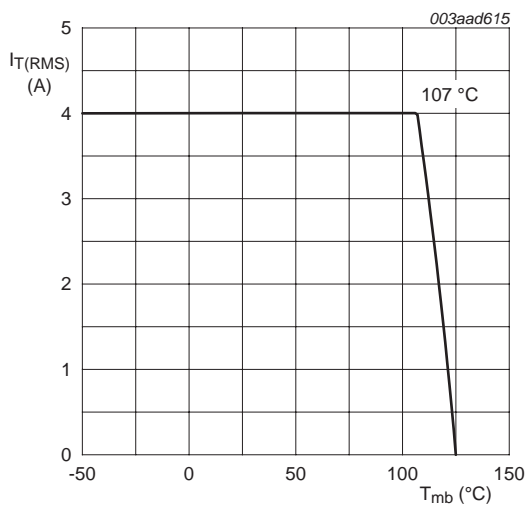
Type number	Package		Version
	Name	Description	
BTA204-800C	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78
BTA204-800C/DG	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

## 4. Limiting values

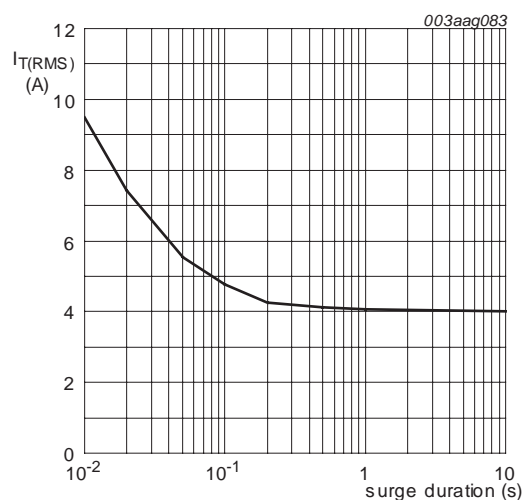
**Table 4. Limiting values**

*In accordance with the Absolute Maximum Rating System (IEC 60134).*

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{\text{DRM}}$	repetitive peak off-state voltage		-	800	V
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_{\text{mb}} \leq 107\text{ °C}$ ; see <a href="#">Figure 1</a> ; see <a href="#">Figure 2</a> ; see <a href="#">Figure 3</a>	-	4	A
$I_{\text{TSM}}$	non-repetitive peak on-state current	full sine wave; $T_{\text{j(init)}} = 25\text{ °C}$ ; $t_{\text{p}} = 20\text{ ms}$ ; see <a href="#">Figure 4</a> ; see <a href="#">Figure 5</a>	-	25	A
		full sine wave; $T_{\text{j(init)}} = 25\text{ °C}$ ; $t_{\text{p}} = 16.7\text{ ms}$	-	27	A
$I^2t$	$I^2t$ for fusing	$t_{\text{p}} = 10\text{ ms}$ ; sine-wave pulse	-	3.1	A <sup>2</sup> s
$dI_{\text{T}}/dt$	rate of rise of on-state current	$I_{\text{T}} = 6\text{ A}$ ; $I_{\text{G}} = 0.2\text{ A}$ ; $dI_{\text{G}}/dt = 0.2\text{ A}/\mu\text{s}$	-	100	A/ $\mu\text{s}$
$I_{\text{GM}}$	peak gate current		-	2	A
$P_{\text{GM}}$	peak gate power		-	5	W
$P_{\text{G(AV)}}$	average gate power	over any 20 ms period	-	0.5	W
$T_{\text{stg}}$	storage temperature		-40	150	°C
$T_{\text{j}}$	junction temperature		-	125	°C



**Fig 1. RMS on-state current as a function of mounting base temperature; maximum values**



$f = 50\text{ Hz}$ ;  $T_{\text{mb}} = 107\text{ °C}$

**Fig 2. RMS on-state current as a function of surge duration; maximum values**

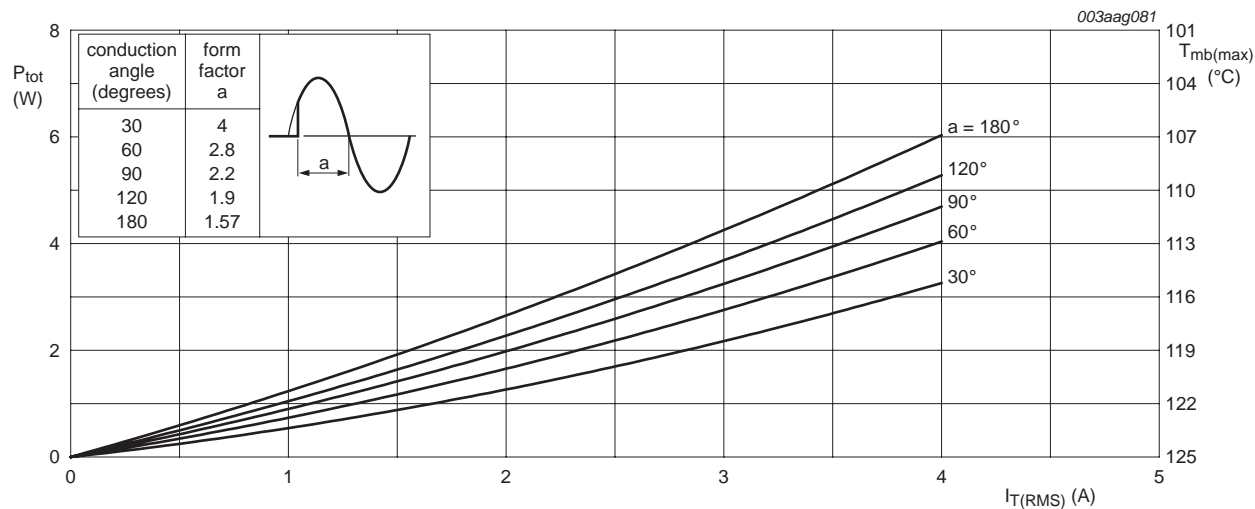


Fig 3. Total power dissipation as a function of RMS on-state current; maximum values

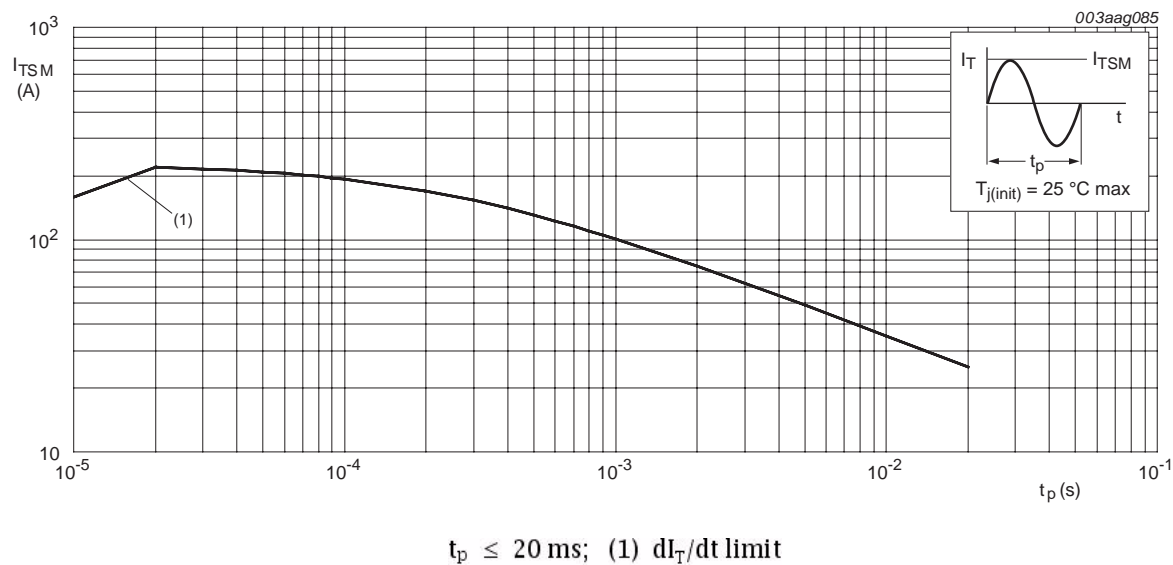
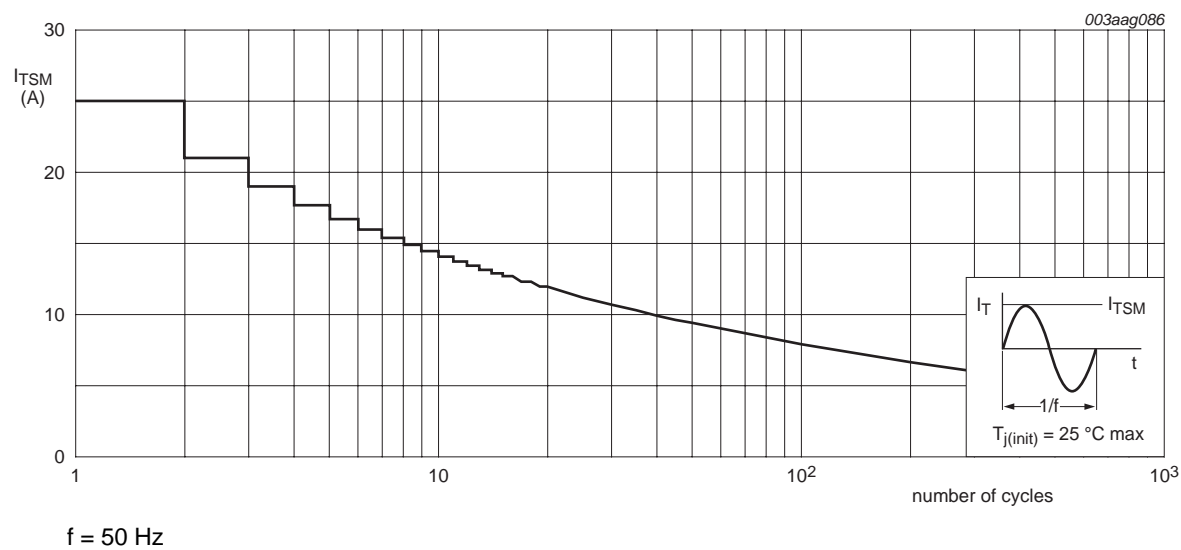


Fig 4. Non-repetitive peak on-state current as a function of pulse width; maximum values

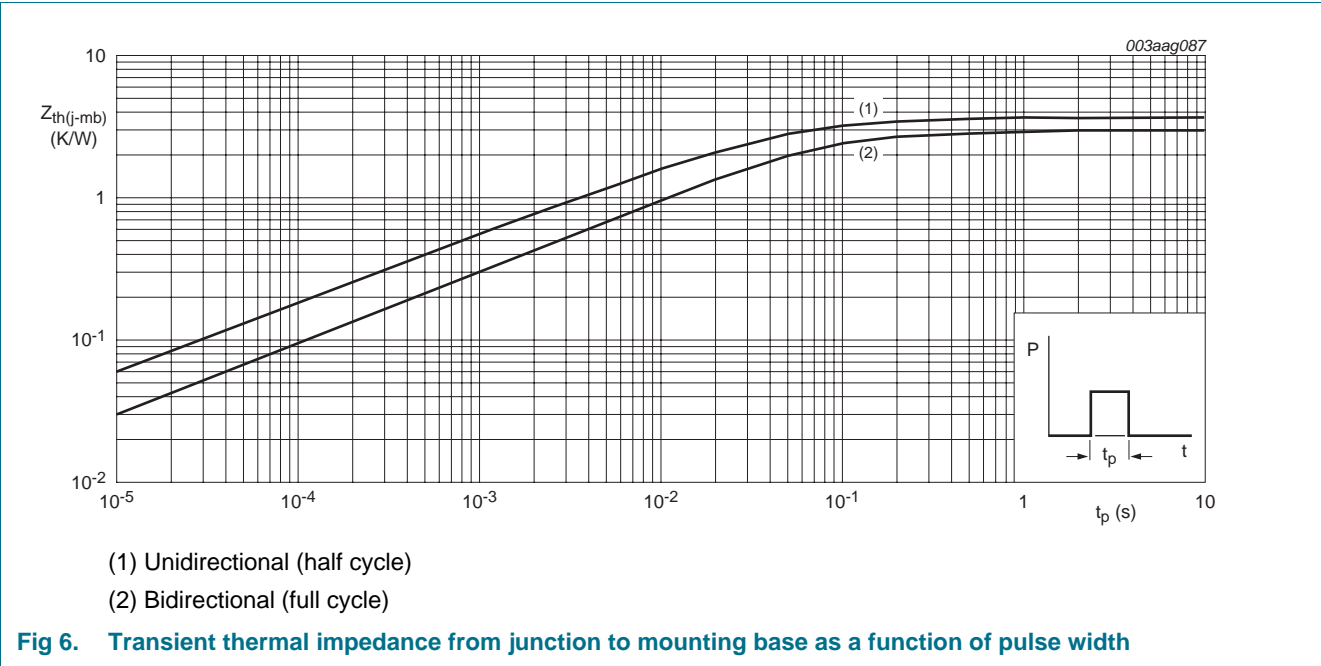


**Fig 5. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values**

5. Thermal characteristics

Table 5. Thermal characteristics

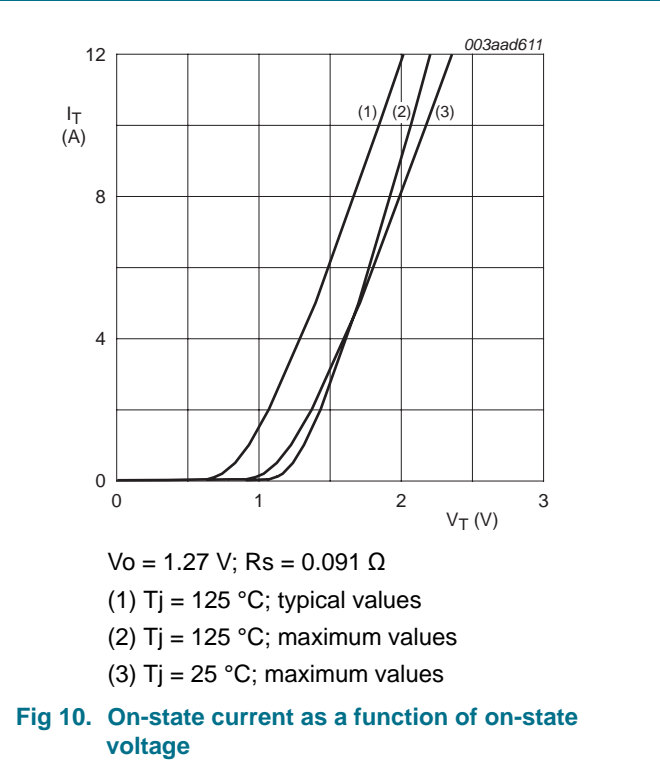
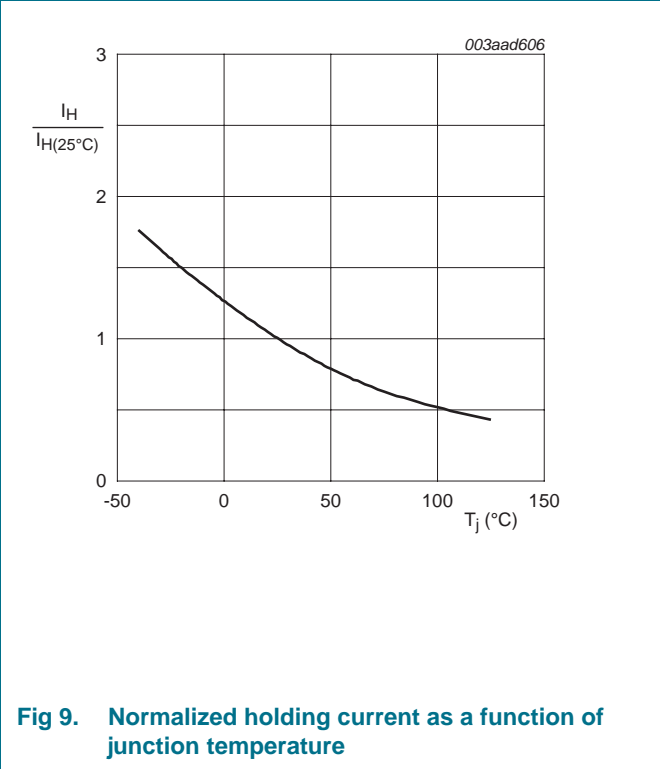
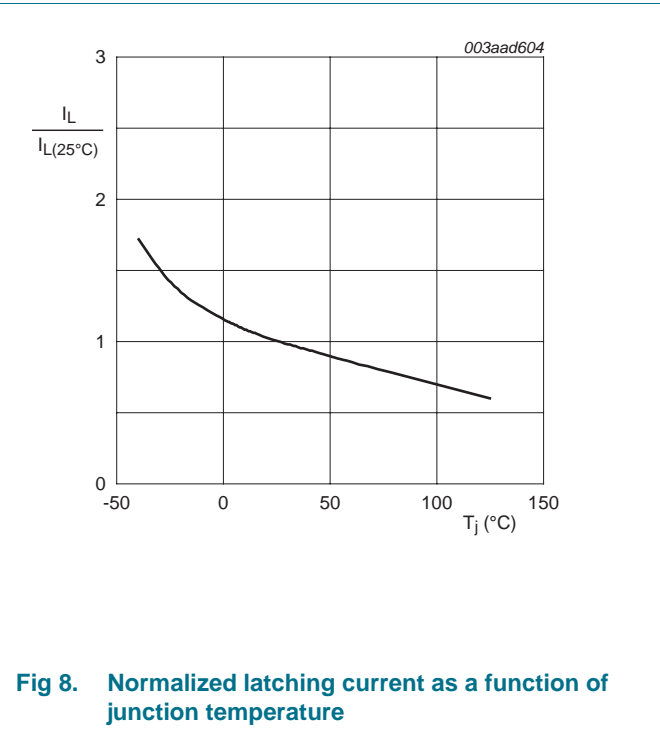
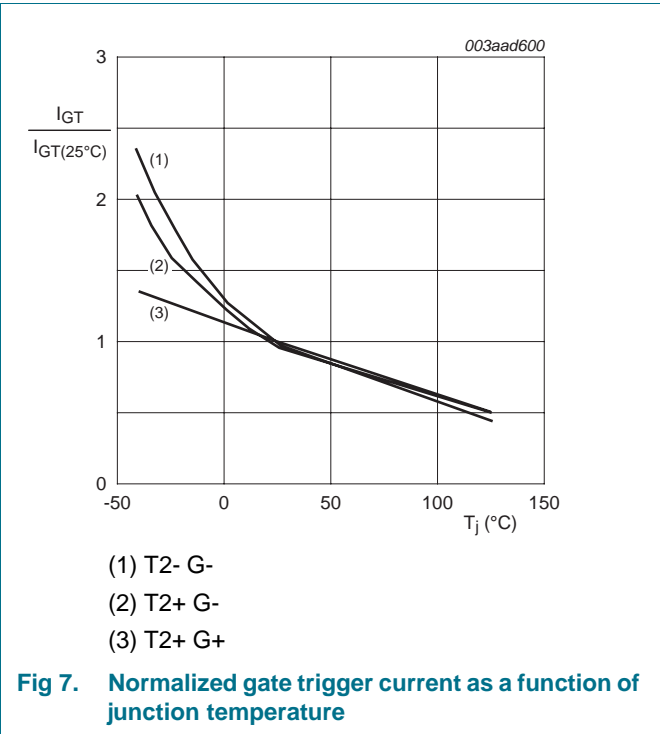
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	full cycle; see <a href="#">Figure 6</a>	-	-	3	K/W
		half cycle; see <a href="#">Figure 6</a>	-	-	3.7	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	60	-	K/W



## 6. Characteristics

**Table 6. Characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	-	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	-	-	35	mA
$I_L$	latching current	$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	-	20	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	-	30	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	-	20	mA
$I_H$	holding current	$V_D = 12\text{ V}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 9</a>	-	-	20	mA
$V_T$	on-state voltage	$I_T = 5\text{ A}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 10</a>	-	1.4	1.7	V
$V_{GT}$	gate trigger voltage	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 11</a>	-	0.7	1.5	V
		$V_D = 400\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 125\text{ °C}$ ; see <a href="#">Figure 11</a>	0.25	0.4	-	V
$I_D$	off-state current	$V_D = 800\text{ V}$ ; $T_j = 125\text{ °C}$	-	0.1	0.5	mA
<b>Dynamic characteristics</b>						
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}$ ; $T_j = 125\text{ °C}$ ; exponential waveform; gate open circuit	1000	-	-	V/ $\mu$ s
$dI_{com}/dt$	rate of change of commutating current	$V_D = 400\text{ V}$ ; $T_j = 125\text{ °C}$ ; $I_{T(RMS)} = 4\text{ A}$ ; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$ ; snubberless condition; gate open circuit	3	-	-	A/ms
$t_{gt}$	gate-controlled turn-on time	$I_{TM} = 12\text{ A}$ ; $V_D = 800\text{ V}$ ; $I_G = 0.1\text{ A}$ ; $dI_G/dt = 5\text{ A}/\mu\text{s}$	-	2	-	$\mu$ s





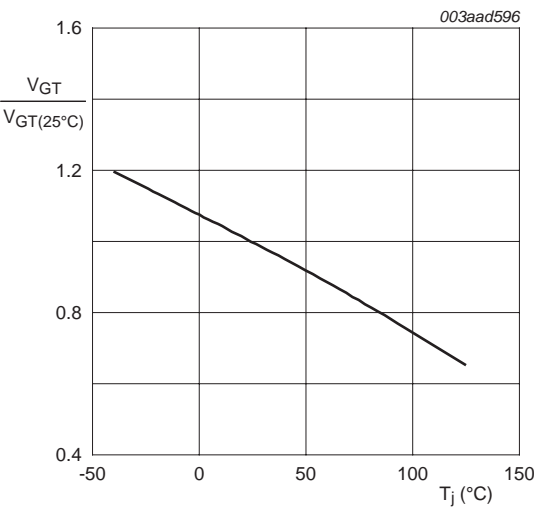


Fig 11. Normalized gate trigger voltage as a function of junction temperature

7. Package outline

Plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB SOT78

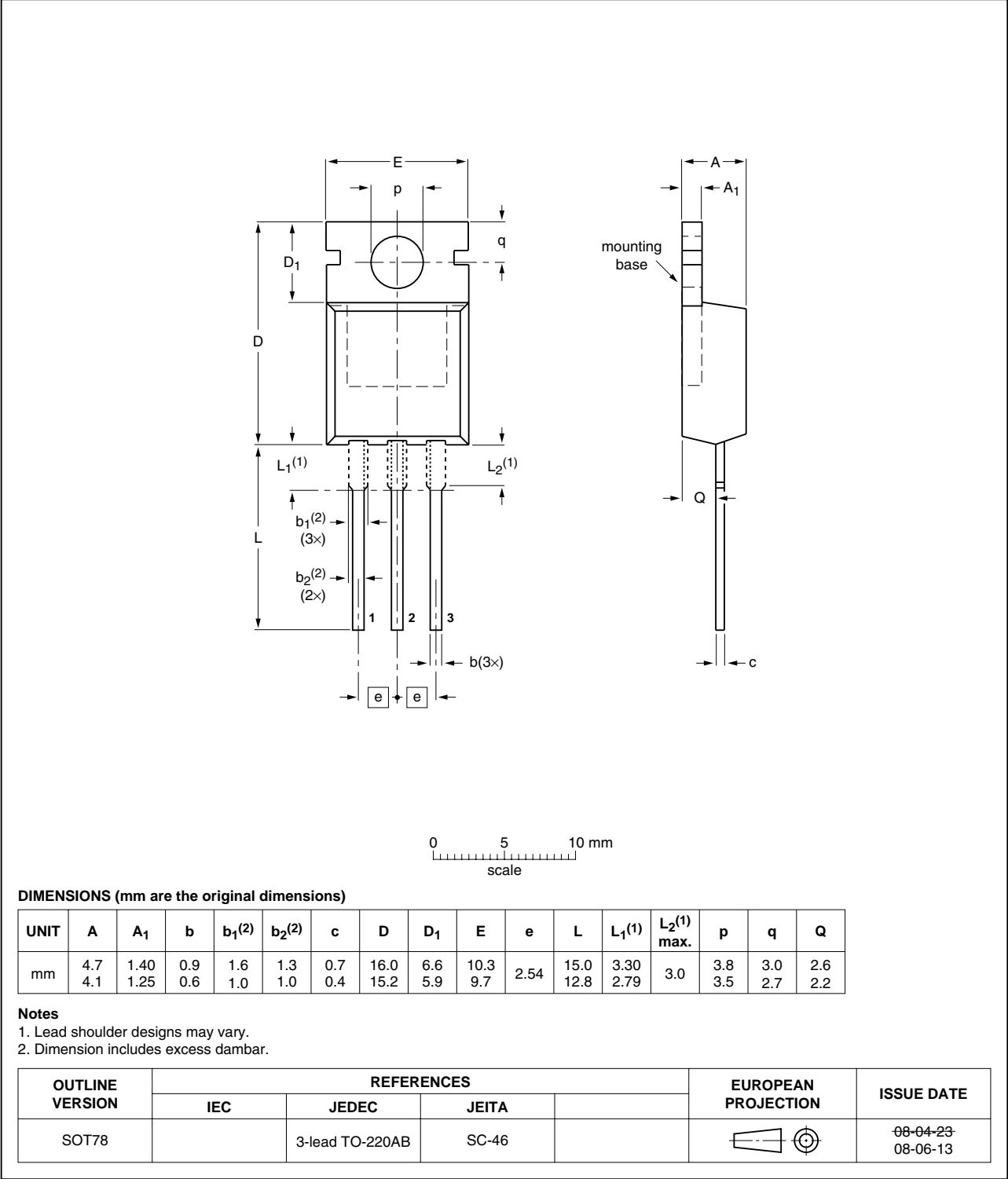


Fig 12. Package outline SOT78 (TO-220AB)

## 8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA204-800C v.3	20110509	Product data sheet	-	BTA204_SERIES_B_C v.2
Modifications:	<ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li><li>• Type number BTA204-800C separated from data sheet BTA204_SERIES_B_C v.2.</li></ul>			
BTA204_SERIES_B_C v.2	19981201	Product specification	-	BTA204_SERIES_B_C v.1

## 9. Legal information

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Document status <sup>[1] [2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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## 11. Contents

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<b>1</b>	<b>Product profile</b> . . . . .	<b>1</b>
1.1	General description . . . . .	1
1.2	Features and benefits . . . . .	1
1.3	Applications . . . . .	1
1.4	Quick reference data . . . . .	1
<b>2</b>	<b>Pinning information</b> . . . . .	<b>2</b>
<b>3</b>	<b>Ordering information</b> . . . . .	<b>2</b>
<b>4</b>	<b>Limiting values</b> . . . . .	<b>3</b>
<b>5</b>	<b>Thermal characteristics</b> . . . . .	<b>6</b>
<b>6</b>	<b>Characteristics</b> . . . . .	<b>7</b>
<b>7</b>	<b>Package outline</b> . . . . .	<b>10</b>
<b>8</b>	<b>Revision history</b> . . . . .	<b>11</b>
<b>9</b>	<b>Legal information</b> . . . . .	<b>12</b>
9.1	Data sheet status . . . . .	12
9.2	Definitions . . . . .	12
9.3	Disclaimers . . . . .	12
9.4	Trademarks . . . . .	13
<b>10</b>	<b>Contact information</b> . . . . .	<b>13</b>

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