



3Q Hi-Com Triac Rev. 05 — 12 April 2011

Product data sheet

1. Product profile

1.1 General description

Planar passivated high commutation three quadrant triac in a SOT78 plastic package. This "series E" triac balances the requirements of commutation performance and gate sensitivity. The "sensitive gate" "series E" is intended for interfacing with low power drivers including microcontrollers.

1.2 Features and benefits

- 3Q technology for improved noise immunity
- Direct interfacing with low power drivers and microcontrollers
- Good immunity to false turn-on by dV/dt
- High commutation capability with sensitive gate
 High voltage capability
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only

1.3 Applications

Electronic thermostats

General purpose motor controls

1.4 Quick reference data

Table 1. **Quick reference data** Symbol Conditions Parameter Min Тур Max Unit V_{DRM} repetitive peak 600 V off-state voltage non-repetitive peak ITSM full sine wave; T_{j(init)} = 25 °C; 65 А _ $t_p = 20 \text{ ms}; \text{ see Figure 4};$ on-state current see Figure 5 RMS on-state current full sine wave; $T_{mb} \le 102 \text{ °C}$; 8 А I_{T(RMS)} see Figure 1; see Figure 2; see Figure 3 Static characteristics gate trigger current $V_D = 12 V; I_T = 0.1 A; T2+G+;$ 10 mΑ IGT T_i = 25 °C; see Figure 7 $V_D = 12 V; I_T = 0.1 A; T2+G-;$ 10 mΑ $T_i = 25 \text{ °C}; \text{ see Figure 7}$ $V_D = 12 V; I_T = 0.1 A; T2-G;$ 10 mΑ T_i = 25 °C; see Figure 7



2. Pinning information

Table 2.	Pinning	j information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		
2	T2	main terminal 2	mb	T2-T1
3	G	gate		`G sym051
mb	Τ2	mounting base; main terminal 2		

3. Ordering information

Table 3.Ordering information

Type number	Package				
	Name	Description	Version		
BTA208-600E	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78		

SOT78 (TO-220AB)

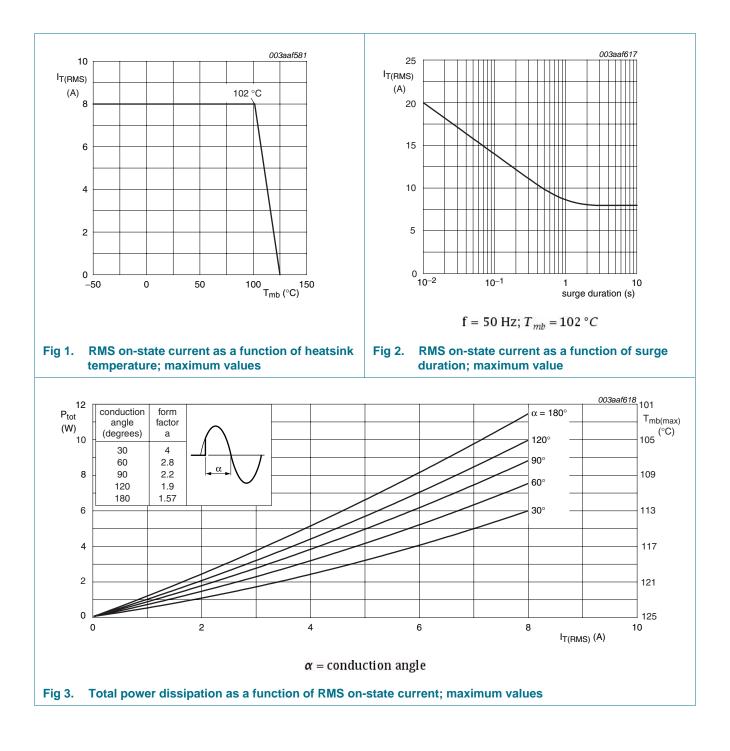
4. Limiting values

Table 4.Limiting values

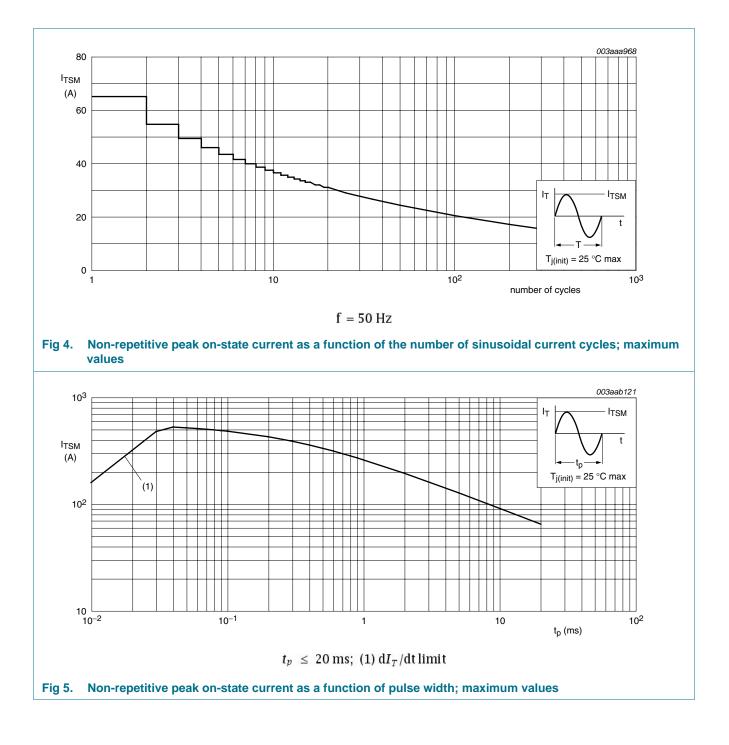
In accordance with the Absolute Maximum Rating System (IEC 60134).

		09 ()			
Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	600	V
I _{T(RMS)}	RMS on-state current	full sine wave; T _{mb} ≤ 102 °C; see <u>Figure 1;</u> see <u>Figure 2</u> ; see <u>Figure 3</u>	-	8	A
I _{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 20 \text{ ms}$; see <u>Figure 4</u> ; see <u>Figure 5</u>	-	65	A
		full sine wave; $T_{j(init)} = 25 \text{ °C}$; $t_p = 16.7 \text{ ms}$	-	72	А
l ² t	I ² t for fusing	t _p = 10 ms; sine-wave pulse	-	21	A ² s
dl _T /dt	rate of rise of on-state current	$I_T = 12 \text{ A}; I_G = 0.2 \text{ A}; dI_G/dt = 0.2 \text{ A}/\mu \text{s}$	-	100	A/µs
I _{GM}	peak gate current		-	2	А
V _{GM}	peak gate voltage		-	5	V
P _{GM}	peak gate power		-	5	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C

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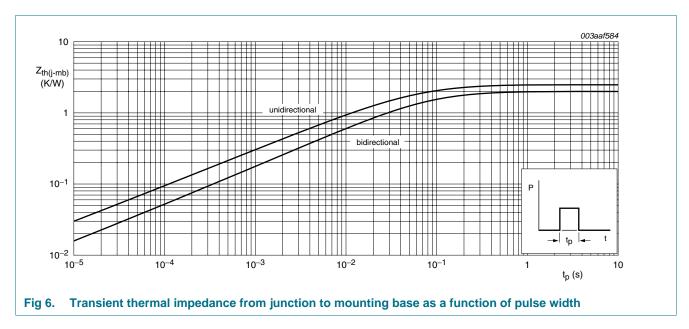
BTA208-600E



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Thermal characteristics 5.

Table 5.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	full cycle; see Figure 6	-	-	2	K/W
		half cycle; see Figure 6	-	-	2.4	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W

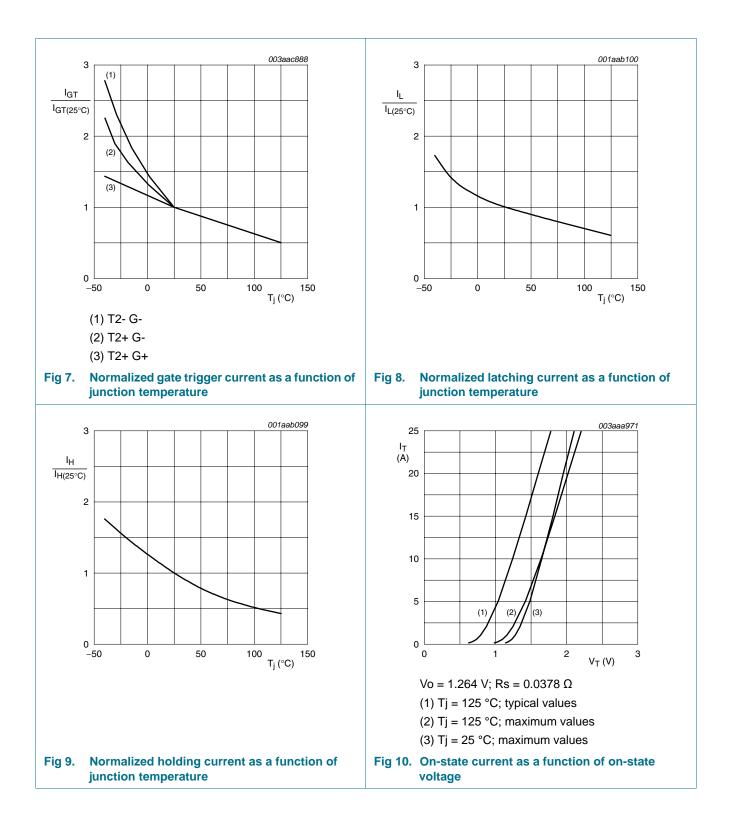


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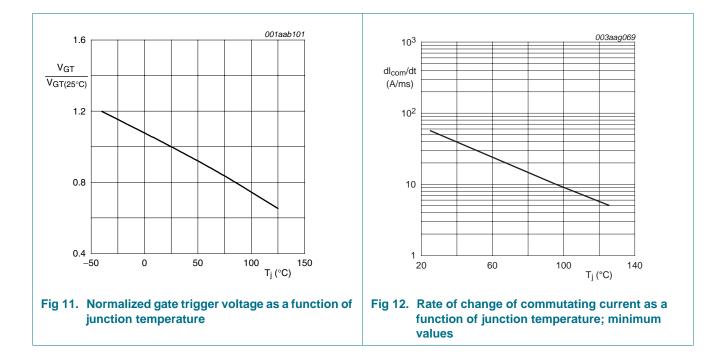
6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; see <u>Figure 7</u>	-	-	10	mA
		$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T2+ G-}; \text{ T}_j = 25 \text{ °C};$ see Figure 7	-	-	10	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; see <u>Figure 7</u>	-	-	10	mA
ΙL	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; see <u>Figure 8</u>	-	-	25	mA
		V _D = 12 V; I _G = 0.1 A; T2+ G-; T _j = 25 °C; see <u>Figure 8</u>	-	-	30	mA
		V _D = 12 V; I _G = 0.1 A; T2- G-; T _j = 25 °C; see <u>Figure 8</u>	-	-	30	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; see <u>Figure 9</u>	-	-	25	mA
V _T	on-state voltage	I _T = 10 A; T _j = 25 °C; see <u>Figure 10</u>	-	-	1.65	V
V_{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; see <u>Figure 11</u>	-	-	1.5	V
		$V_D = 400 \text{ V}; I_T = 0.1 \text{ A}; T_j = 125 \text{ °C};$ see Figure 11	0.25	-	-	V
I _D	off-state current	V _D = 600 V; T _j = 125 °C	-	-	0.5	mA
Dynamic	characteristics					
dV _D /dt	rate of rise of off-state voltage	V _{DM} = 402 V; T _j = 110 °C; exponential waveform; gate open circuit	60	-	-	V/µs
dl _{com} /dt	rate of change of commutating current	V_D = 400 V; T _j = 125 °C; I _{T(RMS)} = 8 A; dV _{com} /dt = 0.1 V/µs; gate open circuit	10	-	-	A/ms
		$V_D = 400 \text{ V}; \text{ T}_j = 125 \text{ °C}; \text{ I}_{T(RMS)} = 8 \text{ A};$ dV _{com} /dt = 10 V/µs; gate open circuit; see <u>Figure 12</u>	5	-	-	A/ms

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Package outline 7.

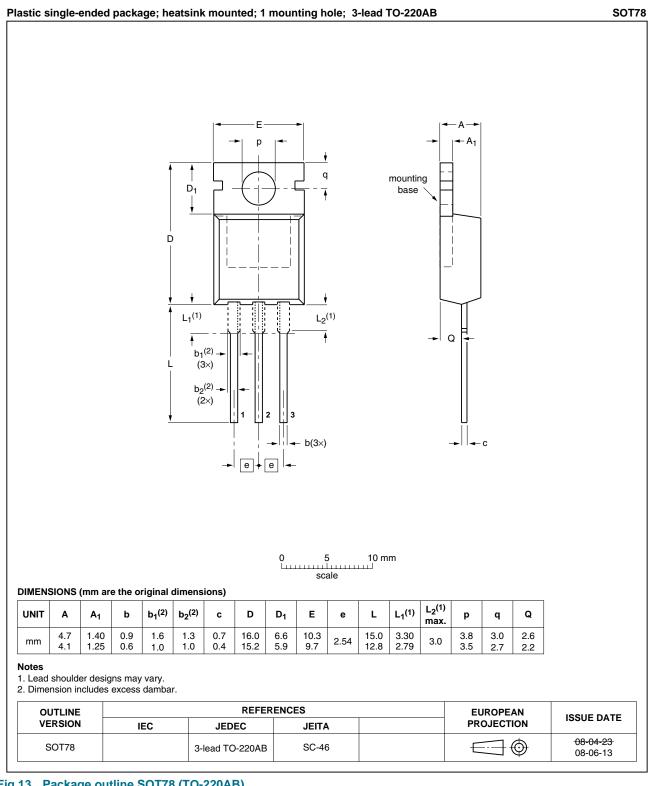


Fig 13. Package outline SOT78 (TO-220AB)

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8. Revision history

Table 7.Revision history				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA208-600E v.5	20110412	Product data sheet	-	BTA208_SERIES_D_E_F v.4
Modifications:		of this data sheet has be of NXP Semiconductors.	en redesigned to c	comply with the new identity
	 Legal texts 	have been adapted to th	e new company na	ame where appropriate.
	 Type numb 	er BTA208-600E separat	ed from data shee	t BTA208_SERIES_D_E_F v.4.
BTA208_SERIES_D_E_F v.4	20020301	Product specification	-	BTA208_SERIES_D_E_F v.3

9. Legal information

9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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