

BUK7514-55A N-channel TrenchMOS standard level FET Rev. 2 – 26 April 2011

Product data sheet

Product profile 1.

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

AEC Q101 compliant

Low conduction losses due to low on-state resistance

1.3 Applications

Automotive and general purpose power switching

1.4 Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|----------------------|--|---|-----|-----|-----|-------|
| Cymbol | i didiliotoi | | | ·γP | max | onine |
| V _{DS} | drain-source voltage | T _j ≥ 25 °C; T _j ≤ 175 °C | - | - | 55 | V |
| I _D | drain current | T _{mb} = 25 °C | - | - | 73 | А |
| P _{tot} | total power dissipation | | - | - | 166 | W |
| Static char | acteristics | | | | | |
| R _{DSon} | drain-source on-state resistance | V_{GS} = 10 V; I_D = 25 A; T_j = 25 °C | - | 12 | 14 | mΩ |
| Avalanche | Ruggedness | | | | | |
| E _{DS(AL)S} | non-repetitive drain-source avalanche energy | $\begin{split} I_D &= 50 \text{ A}; \text{V}_{\text{sup}} \leq 25 \text{ V}; \\ R_{\text{GS}} &= 50 \Omega; \text{V}_{\text{GS}} = 5 \text{V}; \\ T_{j(\text{init})} &= 25 ^{\circ}\text{C}; \\ \text{unclamped} \end{split}$ | - | - | 125 | mJ |



N-channel TrenchMOS standard level FET

2. Pinning information

| Table 2. | Pinning | j information | | |
|----------|---------|--|--------------------|----------------|
| Pin | Symbol | Description | Simplified outline | Graphic symbol |
| 1 | G | gate | | - |
| 2 | D | drain | mb | |
| 3 | S | source | | |
| mb | D | source mounting base; connected to drain | | mbb076 S |
| | | | SOT78A (TO-220AB) | |

3. Ordering information

Table 3.Ordering information

| Type number | Package | | |
|-------------|----------|---|---------|
| | Name | Description | Version |
| BUK7514-55A | TO-220AB | plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB | SOT78A |

N-channel TrenchMOS standard level FET

4. Limiting values

Table 4. Limiting values

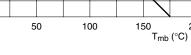
In accordance with the Absolute Maximum Rating System (IEC 60134).

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| | | 5 5 7 | | | |
|----------------------|--|---|-----|-----|------|
| Symbol | Parameter | Conditions | Min | Max | Unit |
| V _{DS} | drain-source voltage | T _j ≥ 25 °C; T _j ≤ 175 °C | - | 55 | V |
| V _{DGR} | drain-gate voltage | $R_{GS} = 20 \text{ k}\Omega$ | - | 55 | V |
| V _{GS} | gate-source voltage | | -20 | 20 | V |
| I _D | drain current | T _{mb} = 25 °C | - | 73 | А |
| | | T _{mb} = 100 °C | - | 52 | А |
| I _{DM} | peak drain current | T _{mb} = 25 °C; pulsed | - | 266 | А |
| P _{tot} | total power dissipation | T _{mb} = 25 °C | - | 166 | W |
| T _{stg} | storage temperature | | -55 | 175 | °C |
| Tj | junction temperature | | -55 | 175 | °C |
| Source-drai | in diode | | | | |
| I _S | source current | T _{mb} = 25 °C | - | 73 | А |
| I _{SM} | peak source current | pulsed; T _{mb} = 25 °C | - | 266 | А |
| Avalanche I | Ruggedness | | | | |
| E _{DS(AL)S} | non-repetitive drain-source avalanche energy | $\begin{split} I_D &= 50 \text{ A}; V_{sup} \leq 25 \text{ V}; R_{GS} = 50 \Omega; \\ V_{GS} &= 5 \text{ V}; T_{j(init)} = 25 ^\circ\text{C}; unclamped \end{split}$ | - | 125 | mJ |
| | | | | | |

avalanche energy



$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

Fig 1. Normalized total power dissipation as a function of mounting base temperature

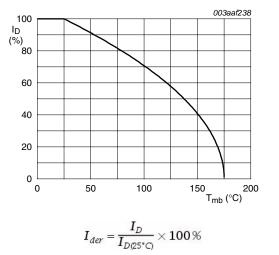




Fig 2. Normalized continuous drain current as a function of mounting base temperature

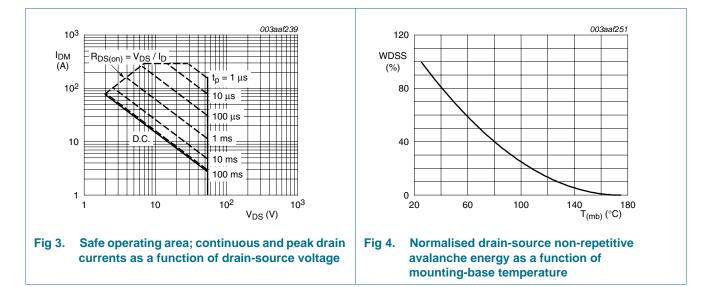
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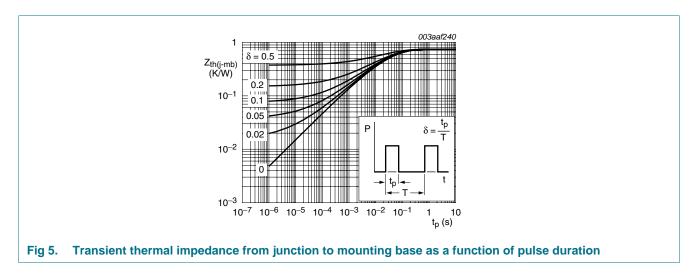
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5. Thermal characteristics

Table 5. Thermal characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------------|---|-------------|-----|-----|-----|------|
| R _{th(j-mb)} | thermal resistance from junction to mounting base | | - | - | 0.9 | K/W |
| R _{th(j-a)} | thermal resistance from junction to ambient | in free air | - | 60 | - | K/W |

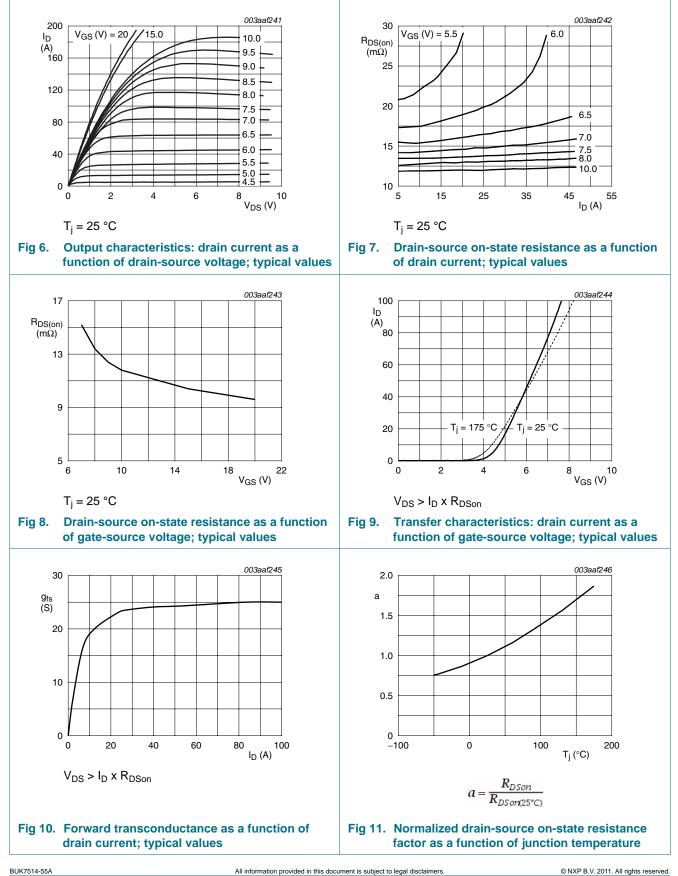


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6. Characteristics

| Table 6. | Characteristics | | | | | |
|----------------------|-------------------------------|--|-----|------|------|------|
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
| Static cha | aracteristics | | | | | |
| V _{(BR)DSS} | drain-source | $I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$ | 55 | - | - | V |
| | breakdown voltage | $I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$ | 50 | - | - | V |
| V _{GS(th)} | gate-source threshold | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$ | 2 | 3 | 4 | V |
| | voltage | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$ | 1 | - | - | V |
| | | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ °C}$ | - | - | 4.4 | V |
| I _{DSS} | drain leakage current | $V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$ | - | 0.05 | 10 | μA |
| | | $V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$ | - | - | 500 | μA |
| I _{GSS} | gate leakage current | $V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$ | - | 2 | 100 | nA |
| | | V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C | - | 2 | 100 | nA |
| R _{DSon} | drain-source on-state | V_{GS} = 10 V; I_D = 25 A; T_j = 175 °C | - | - | 28 | mΩ |
| resistance | | V _{GS} = 10 V; I _D = 25 A; T _j = 25 °C | - | 12 | 14 | mΩ |
| Dynamic | characteristics | | | | | |
| C _{iss} | input capacitance | $V_{GS} = 0 V; V_{DS} = 25 V; f = 1 MHz;$ | - | 1848 | 2464 | pF |
| C _{oss} | output capacitance | $T_j = 25 \ ^{\circ}C$ | - | 421 | 506 | pF |
| C _{rss} | reverse transfer capacitance | | - | 231 | 317 | pF |
| d(on) | turn-on delay time | $V_{DS} = 30 \text{ V}; \text{ R}_{L} = 1.2 \Omega; \text{ V}_{GS} = 5 \text{ V};$ | - | 17 | 26 | ns |
| t _r | rise time | R _{G(ext)} = 10 Ω; T _j = 25 °C | - | 79 | 119 | ns |
| t _{d(off)} | turn-off delay time | | - | 57 | 80 | ns |
| t _f | fall time | | - | 51 | 71 | ns |
| L _D | internal drain inductance | measured from drain lead 6 mm from package to centre of die; $T_j = 25 ^{\circ}\text{C}$ | - | 4.5 | - | nH |
| | | measured from contact screw on tab to centre of die; $T_j = 25 \text{ °C}$ | - | 3.5 | - | nH |
| -s | internal source inductance | measured from source lead to source bond pad; $T_j = 25 \text{ °C}$ | - | 7.5 | - | nH |
| Source-d | rain diode | | | | | |
| V _{SD} | source-drain voltage | $I_{S} = 25 \text{ A}; V_{GS} = 0 \text{ V}; T_{j} = 25 \text{ °C}$ | - | 0.85 | 1.2 | V |
| | | $I_{S} = 73 \text{ A}; V_{GS} = 0 \text{ V}; T_{j} = 25 \text{ °C}$ | - | 1.1 | - | V |
| t _{rr} | reverse recovery time | $I_{S} = 73 \text{ A}; \text{ dI}_{S}/\text{dt} = -100 \text{ A}/\mu\text{s};$ | - | 54 | - | ns |
| Q _r | recovered charge | V _{GS} = -10 V; V _{DS} = 30 V; T _j = 25 °C | - | 0.12 | - | μC |

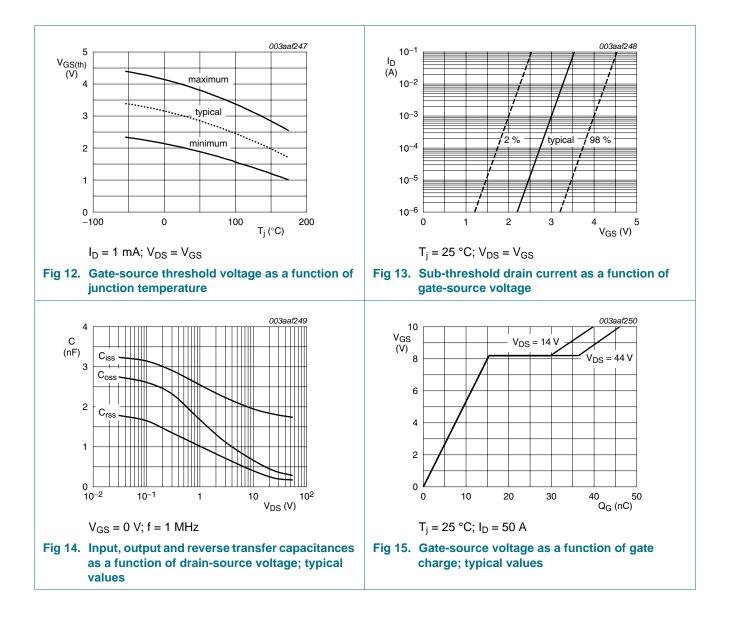
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7. Package outline

| | | | | | | | | | | mo | unting ase | | -220A | | | |
|-------------------------------|-------------------------------|---------------------------------------|------------------------|------------------------------------|------------------------|--------------------------|------------------|-------------------------|----------|-----------|---------------------------------------|------------------------|------------------------|------------------------|------------------------|------------|
| | | | | | | | | | | | | | | | | |
| | | | | | | | 0 LLL | tiiii sca | huuu | 0 mm | | | | | | |
| | | nm are ti | | | | | | sca | | <u> </u> | L (1) | L2 | - | - | - | 7 |
| UNIT | IONS (n A 4.5 | A ₁ | he origin b 0.9 | nal dime b1 1.3 | nsions) c | D | D1 | E | ale e | L | L ₁ ⁽¹⁾ 3.30 | L ₂ max. | p 3.8 | q 3.0 | Q 2.6 | |
| DIMENS UNIT mm | Α | | b | b ₁ | с | | | sca | | <u> </u> | L1 ⁽¹⁾ 3.30 2.79 | L2 max. 3.0 | p 3.8 3.6 | q 3.0 2.7 | Q 2.6 2.2 | - |
| UNIT mm | A 4.5 | A ₁ 1.39 | b 0.9 | b 1 1.3 | c 0.7 | D 15.8 | D1 6.4 | E 10.3 | ale e | L 15.0 | 3.30 | max. | 3.8 | 3.0 | 2.6 | |
| UNIT mm lote . Termi | A 4.5 4.1 nals in th | A ₁ 1.39 | b 0.9 0.6 | b 1 1.3 1.0 | с 0.7 | D 15.8 15.2 | D1 6.4 5.9 | sca E 10.3 9.7 | ale e | L 15.0 | 3.30 | max. | 3.8 3.6 | 3.0 2.7 | 2.6 2.2 | |
| UNIT mm lote . Termi | A 4.5 4.1 | A ₁ 1.39 1.27 | b 0.9 0.6 | b 1 1.3 1.0 inned. | c 0.7 0.4 | D 15.8 15.2 | D1 6.4 | sca E 10.3 9.7 | ale e | L 15.0 | 3.30 | max. | 3.8 3.6 EUR | 3.0 | 2.6 2.2 | ISSUE DATE |

Fig 16. Package outline SOT78A (TO-220AB)

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N-channel TrenchMOS standard level FET

8. Revision history

| Table 7. Revision hi | story | | | |
|----------------------|--|-------------------------------------|---------------------------|---------------------------|
| Document ID | Release date | Data sheet status | Change notice | Supersedes |
| BUK7514-55A v.2 | 20110426 | Product data sheet | - | BUK7514_7614-55A_1 |
| Modifications: | The format of this of NXP Semiconduction | data sheet has been rede ictors. | signed to comply with the | e new identity guidelines |
| | Legal texts have be | een adapted to the new c | ompany name where app | propriate. |
| | Type number BUK | 7514-55A separated form | n data sheet BUK7514_76 | 614-55A_1. |
| BUK7514_7614-55A_1 | 20000701 | Product specification | - | - |

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| Document status [1] [2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
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BUK7514-55A

N-channel TrenchMOS standard level FET

11. Contents

| 1 | Product profile1 |
|-----|--------------------------|
| 1.1 | General description1 |
| 1.2 | Features and benefits1 |
| 1.3 | Applications1 |
| 1.4 | Quick reference data1 |
| 2 | Pinning information2 |
| 3 | Ordering information2 |
| 4 | Limiting values3 |
| 5 | Thermal characteristics4 |
| 6 | Characteristics5 |
| 7 | Package outline8 |
| 8 | Revision history9 |
| 9 | Legal information10 |
| 9.1 | Data sheet status |
| 9.2 | Definitions10 |
| 9.3 | Disclaimers |
| 9.4 | Trademarks11 |
| 10 | Contact information11 |

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