

N-channel TrenchMOS standard level FET Rev. 2 — 26 April 2011

Product data sheet

Product profile 1.

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

1.2 Features and benefits

AEC Q101 compliant

Low conduction losses due to low on-state resistance

1.3 Applications

Automotive and general purpose power switching

1.4 Quick reference data

Table 1. Quick reference data

$V_{DS} \qquad drain-source voltage \qquad T_{j} \ge 25 \ ^{\circ}C; \ T_{j} \le 175 \ ^{\circ}C \qquad - \qquad - \qquad 1$ $I_{D} \qquad drain \ current \qquad T_{mb} = 25 \ ^{\circ}C \qquad - \qquad - \qquad 4$ $P_{tot} \qquad total \ power \ dissipation \qquad - \qquad - \qquad 1$ $Static \ characteristics \qquad \qquad$							
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	mbol	Parameter	Conditions	Min	Тур	Ma x	Uni t
$\begin{array}{c c c c c c c c c c c c c c c c c c c $	S	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	100	V
		drain current	T _{mb} = 25 °C	-	-	47	А
$ \begin{array}{c} R_{DSon} & \text{drain-source on-state} & V_{GS} = 10 \; V; \; I_{D} = 25 \; A; & - \; 20 \; 2 \\ \hline T_{j} = 25 \; ^{\circ} C & & \\ \hline Avalanche ruggedness & & & \\ \hline E_{DS(AL)S} & \text{non-repetitive} & I_{D} = 30 \; A; \; V_{sup} \leq 25 \; V; & - \; - \; 4 \\ \hline drain-source avalanche & R_{GS} = 50 \; \Omega; \; V_{GS} = 5 \; V; \end{array} $	t	total power dissipation		-	-	166	W
$\label{eq:resistance} \begin{array}{c} T_{j} = 25 \ ^{\circ}\text{C} \end{array}$	atic chara	cteristics					
$ E_{DS(AL)S} \begin{array}{l} \text{non-repetitive} & I_D = 30 \text{ A}; V_{sup} \leq 25 \text{ V}; \\ \text{drain-source avalanche} & R_{GS} = 50 \Omega; V_{GS} = 5 \text{ V}; \end{array} $	Son			-	20	28	mΩ
drain-source avalanche $R_{GS} = 50 \Omega; V_{GS} = 5 V;$	alanche	ruggedness					
energy T _{j(init)} = 25 °C; unclamped	S(AL)S	drain-source avalanche		-	-	45	mJ



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2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	_	_
2	D	drain	mb	
3	S	source		
mb	S source D mounting base; connected to drain		mbb076 S	
			SOT78A (TO-220AB)	

3. Ordering information

Table 3.Ordering information

Type number	Package		
	Name	Description	Version
BUK7528-100A	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78A

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4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

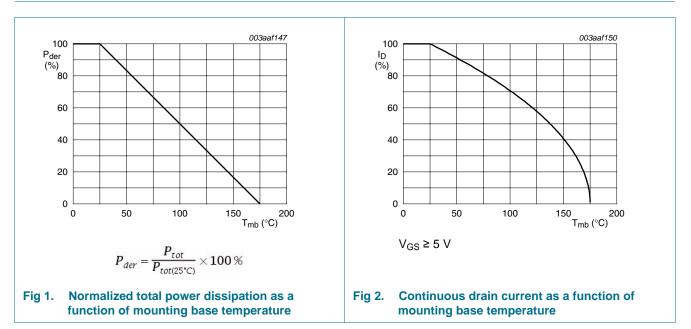
Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	100	V
V _{DGR}	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	100	V
V _{GS}	gate-source voltage		-20	20	V
I _D	drain current	T _{mb} = 100 °C	-	33	А
		T _{mb} = 25 °C	-	47	А
I _{DM}	peak drain current	T _{mb} = 25 °C; pulsed	-	187	А
P _{tot}	total power dissipation	T _{mb} = 25 °C	-	166	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
Source-drai	in diode				
I _S	source current	T _{mb} = 25 °C	-	47	А
I _{SM}	peak source current	pulsed; T _{mb} = 25 °C	-	187	А
Avalanche I	ruggedness				
E _{DS(AL)S}	non-repetitive drain-source	$I_D = 30$ A; $V_{sup} \le 25$ V; $R_{GS} = 50$ Ω;	-	45	mJ



non-repetitive drain-source avalanche energy

 $I_D = 30 \text{ A}; \text{ } V_{sup} \leq 25 \text{ V}; \text{ } \text{R}_{GS} = 50 \text{ } \Omega; \\ V_{GS} = 5 \text{ V}; \text{ } \text{T}_{j(init)} = 25 \text{ }^\circ\text{C}; \text{ unclamped}$

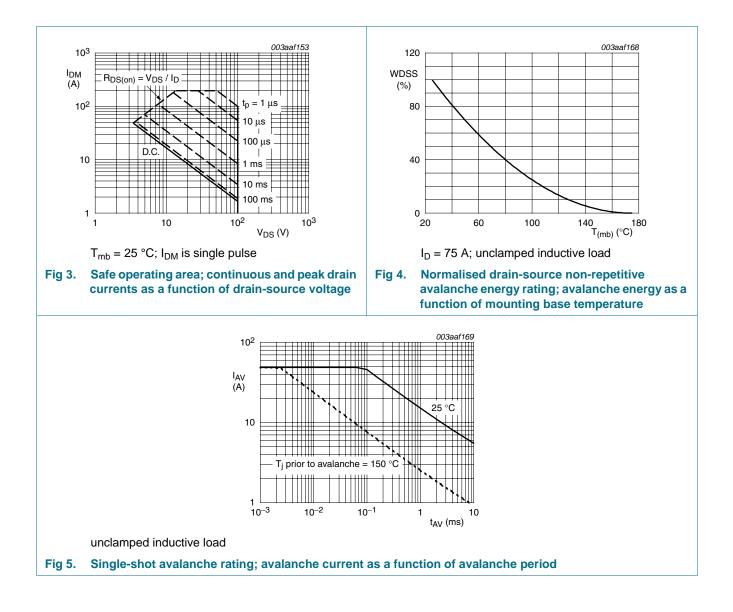
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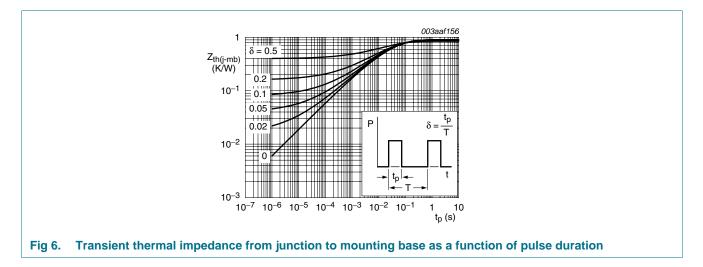
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5. Thermal characteristics

Table 5.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base		-	-	0.9	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W



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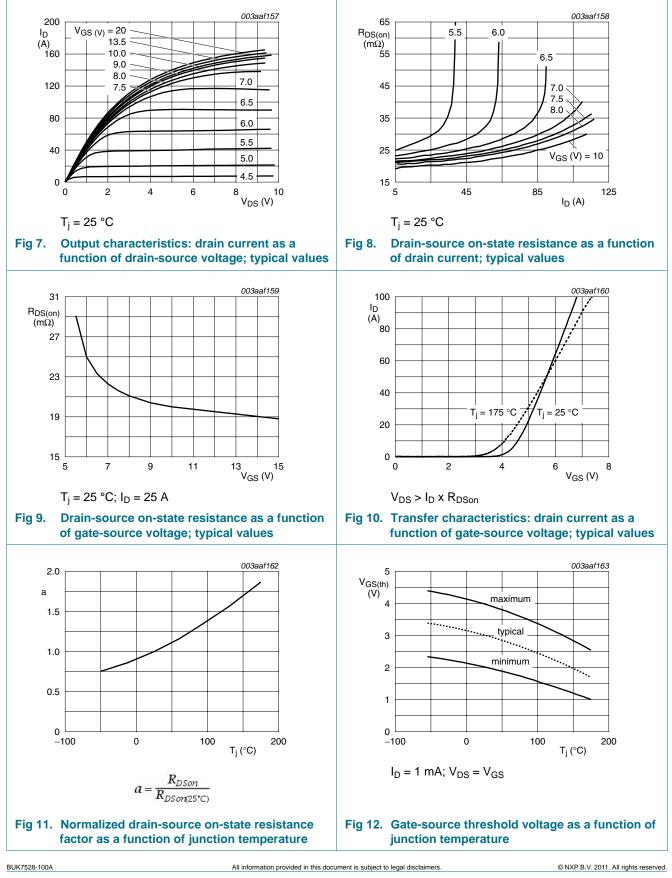
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6. Characteristics

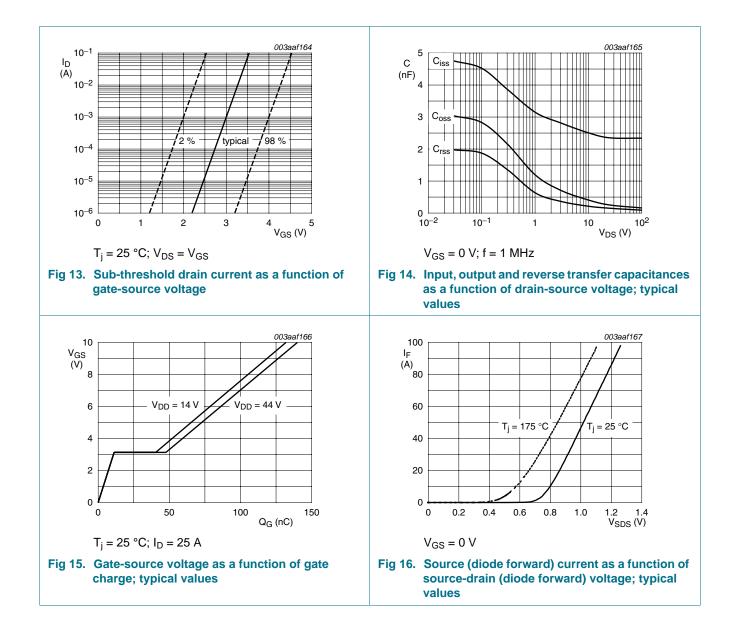
Sympol	Parameter	Conditions	Min	Тур	Max	Unit
Symbol	aracteristics	Conditions	NVIII I	тур	Max	Unit
			100	-	-	V
V _{(BR)DSS}	drain-source breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$				-
		$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	89	-	-	V
V _{GS(th)}	gate-source threshold voltage	$I_{D} = 1 \text{ mA}; V_{DS} = V_{GS}; T_{j} = 25 \text{ °C}$	2	3	4	V
	volidge	$I_{D} = 1 \text{ mA}; V_{DS} = V_{GS}; T_{j} = -55 \text{ °C}$	-	-	4.4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ °C}$	1	-	-	V
I _{DSS}	drain leakage current	V_{DS} = 100 V; V_{GS} = 0 V; T_j = 175 °C	-	-	500	μA
		V_{DS} = 100 V; V_{GS} = 0 V; T_j = 25 °C	-	0.05	10	μA
I _{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	2	100	nA
R _{DSon}	drain-source on-state	V _{GS} = 10 V; I _D = 25 A; T _i = 175 °C	-	-	76	mΩ
	resistance	V _{GS} = 10 V; I _D = 25 A; T _i = 25 °C	-	20	28	mΩ
Dynamic	characteristics					
C _{iss}	input capacitance	V _{GS} = 0 V; V _{DS} = 25 V; f = 1 MHz;	-	2320	3100	pF
C _{oss}	output capacitance	$T_j = 25 \ ^{\circ}C$	-	315	378	pF
C _{rss}	reverse transfer		-	187	256	pF
- 133	capacitance					,
t _{d(on)}	turn-on delay time	$V_{DS} = 30 \text{ V}; \text{ R}_{L} = 1.2 \Omega; \text{ V}_{GS} = 10 \text{ V};$	-	15	23	ns
		R _{G(ext)} 10 Ω; T _j = 25 °C				
t _r	rise time	$V_{DS} = 30 \text{ V}; \text{ R}_{L} = 1.2 \Omega; \text{ V}_{GS} = 10 \text{ V};$	-	70	105	ns
t _{d(off)}	turn-off delay time	$R_{G(ext)} = 10 \ \Omega; \ T_j = 25 \ ^{\circ}C$	-	83	116	ns
t _f	fall time		-	45	63	ns
L _D	internal drain inductance	from drain lead 6 mm from package to centre of die; $T_j = 25 \text{ °C}$	-	4.5	-	nH
		from contact screw on tab to centre of die; $T_j = 25$ °C	-	3.5	-	nH
L _S	internal source inductance	from source lead to source bond pad; $T_j = 25 \text{ °C}$	-	7.5	-	nH
Source-d	rain diode	· · ·				
V _{SD}	source-drain voltage	I _S = 25 A; V _{GS} = 0 V; T _i = 25 °C	-	0.85	1.2	V
	Ũ	$I_{\rm S} = 47$ A; $V_{\rm GS} = 0$ V; $T_{\rm i} = 25$ °C	-	1.1	-	V
t _{rr}	reverse recovery time	$I_{S} = 47 \text{ A}; \text{ dI}_{S}/\text{dt} = -100 \text{ A}/\mu\text{s};$	-	66	-	ns

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7. Package outline

0 5 10 mm Lutitud scale
DIMENSIONS (mm are the original dimensions) UNIT A A ₁ b b ₁ c D D ₁ E e L $L_1^{(1)}$ L_2^{2} p q Q
mm 4.5 1.39 0.9 1.3 0.7 15.8 6.4 10.3 2.54 15.0 3.30 2.0 3.8 3.0 2.6
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

Fig 17. Package outline SOT78A (TO-220AB)

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8. Revision history

Table 7.	Revision history				
Document	ID	Release date	Data sheet status	Change notice	Supersedes
BUK7528-1	00A v.2	20110426	Product data sheet	-	BUK7528_7628-100A_1
Modification	ns:		t of this data sheet has be of NXP Semiconductors.	•	nply with the new identity
		 Legal texts 	have been adapted to the	ne new company name	e where appropriate.
		 Type number 	per BUK7528-100A separ	rated from data sheet	BUK7528_7628-100A_1.
BUK7528_	7628-100A_1	20000301	Product specification	-	-

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9. Legal information

9.1 Data sheet status

Document status [1] [2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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