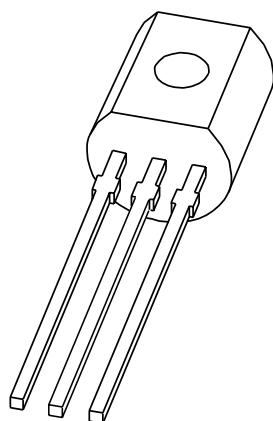


DATA SHEET



JC327

PNP general purpose transistor

Product specification
Supersedes data of 1999 Apr 27

2004 Dec 08

PNP general purpose transistor

JC327

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 45 V).

APPLICATIONS

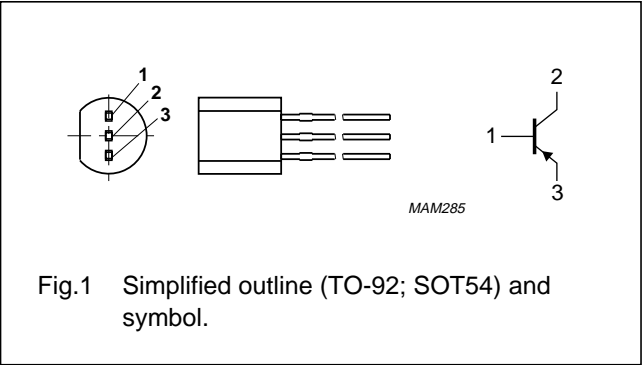
- General purpose switching and amplification, e.g. driver and output stages of audio amplifiers.

DESCRIPTION

PNP transistor in a TO-92; SOT54 plastic package.
NPN complement: JC337.

PINNING

PIN	DESCRIPTION
1	base
2	collector
3	emitter



ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
JC327-25	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54

PNP general purpose transistor

JC327

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{CBO}	collector-base voltage	open emitter	–	–50	V
V_{CEO}	collector-emitter voltage	open base; $I_C = -10$ mA	–	–45	V
V_{EBO}	emitter-base voltage	open collector	–	–5	V
I_C	collector current (DC)		–	–500	mA
I_{CM}	peak collector current		–	–1	A
I_{BM}	peak base current		–	–200	mA
P_{tot}	total power dissipation	$T_{amb} \leq 25$ °C; note 1	–	625	mW
T_{stg}	storage temperature		–65	+150	°C
T_j	junction temperature		–	150	°C
T_{amb}	ambient temperature		–65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th(j-a)}$	thermal resistance from junction to ambient	note 1	200	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 $T_{amb} = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CBO}	collector-base cut-off current	$V_{CB} = -20$ V; $I_E = 0$ A	–	–	–100	nA
		$V_{CB} = -20$ V; $I_E = 0$ A; $T_j = 150$ °C	–	–	–5	μA
I_{EBO}	emitter-base cut-off current	$V_{EB} = -5$ V; $I_C = 0$ A	–	–	–100	nA
h_{FE}	DC current gain	$V_{CE} = -1$ V				
		$I_C = -100$ mA	160	–	400	
		$I_C = -500$ mA	40	–	–	
V_{CEsat}	collector-emitter saturation voltage	$I_C = -500$ mA; $I_B = -50$ mA	–	–	–700	mV
V_{BE}	base-emitter voltage	$V_{CE} = -1$ V; $I_C = -500$ mA; note 1	–	–	–1.2	V
C_c	collector capacitance	$V_{CB} = -10$ V; $I_E = i_e = 0$ A; $f = 1$ MHz	–	8	–	pF
f_T	transition frequency	$V_{CE} = -5$ V; $I_C = -10$ mA; $f = 100$ MHz	80	–	–	MHz

Note

1. V_{BE} decreases by about –2 mV/K with increasing temperature.

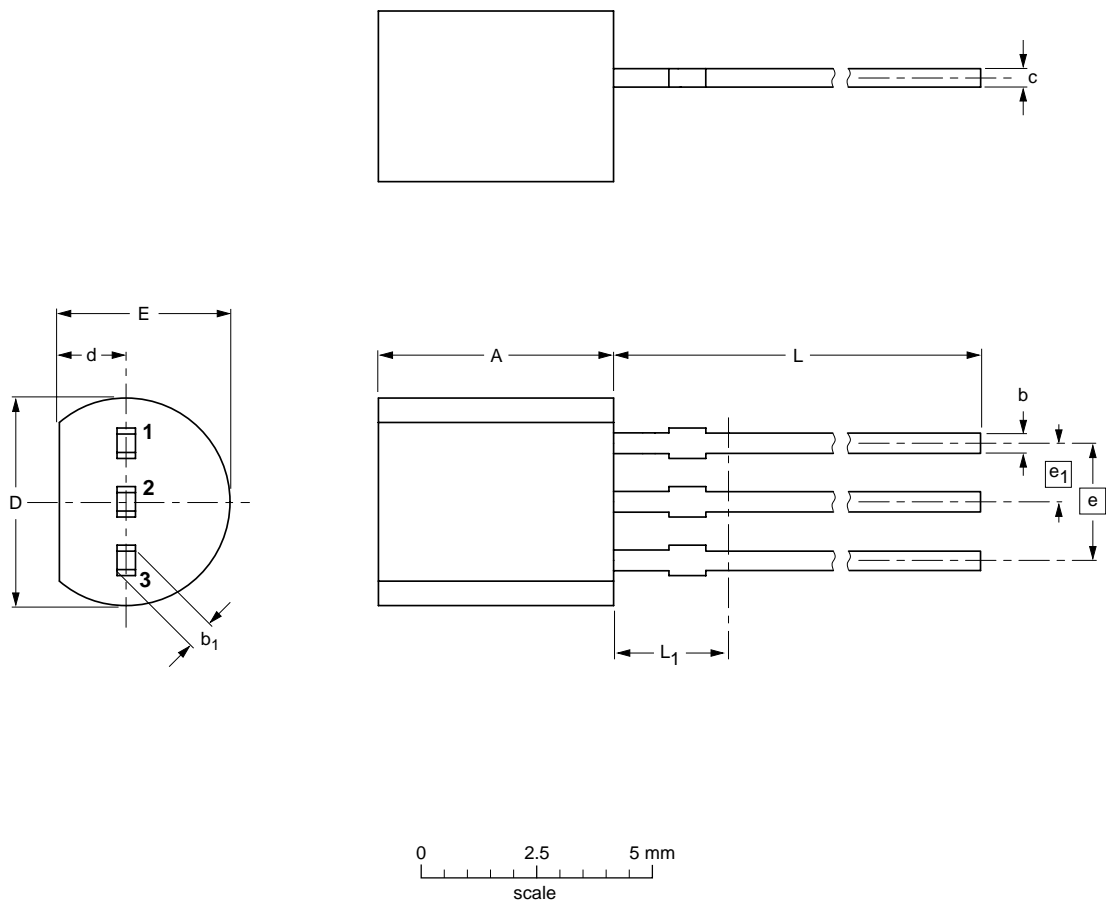
PNP general purpose transistor

JC327

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54




DIMENSIONS (mm are the original dimensions)

UNIT	A	b	b ₁	c	D	d	E	e	e ₁	L	L ₁ ⁽¹⁾ max.
mm	5.2 5.0	0.48 0.40	0.66 0.55	0.45 0.38	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

Note

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	JEITA			
SOT54		TO-92	SC-43A			04-06-28 04-11-16

PNP general purpose transistor

JC327

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾⁽³⁾	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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Notes

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3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

DEFINITIONS

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