

# NX7002AK 60 V, single N-channel Trench MOSFET 13 February 2013

Product data sheet

## 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

## 2. Features and benefits

- Very fast switching
- Trench MOSFET technology
- ESD protected

## 3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

## 4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>amb</sub> = 25 °C		-	-	60	V
V <sub>GS</sub>	gate-source voltage	-		-20	-	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>sp</sub> = 25 °C		-	-	300	mA
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	-	190	mA
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS}$ = 10 V; I <sub>D</sub> = 100 mA; T <sub>j</sub> = 25 °C		-	3	4.5	Ω

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm<sup>2</sup>.





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## 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain		G ( The provide state of the p
			TO-236AB (SOT23)	
				S 017aaa255

## 6. Ordering information

Table 3.       Ordering information							
Type number Package							
	Name	Description	Version				
NX7002AK	TO-236AB	plastic surface-mounted package; 3 leads	SOT23				

## 7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
NX7002AK	%CM

[1] % = placeholder for manufacturing site code

## 8. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>DS</sub>	drain-source voltage	T <sub>amb</sub> = 25 °C		-	60	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>sp</sub> = 25 °C		-	300	mA
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	190	mA
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C	[1]	-	120	mA
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	760	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	265	mW

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Symbol	Parameter	Conditions		Min	Мах	Unit
			[1]	-	325	mW
		T <sub>sp</sub> = 25 °C		-	1330	mW
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode	''				
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	190	mA

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm<sup>2</sup>.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

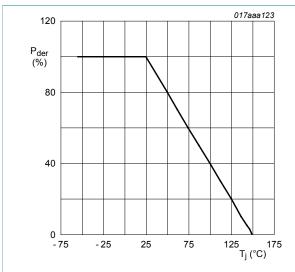


Fig. 1. Normalized total power dissipation as a function of junction temperature

$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$

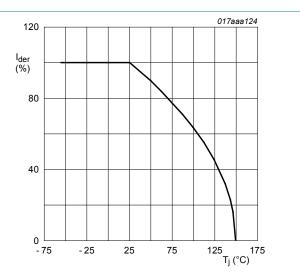
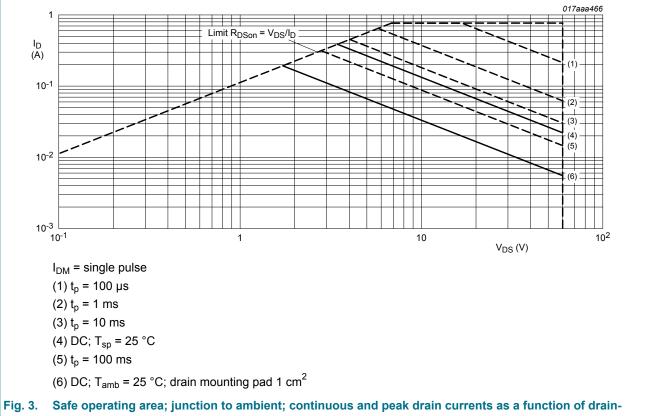


Fig. 2. Normalized continuous drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^\circ\text{C})}} \times 100 \%$$

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source voltage

## 9. Thermal characteristics

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
fr	thermal resistance	in free air	[1]	-	410	470	K/W
	from junction to ambient		[2]	-	330	380	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	-	95	K/W

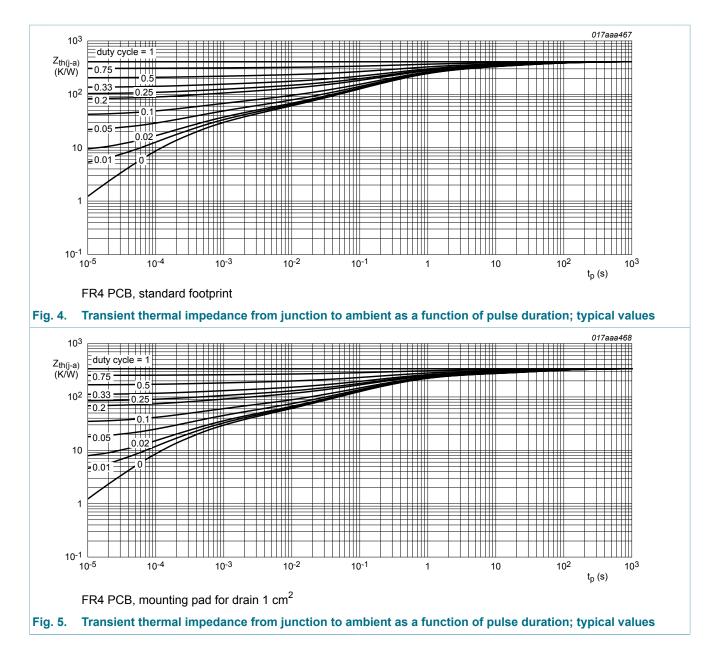
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm<sup>2</sup>.

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## **10. Characteristics**

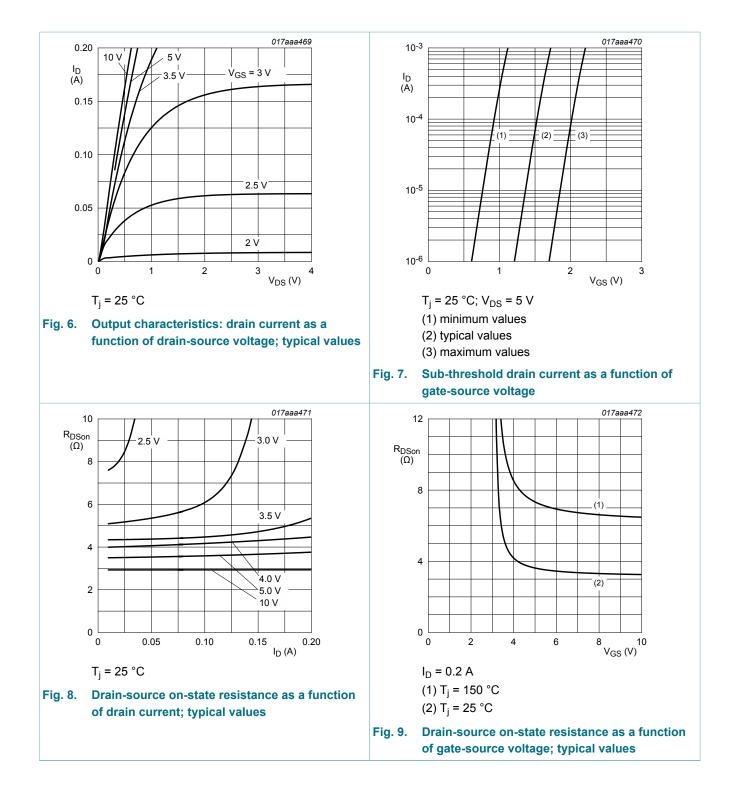
Table 7. C	haracteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Static characteristics							
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = 250 µA; $V_{GS}$ = 0 V; $T_j$ = 25 °C		60	-	-	V
V <sub>GSth</sub>	gate-source threshold voltage	I <sub>D</sub> = 250 μA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C		1.1	1.6	2.1	V
I <sub>DSS</sub>	drain leakage current	$V_{DS}$ = 60 V; $V_{GS}$ = 0 V; $T_j$ = 25 °C		-	-	1	μA
		$V_{DS}$ = 60 V; $V_{GS}$ = 0 V; $T_j$ = 150 °C		-	-	10	μA
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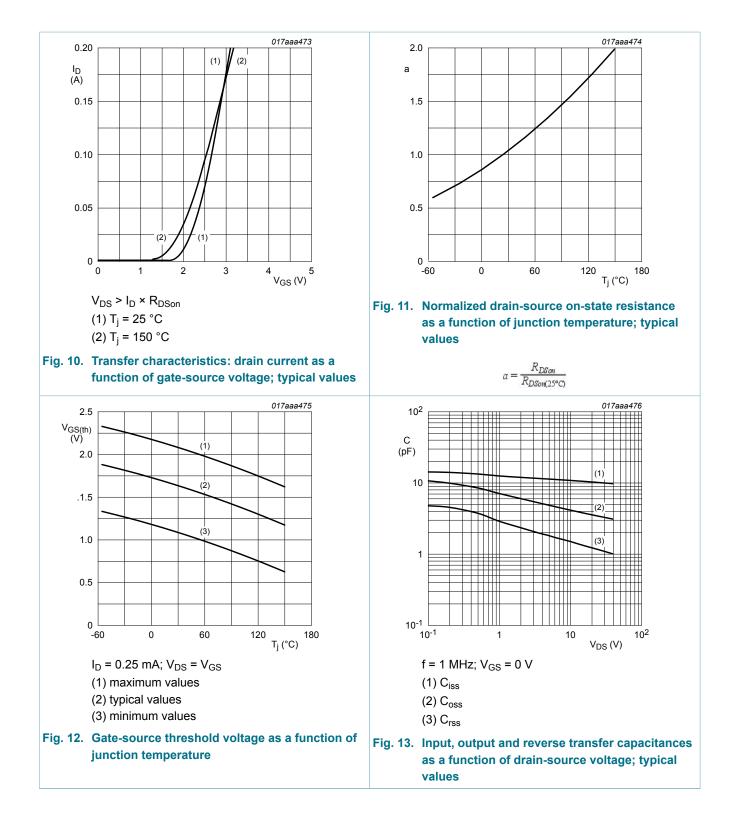
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	2	μA
		$V_{GS}$ = -20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	2	μA
		$V_{GS}$ = 10 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	0.5	μA
		V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	0.5	μA
		$V_{GS}$ = 5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	100	nA
		$V_{GS}$ = -5 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	100	nA
R <sub>DSon</sub>	drain-source on-state	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 100 mA; T <sub>j</sub> = 25 °C	-	3	4.5	Ω
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 100 mA; T <sub>j</sub> = 150 °C	-	6.2	9.2	Ω
		$V_{GS}$ = 5 V; I <sub>D</sub> = 100 mA; T <sub>j</sub> = 25 °C	-	3.7	5.2	Ω
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 200 mA; T <sub>j</sub> = 25 °C	-	230	-	mS
Dynamic cl	haracteristics	· · ·	I			
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = 30 V; I <sub>D</sub> = 200 mA; V <sub>GS</sub> = 4.5 V;	-	0.33	0.43	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	0.12	-	nC
Q <sub>GD</sub>	gate-drain charge		-	0.09	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 10 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	11	17	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	3.4	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	1.4	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = 40 V; R <sub>L</sub> = 250 Ω; V <sub>GS</sub> = 10 V;	-	6	12	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	7	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	20	40	ns
t <sub>f</sub>	fall time		-	14	-	ns
Source-dra	in diode					
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 115 mA; V <sub>GS</sub> = 0 V; T <sub>i</sub> = 25 °C	0.47	0.7	1.2	V

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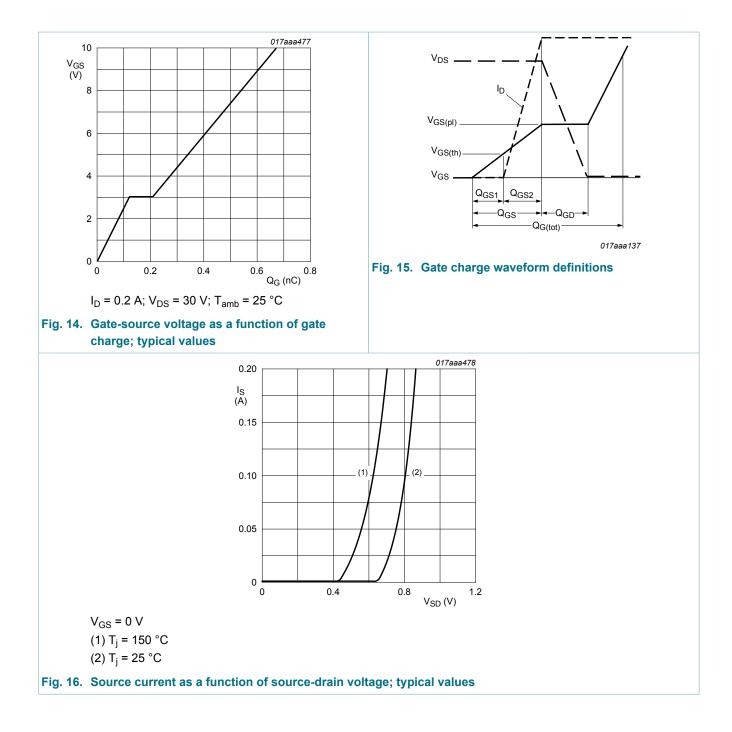


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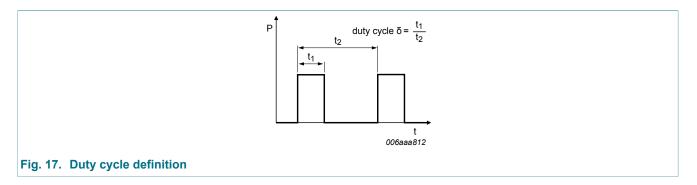
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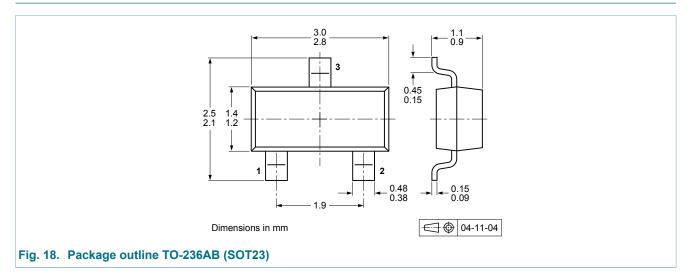


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## **11. Test information**

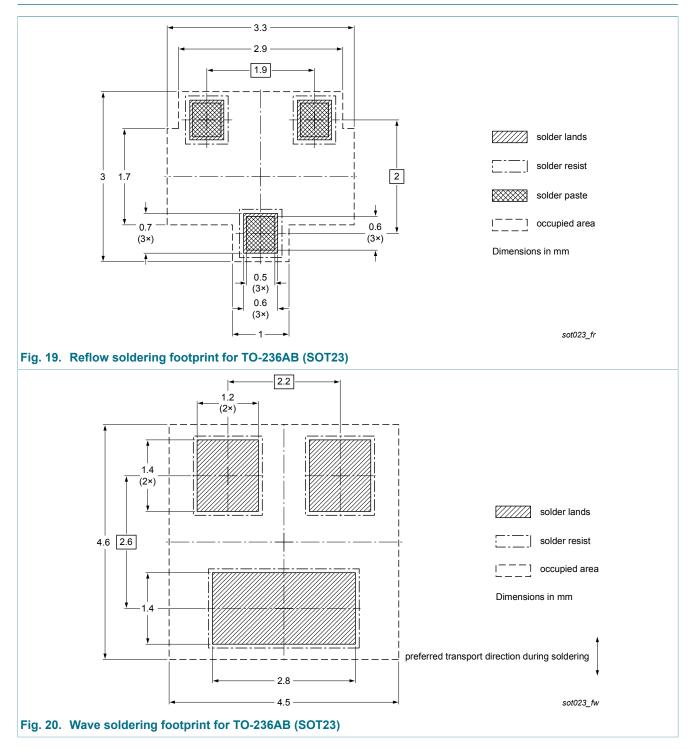


## **12. Package outline**



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## 13. Soldering



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# 14. Revision history

Table 8.Revision	history			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
NX7002AK v.5	20130213	Product data sheet	-	NX7002AK v.4
Modifications:	Limiting values	s: corrected conditions to dra	in current I <sub>D</sub>	
NX7002AK v.4	20121213	Product data sheet	-	NX7002AK v.3
NX7002AK v.3	20120710	Product data sheet	-	NX7002AK v.2
NX7002AK v.2	20120301	Product data sheet	-	NX7002AK v.1
NX7002AK v.1	20120212	Product data sheet	-	-

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### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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[2] The term 'short data sheet' is explained in section "Definitions".

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