# PSMN2R0-60BS



# N-channel 60 V 2.0 mΩ standard level MOSFET in D2PAK Rev. 01 — 17 January 2011 Obiective data

**Objective data sheet** 

#### **Product profile** 1.

## 1.1 General description

Standard level N-channel MOSFET in a D2PAK package qualified to 175 °C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

## 1.2 Features and benefits

- High efficiency due to low switching and conduction losses
- Suitable for standard level gate drive sources

## 1.3 Applications

- DC-to-DC converters
- Load switching

- Motor control
- Server power supplies

#### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}$		-	-	60	V
I <sub>D</sub>	drain current	$T_{mb}$ = 25 °C; $V_{GS}$ = 10 V; see <u>Figure 1</u>	[1]	-	-	120	Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>		-	-	306	W
Tj	junction temperature			-55	-	175	°C
Static chara	acteristics						
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 12}}{\text{ or } 12}$		-	1.7	2	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 100 \text{ °C}; \text{ see } \underline{\text{Figure 13}};$ $\text{see } \underline{\text{Figure 12}}$		-	2.7	3.2	mΩ



Table 1. Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Dynamic ch	naracteristics					
$Q_{GD}$	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 75 \text{ A};$	-	31.6	-	nC
Q <sub>G(tot)</sub>	total gate charge	V <sub>DS</sub> = 30 V; see <u>Figure 14;</u> see <u>Figure 15</u>	-	137	-	nC
Avalanche	ruggedness					
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; $I_D$ = 120 A; $V_{sup}$ ≤ 60 V; $R_{GS}$ = 50 Ω; Unclamped	-	-	913	mJ

<sup>[1]</sup> Continuous current is limited by package.

## 2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain	mb	B
3	S	source		
mb	D	mounting base; connected to drain	ain	
				mbb076 Ś
			⊔ ⊔ 1 3	
			SOT404 (D2PAK)	

# 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PSMN2R0-60BS	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

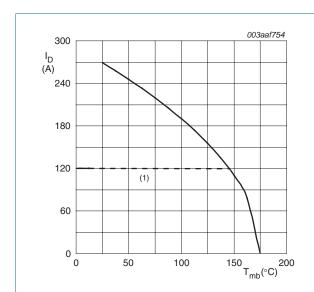
## **Limiting values**

**Limiting values** Table 4.

In accordance with the Absolute Maximum Rating System (IEC 60134).

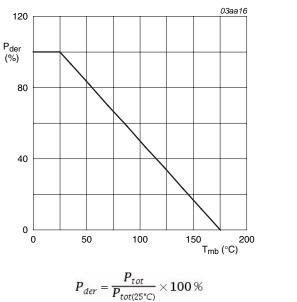
Symbol	Parameter	Conditions		Min	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C		-	60	V
$V_{DGR}$	drain-gate voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$		-	60	V
$V_{GS}$	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 100 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{Model}}$		-	120	Α
		$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{ I}}$	1]	-	120	Α
I <sub>DM</sub>	peak drain current	pulsed; $t_p \le 10 \mu s$ ; $T_{mb} = 25 \text{ °C}$ ; see Figure 3		-	1076	Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>		-	306	W
T <sub>stg</sub>	storage temperature			-55	175	°C
Tj	junction temperature			-55	175	°C
T <sub>sld(M)</sub>	peak soldering temperature			-	260	°C
Source-drain	n diode					
Is	source current	$T_{mb} = 25  ^{\circ}C$	1]	-	120	Α
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \mu s$ ; $T_{mb} = 25 \text{ °C}$		-	1076	Α
Avalanche ru	ıggedness					
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$V_{GS} = 10 \text{ V; } T_{j(init)} = 25 \text{ °C; } I_D = 120 \text{ A; } V_{sup} \le 60 \text{ V; } R_{GS} = 50  \Omega; \text{ Unclamped}$		-	913	mJ

## [1] Continuous current is limited by package.

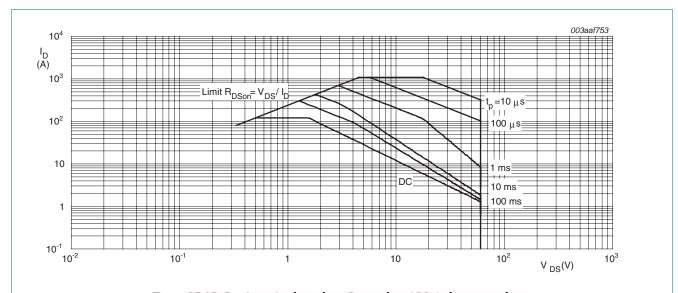


 $V_{GS} \ge 10 \text{ V(1)}$  Capped at 120 A due to package

Continuous drain current as a function of Fig 1. mounting base temperature.



Normalized total power dissipation as a Fig 2. function of mounting base temperature



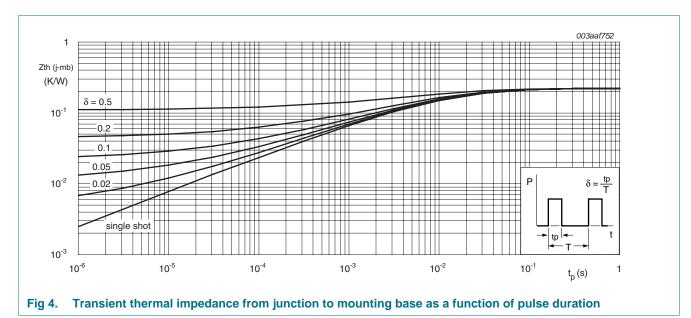
 $T_{mb}$  = 25 °C;  $I_{DM}$  is a single pulse; Capped at 120 A due to package

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

## 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	0.22	0.49	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	Vertical in free air	-	60	-	K/W



## 6. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
$V_{(BR)DSS}$	drain-source breakdown	$I_D = 250 \ \mu A; \ V_{GS} = 0 \ V; \ T_j = -55 \ ^{\circ}C$	54	-	-	V
	voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	60	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1$ mA; $V_{DS} = V_{GS}$ ; $T_j = 25$ °C; see <u>Figure 10</u> ; see <u>Figure 11</u>	2	3	4	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 1$ mA; $V_{DS} = V_{GS}$ ; $T_j = 175$ °C; see Figure 11	1	-	-	V
		$I_D = 1 \text{ mA}$ ; $V_{DS} = V_{GS}$ ; $T_j = -55 \text{ °C}$ ; see Figure 11	-	-	4.6	V
$I_{DSS}$	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	0.03	1	μΑ
		$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 ^{\circ}\text{C}$	-	-	500	μΑ
$I_{GSS}$	gate leakage current	$V_{GS}$ = -20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	100	nA
		$V_{GS}$ = 20 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	100	nΑ
R <sub>DSon</sub> drain-source on-state resistance		$V_{GS}$ = 10 V; $I_D$ = 25 A; $T_j$ = 25 °C; see Figure 12	-	1.7	2	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 175 ^{\circ}\text{C};$ see Figure 13; see Figure 12	-	3.6	4.2	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 100 \text{ °C};$ see Figure 13; see Figure 12	-	2.7	3.2	mΩ
$R_G$	gate resistance	f = 1 MHz	-	0.9	-	Ω
Dynamic cl	naracteristics					
Q <sub>G(tot)</sub> total gate charge	$I_D = 75 \text{ A}$ ; $V_{DS} = 30 \text{ V}$ ; $V_{GS} = 10 \text{ V}$ ; see Figure 14; see Figure 15	-	137	-	nC	
		$I_D = 0 \text{ A}$ ; $V_{DS} = 0 \text{ V}$ ; $V_{GS} = 10 \text{ V}$ ; see Figure 14; see Figure 15	-	129	-	nC
$Q_{GS}$	gate-source charge	$I_D = 75 \text{ A}; V_{DS} = 30 \text{ V}; V_{GS} = 10 \text{ V}$	-	48	-	nC
$Q_{GS(th)}$	pre-threshold gate-source charge	$I_D = 75 \text{ A}$ ; $V_{DS} = 30 \text{ V}$ ; $V_{GS} = 10 \text{ V}$ ; see Figure 14; see Figure 15	-	29.3	-	nC
Q <sub>GS(th-pl)</sub>	post-threshold gate-source charge		-	18.8	-	nC
$Q_{GD}$	gate-drain charge		-	31.6	-	nC
$V_{GS(pl)}$	gate-source plateau voltage	V <sub>DS</sub> = 30 V; see <u>Figure 14;</u> see <u>Figure 15</u>	-	5.7	-	V
C <sub>iss</sub>	input capacitance	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	9997	-	рF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C; see <u>Figure 16</u>	-	1210	-	рF
C <sub>rss</sub>	reverse transfer capacitance		-	594	-	рF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS} = 30 \text{ V}; \text{ R}_{L} = 0.4 \Omega; V_{GS} = 10 \text{ V}; \\ R_{G(ext)} = 4.7 \Omega; I_{D} = 75 \text{ A}$	-	42	-	ns
t <sub>r</sub>	rise time	$V_{DS} = 30 \text{ V}; R_L = 0.4 \Omega; V_{GS} = 10 \text{ V};$	-	56	-	ns
t <sub>d(off)</sub>	turn-off delay time	$R_{G(ext)} = 4.7 \Omega$	-	115	-	ns
t <sub>f</sub>	fall time		-	48.5	-	ns

Table 6. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-dra	in diode					
$V_{SD}$	source-drain voltage	$I_S = 25 \text{ A}$ ; $V_{GS} = 0 \text{ V}$ ; $T_j = 25 \text{ °C}$ ; see Figure 17	-	0.8	1.2	V
t <sub>rr</sub>	reverse recovery time	$I_S = 25 \text{ A}$ ; $dI_S/dt = -100 \text{ A/}\mu\text{s}$ ; $V_{GS} = 0 \text{ V}$ ; $V_{DS} = 30 \text{ V}$	-	57.2	-	ns
Q <sub>r</sub>	recovered charge	$I_S = 25 \text{ A}$ ; $dI_S/dt = -100 \text{ A/}\mu\text{s}$ ; $V_{GS} = 0 \text{ V}$ ; $V_{DS} = 30 \text{ V}$	-	80	-	nC

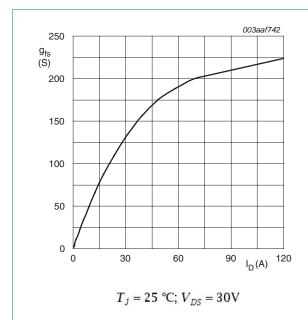


Fig 5. Forward transconductance as a function of drain current; typical values

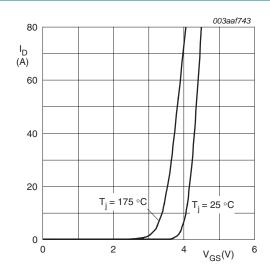


Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values

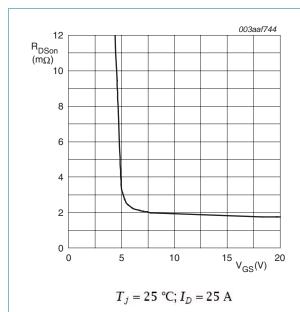


Fig 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

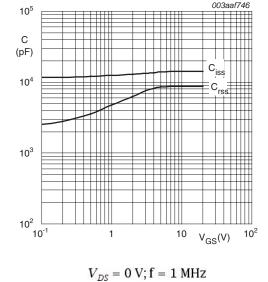


Fig 8. Input and reverse transfer capacitances as a

function of gate-source voltage, typical values

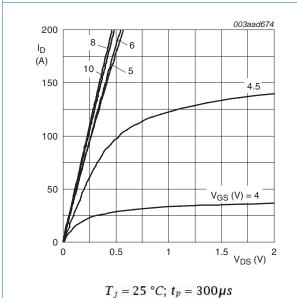
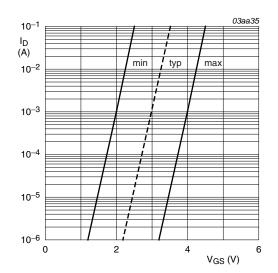


Fig 9. Output characteristics: drain current as a function of drain-source voltage; typical values



 $T_j = 25 \,^{\circ}C; V_{DS} = 5V$ 

Fig 10. Sub-threshold drain current as a function of gate-source voltage

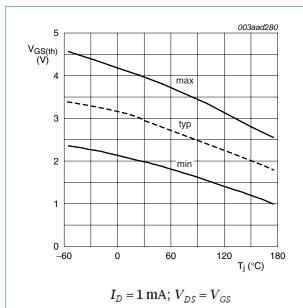


Fig 11. Gate-source threshold voltage as a function of junction temperature

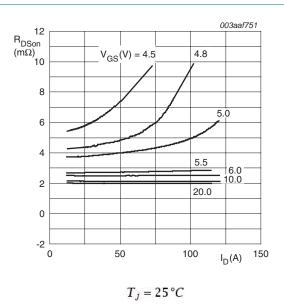


Fig 12. Drain-source on-state resistance as a function of drain current; typical values

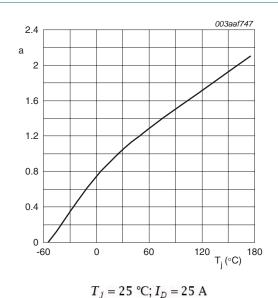


Fig 13. Drain-source on-state resistance as a function of gate-source voltage; typical values

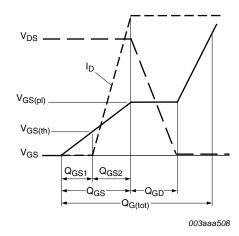


Fig 14. Gate charge waveform definitions

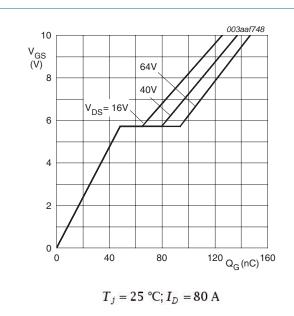
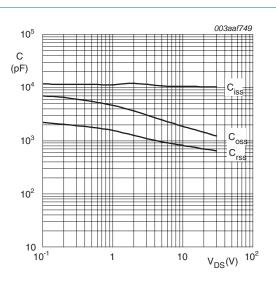


Fig 15. Gate-source voltage as a function of gate charge; typical values



 $V_{GS} = 0 \text{ V; } f = 1 \text{ MHz}$ 

Fig 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

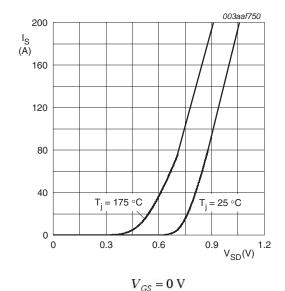


Fig 17. Source (diode forward) current as a function of source-drain (diode forward) voltage; typical values

## 7. Package outline

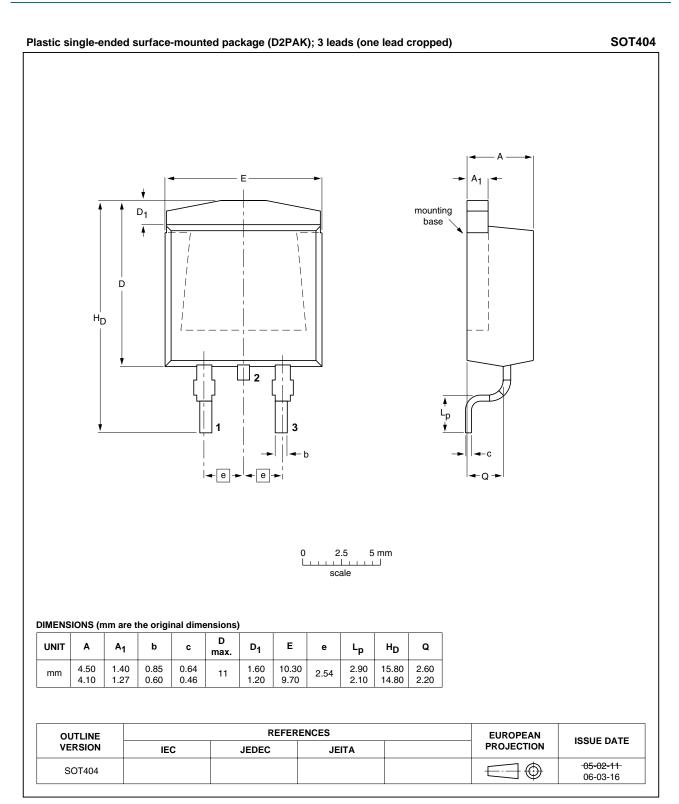


Fig 18. Package outline SOT404 (D2PAK)

NXP Semiconductors PSMN2R0-60BS

## N-channel 60 V 2.0 m $\Omega$ standard level MOSFET in D2PAK

# 8. Revision history

## Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PSMN2R0-60BS v.1	20110117	Objective data sheet	-	-

## 9. Legal information

#### 9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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## PSMN2R0-60BS

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# PSMN2R0-60BS

## **NXP Semiconductors**

## N-channel 60 V 2.0 mΩ standard level MOSFET in D2PAK

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