# **74AUP1G17**

# Low-power Schmitt trigger Rev. 7 — 16 July 2012

**Product data sheet** 

#### **General description** 1.

The 74AUP1G17 provides the single Schmitt trigger buffer. It is capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

This device ensures a very low static and dynamic power consumption across the entire V<sub>CC</sub> range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I<sub>OFF</sub>. The I<sub>OFF</sub> circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

The inputs switch at different points for positive and negative-going signals. The difference between the positive voltage  $V_{T+}$  and the negative voltage  $V_{T-}$  is defined as the input hysteresis voltage V<sub>H</sub>.

#### 2. **Features and benefits**

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - ◆ CDM JESD22-C101E exceeds 1000 V
- Low static power consumption;  $I_{CC} = 0.9 \mu A$  (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



# 3. Ordering information

Table 1. Ordering information

Type number	Package									
	Temperature range	Name	Description	Version						
74AUP1G17GW	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1						
74AUP1G17GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1.45 $\times$ 0.5 mm	SOT886						
74AUP1G17GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1 $\times$ 0.5 mm	SOT891						
74AUP1G17GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115						
74AUP1G17GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 $\times$ 1.0 $\times$ 0.35 mm	SOT1202						
74AUP1G17GX	–40 °C to +125 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body $0.8 \times 0.8 \times 0.35$ mm	SOT1226						

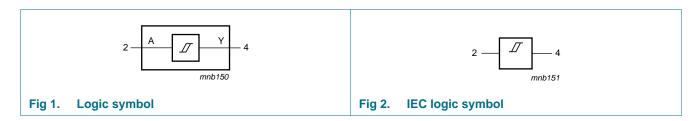
## 4. Marking

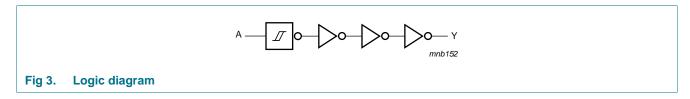
Table 2. Marking

Type number	Marking code <sup>[1]</sup>
74AUP1G17GW	рЈ
74AUP1G17GM	рЈ
74AUP1G17GF	рЈ
74AUP1G17GN	рЈ
74AUP1G17GS	рЈ
74AUP1G17GS	рЈ

<sup>[1]</sup> The pin 1 indicator is located on the lower left corner of the device, below the marking code.

# 5. Functional diagram



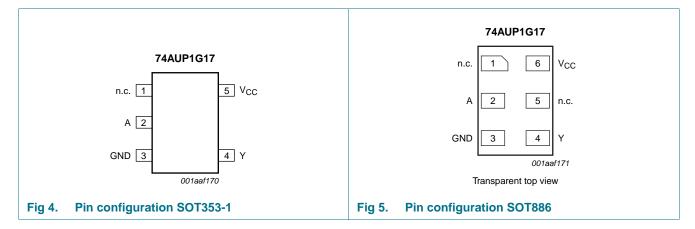


74AUP1G17

All information provided in this document is subject to legal disclaimers.

# 6. Pinning information

#### 6.1 Pinning





### 6.2 Pin description

Table 3. Pin description

Symbol	Pin		Description			
	TSSOP5 and X2SON5	XSON6				
n.c.	1	1	not connected			
A	2	2	data input			
GND	3	3	ground (0 V)			
Υ	4	4	data output			
n.c.	-	5	not connected			
$V_{CC}$	5	6	supply voltage			

## 7. Functional description

Table 4. Function table[1]

Input	Output
A	Υ
L	L
Н	Н

<sup>[1]</sup> H = HIGH voltage level; L = LOW voltage level.

# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
Symbol	Faranietei	Conditions	IAIIII	IVIAX	Oilit
$V_{CC}$	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	$V_I < 0 V$	-50	-	mA
VI	input voltage		<u>[1]</u> –0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode	<u>[1]</u> –0.5	+4.6	V
Io	output current	$V_O = 0 V \text{ to } V_{CC}$	-	±20	mA
I <sub>CC</sub>	supply current		-	+50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb}$ = -40 °C to +125 °C	[2] _	250	mW

<sup>[1]</sup> The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

# 9. Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{CC}$	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	$V_{CC}$	V
		Power-down mode; V <sub>CC</sub> = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C

<sup>[2]</sup> For TSSOP5 packages: above 87.5 °C the value of P<sub>tot</sub> derates linearly with 4.0 mW/K.
For XSON6 and X2SON5 packages: above 118 °C the value of P<sub>tot</sub> derates linearly with 7.8 mW/K.

## 10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	5 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O} = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> - 0.1	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.75 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.11	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.32	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	2.05	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.9	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.72	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.6	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_O = 20 \mu A$ ; $V_{CC} = 0.8 \text{ V}$ to 3.6 V	-	-	0.1	V
		$I_{O} = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.3 \times V_{\text{CC}}$	V
		$I_{O} = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.31	V
		$I_{O} = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.31	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.31	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.44	V
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.44	V
l <sub>l</sub>	input leakage current	$V_I$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.2	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	$V_1 \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.5	μΑ
Δl <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
Cı	input capacitance	$V_I = GND \text{ or } V_{CC}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	1.1	-	pF
Co	output capacitance	$V_O = GND; V_{CC} = 0 V$	-	1.7	-	рF
T <sub>amb</sub> = -	40 °C to +85 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O} = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	$V_{CC}-0.1$	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.7 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	1.03	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.30	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.97	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.85	-	-	V
		$I_O = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.67	-	-	V
		$I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.55	-	-	V
4AUP1G17		All information provided in this document is subject to legal disclaimers.			© NXP B.V. 2012. All rig	ghts reserv

 Table 7.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{OL}$	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_O$ = 20 $\mu$ A; $V_{CC}$ = 0.8 $V$ to 3.6 $V$	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.3 \times V_{CC}$	V
		$I_{O} = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.37	V
		$I_{O} = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.35	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.33	V
		$I_{O} = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.45	V
		$I_{O} = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.33	V
		$I_{O} = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.45	V
l <sub>l</sub>	input leakage current	$V_I$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μΑ
l <sub>OFF</sub>	power-off leakage current	$V_I$ or $V_O = 0$ V to 3.6 V; $V_{CC} = 0$ V	-	-	±0.5	μΑ
Δl <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_O = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μΑ
lcc	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.9	μΑ
Δl <sub>CC</sub>	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	50	μΑ
T <sub>amb</sub> = -	40 °C to +125 °C					
V <sub>OH</sub>	HIGH-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_{O} = -20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	V <sub>CC</sub> – 0.11	-	-	V
		$I_{O} = -1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	$0.6 \times V_{CC}$	-	-	V
		$I_{O} = -1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	0.93	-	-	V
		$I_{O} = -1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	1.17	-	-	V
		$I_{O} = -2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.77	-	-	V
		$I_{O} = -3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	1.67	-	-	V
		$I_{O} = -2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{T+}$ or $V_{T-}$				
		$I_O = 20 \mu A$ ; $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.11	V
		$I_{O} = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.33 \times V_{CC}$	V
		$I_{O} = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.41	V
		$I_{O} = 1.9 \text{ mA}; V_{CC} = 1.65 \text{ V}$	-	-	0.39	V
		$I_{O} = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.36	V
		$I_O = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.50	V
		$I_O = 2.7 \text{ mA}$ ; $V_{CC} = 3.0 \text{ V}$	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
l <sub>l</sub>	input leakage current	$V_1 = GND \text{ to } 3.6 \text{ V}; V_{CC} = 0 \text{ V to } 3.6 \text{ V}$	-	-	±0.75	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V			±0.75	μA

 Table 7.
 Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$\Delta I_{OFF}$	additional power-off leakage current	$V_1$ or $V_0 = 0$ V to 3.6 V; $V_{CC} = 0$ V to 0.2 V	-	-	±0.75	μΑ
I <sub>CC</sub>	supply current	$V_I$ = GND or $V_{CC}$ ; $I_O$ = 0 A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	1.4	μΑ
$\Delta I_{CC}$	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; I_O = 0 \text{ A};$ $V_{CC} = 3.3 \text{ V}$	-	-	75	μΑ

# 11. Dynamic characteristics

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions			25 °C		-40 °C to +125 °C			Unit
				Min	Typ[1]	Max	Min	Max (85 °C)	Max (125 °C)	
$C_L = 5 p$	F		'			'		'	'	
t <sub>pd</sub>	propagation delay	A to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 \text{ V}$		-	19.0	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.6	5.7	10.6	2.5	10.9	11.1	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		2.4	4.2	6.5	2.3	7.1	7.4	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		2.0	3.6	5.5	1.9	6.1	6.3	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		1.9	3.0	4.2	1.8	4.6	4.8	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		1.8	2.7	3.6	1.5	3.8	4.0	ns
C <sub>L</sub> = 10	pF									
t <sub>pd</sub>	propagation delay	A to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 \text{ V}$		-	22.5	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		2.9	6.6	12.4	2.7	12.9	13.0	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		2.6	4.8	7.8	2.4	8.3	8.7	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		2.5	4.2	6.3	2.4	6.8	7.1	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		2.3	3.5	4.8	2.1	5.3	5.6	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		2.1	3.3	4.4	2.0	4.6	4.8	ns
C <sub>L</sub> = 15	pF									
t <sub>pd</sub>	propagation delay	A to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 \text{ V}$		-	26.0	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.2	7.4	14.1	3.1	14.7	14.9	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		3.1	5.4	8.7	2.8	9.5	9.9	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		2.7	4.7	7.1	2.7	7.8	8.2	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		2.6	4.0	5.6	2.5	6.0	6.3	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		2.5	3.7	4.9	2.2	5.2	5.5	ns

 Table 8.
 Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions			25 °C		-4	0 °C to +1	25 °C	Unit
				Min	Typ[1]	Max	Min	Max (85 °C)	Max (125 °C)	
$C_{L} = 30$	pF		'		•	'				•
t <sub>pd</sub>	propagation delay	A to Y; see Figure 8	[2]							
		$V_{CC} = 0.8 \text{ V}$		-	36.3	-	-	-	-	ns
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		3.9	9.7	19.0	3.7	19.8	20.1	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		3.5	7.0	11.2	3.6	12.4	13.0	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		3.5	6.0	9.2	3.4	10.1	10.7	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		3.4	5.1	7.0	3.2	7.5	7.9	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		3.3	4.8	6.2	3.1	7.1	7.5	ns
$C_L = 5 p$	F, 10 pF, 15 pF and	30 pF								
C <sub>PD</sub>	power dissipation	$f = 1 \text{ MHz}$ ; $V_I = \text{GND to } V_{CC}$	[3]							
	capacitance	$V_{CC} = 0.8 \text{ V}$		-	2.5	-	-	-	-	pF
		$V_{CC} = 1.1 \text{ V to } 1.3 \text{ V}$		-	2.7	-	-	-	-	pF
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$		-	2.8	-	-	-	-	pF
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$		-	3.0	-	-	-	-	pF
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$		-	3.5	-	-	-	-	pF
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$		-	4.0	-	-	-	-	pF

<sup>[1]</sup> All typical values are measured at nominal V<sub>CC</sub>.

$$P_D = C_{PD} \times V_{CC}{}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}{}^2 \times f_o) \text{ where:}$$

f<sub>i</sub> = input frequency in MHz;

 $f_o = output frequency in MHz;$ 

C<sub>L</sub> = output load capacitance in pF;

V<sub>CC</sub> = supply voltage in V;

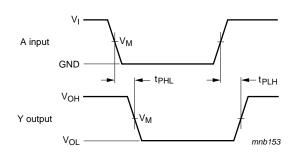
N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_o)$  = sum of the outputs.

<sup>[2]</sup>  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ 

<sup>[3]</sup>  $C_{PD}$  is used to determine the dynamic power dissipation ( $P_D$  in  $\mu W$ ).

#### 12. Waveforms



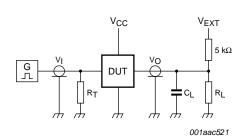
Measurement points are given in Table 9.

Logic levels:  $V_{OL}$  and  $V_{OH}$  are typical output voltage levels that occur with the output load.

Fig 8. The data input (A) to output (Y) propagation delays

Table 9. Measurement points

Supply voltage	Output	Input						
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>	VI	$t_r = t_f$				
0.8 V to 3.6 V	$0.5 \times V_{CC}$	$0.5 \times V_{CC}$	V <sub>CC</sub>	≤ 3.0 ns				



Test data is given in Table 10.

Definitions for test circuit:

 $R_L$  = Load resistance.

 $C_L$  = Load capacitance including jig and probe capacitance.

 $R_T$  = Termination resistance should be equal to the output impedance  $Z_0$  of the pulse generator.

 $V_{EXT}$  = External voltage for measuring switching times.

Fig 9. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Load		V <sub>EXT</sub>				
V <sub>CC</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>		
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k $\Omega$ or 1 M $\Omega$	open	GND	$2\times V_{CC}$		

<sup>[1]</sup> For measuring enable and disable times,  $R_L = 5 \text{ k}\Omega$ , for measuring propagation delays, setup and hold times and pulse width  $R_L = 1 \text{ M}\Omega$ .

74AUP1G17

## 13. Transfer characteristics

Table 11. Transfer characteristics

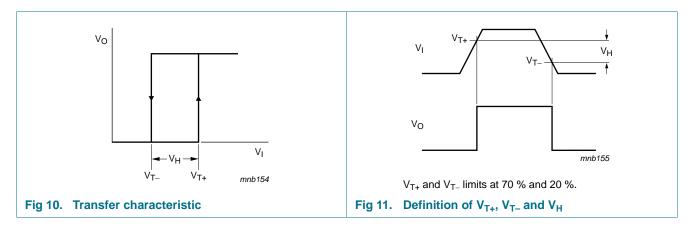
Voltages are referenced to GND (ground = 0 V).

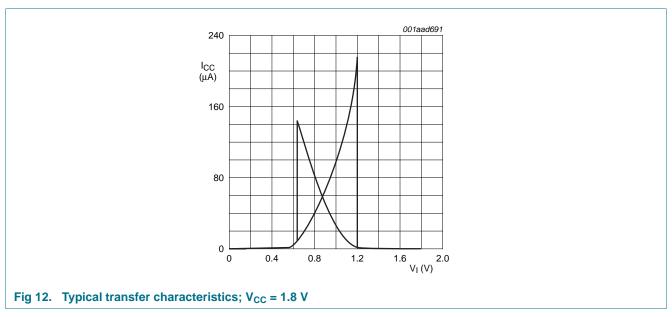
positive-going	Firms 40 15' 44				
	Figure 40 151 44				
threshold voltage	see Figure 10 and Figure 11				
threshold voltage	V <sub>CC</sub> = 0.8 V	0.30	-	0.60	V
	V <sub>CC</sub> = 1.1 V	0.53	-	0.90	V
	V <sub>CC</sub> = 1.4 V	0.74	-	1.11	V
	V <sub>CC</sub> = 1.65 V	0.91	-	1.29	V
	V <sub>CC</sub> = 2.3 V	1.37	-	1.77	V
	V <sub>CC</sub> = 3.0 V	1.88	-	2.29	V
negative-going	see Figure 10 and Figure 11				
threshold voltage	$V_{CC} = 0.8 \text{ V}$	0.10	-	0.60	V
	V <sub>CC</sub> = 1.1 V	0.26	-	0.65	V
	V <sub>CC</sub> = 1.4 V	0.39	-	0.75	V
	V <sub>CC</sub> = 1.65 V	0.47	-	0.84	V
	V <sub>CC</sub> = 2.3 V	0.69	-	1.04	V
	V <sub>CC</sub> = 3.0 V	0.88	-	1.24	V
hysteresis voltage	see <u>Figure 10</u> , <u>Figure 11</u> , <u>Figure 12</u> and <u>Figure 13</u>				
	V <sub>CC</sub> = 0.8 V	0.07	-	0.50	V
	V <sub>CC</sub> = 1.1 V	0.08	-	0.46	V
	V <sub>CC</sub> = 1.4 V	0.18	-	0.56	V
	V <sub>CC</sub> = 1.65 V	0.27	-	0.66	V
	V <sub>CC</sub> = 2.3 V	0.53	-	0.92	V
	V <sub>CC</sub> = 3.0 V	0.79	-	1.31	V
°C to +85 °C					
positive-going	see Figure 10 and Figure 11				
threshold voltage	V <sub>CC</sub> = 0.8 V	0.30	-	0.60	V
	V <sub>CC</sub> = 1.1 V	0.53	-	0.90	V
	V <sub>CC</sub> = 1.4 V	0.74	-	1.11	V
	V <sub>CC</sub> = 1.65 V	0.91	-	1.29	V
	V <sub>CC</sub> = 2.3 V	1.37	-	1.77	V
	V <sub>CC</sub> = 3.0 V	1.88	-	2.29	V
negative-going	see Figure 10 and Figure 11				
threshold voltage	V <sub>CC</sub> = 0.8 V	0.10	-	0.60	V
	V <sub>CC</sub> = 1.1 V	0.26	-	0.65	V
	V <sub>CC</sub> = 1.4 V	0.39	-	0.75	V
	V <sub>CC</sub> = 1.65 V	0.47	-	0.84	V
	V <sub>CC</sub> = 2.3 V	0.69	-	1.04	V
	V <sub>CC</sub> = 3.0 V	0.88	-	1.24	V
	hysteresis voltage  **C to +85 **C  positive-going threshold voltage  negative-going	$V_{CC} = 1.65 \text{ V}$ $V_{CC} = 2.3 \text{ V}$ $V_{CC} = 3.0 \text{ V}$ see Figure 10 and Figure 11 $V_{CC} = 1.4 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 3.0 \text{ V}$ hysteresis voltage $V_{CC} = 3.0 \text{ V}$ see Figure 10, Figure 11, Figure 12 and Figure 13 $V_{CC} = 0.8 \text{ V}$ $V_{CC} = 1.4 \text{ V}$ $V_{CC} = 1.4 \text{ V}$ $V_{CC} = 1.4 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 2.3 \text{ V}$ $V_{CC} = 3.0 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 3.0 \text{ V}$ $V_{CC} = 1.4 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 1.4 \text{ V}$ $V_{CC} = 1.65 \text{ V}$ $V_{CC} = 2.3 \text{ V}$	$ \begin{array}{c} V_{CC} = 1.65 \ V & 0.91 \\ V_{CC} = 2.3 \ V & 1.37 \\ V_{CC} = 3.0 \ V & 1.88 \\ \hline \\ negative-going \\ threshold voltage \\ \hline \\ & V_{CC} = 3.0 \ V & 0.10 \\ \hline \\ & V_{CC} = 0.8 \ V & 0.10 \\ \hline & V_{CC} = 1.1 \ V & 0.26 \\ \hline & V_{CC} = 1.65 \ V & 0.47 \\ \hline & V_{CC} = 2.3 \ V & 0.69 \\ \hline & V_{CC} = 2.3 \ V & 0.69 \\ \hline & V_{CC} = 3.0 \ V & 0.88 \\ \hline \\ & hysteresis voltage \\ \hline \\ & hysteresis voltage \\ \hline \\ & V_{CC} = 0.8 \ V & 0.07 \\ \hline & V_{CC} = 0.8 \ V & 0.07 \\ \hline & V_{CC} = 1.65 \ V & 0.27 \\ \hline & V_{CC} = 1.65 \ V & 0.27 \\ \hline & V_{CC} = 1.65 \ V & 0.27 \\ \hline & V_{CC} = 1.65 \ V & 0.27 \\ \hline & V_{CC} = 2.3 \ V & 0.53 \\ \hline & V_{CC} = 3.0 \ V & 0.30 \\ \hline & V_{CC} = 1.4 \ V & 0.53 \\ \hline & V_{CC} = 1.4 \ V & 0.74 \\ \hline & V_{CC} = 1.65 \ V & 0.91 \\ \hline & V_{CC} = 2.3 \ V & 0.53 \\ \hline & V_{CC} = 2.3 \ V & 0.53 \\ \hline & V_{CC} = 1.65 \ V & 0.91 \\ \hline & V_{CC} = 2.3 \ V & 0.10 \\ \hline & V_{CC} = 1.4 \ V & 0.26 \\ \hline & V_{CC} = 1.4 \ V & 0.26 \\ \hline & V_{CC} = 1.4 \ V & 0.39 \\ \hline & V_{CC} = 1.4 \ V & 0.39 \\ \hline & V_{CC} = 1.65 \ V & 0.47 \\ \hline & V_{CC} = 1.65 \ V & 0.47 \\ \hline & V_{CC} = 1.65 \ V & 0.47 \\ \hline & V_{CC} = 2.3 \ V & 0.69 \\ \hline \end{array}$	$\begin{array}{c} V_{CC} = 1.65 \ V & 0.91 & - \\ V_{CC} = 2.3 \ V & 1.37 & - \\ V_{CC} = 3.0 \ V & 1.88 & - \\ \\ \hline \\ Negative-going threshold voltage & \begin{array}{c} see \ Figure \ 10 \ and \ Figure \ 11 \\ \hline \\ V_{CC} = 0.8 \ V & 0.10 & - \\ \hline \\ V_{CC} = 1.1 \ V & 0.26 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.47 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.47 & - \\ \hline \\ V_{CC} = 2.3 \ V & 0.69 & - \\ \hline \\ V_{CC} = 2.3 \ V & 0.69 & - \\ \hline \\ V_{CC} = 3.0 \ V & 0.88 & - \\ \hline \\ \\ Nysteresis voltage & \begin{array}{c} see \ Figure \ 10 \ Figure \ 11, \\ Figure \ 12 \ and \ Figure \ 13 \\ \hline \\ V_{CC} = 0.8 \ V & 0.07 & - \\ \hline \\ V_{CC} = 1.1 \ V & 0.08 & - \\ \hline \\ V_{CC} = 1.4 \ V & 0.18 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.27 & - \\ \hline \\ V_{CC} = 2.3 \ V & 0.53 & - \\ \hline \\ V_{CC} = 3.0 \ V & 0.79 & - \\ \hline \end{array} \\ \begin{array}{c} \\ \text{°C to +85 °C} \\ \hline \\ positive-going threshold voltage & \begin{array}{c} see \ Figure \ 10 \ and \ Figure \ 11 \\ \hline \\ V_{CC} = 0.8 \ V & 0.30 & - \\ \hline \\ V_{CC} = 1.4 \ V & 0.74 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.91 & - \\ \hline \\ V_{CC} = 2.3 \ V & 0.10 & - \\ \hline \\ V_{CC} = 3.0 \ V & 0.10 & - \\ \hline \\ V_{CC} = 0.8 \ V & 0.10 & - \\ \hline \\ V_{CC} = 1.4 \ V & 0.26 & - \\ \hline \\ V_{CC} = 1.4 \ V & 0.39 & - \\ \hline \\ V_{CC} = 1.4 \ V & 0.39 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.47 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.47 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.47 & - \\ \hline \\ V_{CC} = 1.65 \ V & 0.47 & - \\ \hline \end{array}$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

**Table 11.** Transfer characteristics ...continued Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{H}$	hysteresis voltage	see <u>Figure 10</u> , <u>Figure 11</u> , <u>Figure 12</u> and <u>Figure 13</u>				
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	V
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	V
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	V
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	V
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	V
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	V
$T_{amb} = -40$	°C to +125 °C					
$V_{T+}$	positive-going	see Figure 10 and Figure 11				
	threshold voltage	V <sub>CC</sub> = 0.8 V	0.30	-	0.62	V
		V <sub>CC</sub> = 1.1 V	0.53	-	0.92	V
		V <sub>CC</sub> = 1.4 V	0.74	-	1.13	V
		V <sub>CC</sub> = 1.65 V	0.91	-	1.31	V
		V <sub>CC</sub> = 2.3 V	1.37	-	1.80	V
		V <sub>CC</sub> = 3.0 V	1.88	-	2.32	V
$V_{T-}$	negative-going	see Figure 10 and Figure 11				
	threshold voltage	V <sub>CC</sub> = 0.8 V	0.10	-	0.60	V
		V <sub>CC</sub> = 1.1 V	0.26	-	0.65	V
		V <sub>CC</sub> = 1.4 V	0.39	-	0.75	V
		V <sub>CC</sub> = 1.65 V	0.47	-	0.84	V
		V <sub>CC</sub> = 2.3 V	0.69	-	1.04	V
		V <sub>CC</sub> = 3.0 V	0.88	-	1.24	V
$V_{H}$	hysteresis voltage	see <u>Figure 10</u> , <u>Figure 11</u> , <u>Figure 12</u> and <u>Figure 13</u>				
		V <sub>CC</sub> = 0.8 V	0.07	-	0.50	V
		V <sub>CC</sub> = 1.1 V	0.08	-	0.46	V
		V <sub>CC</sub> = 1.4 V	0.18	-	0.56	V
		V <sub>CC</sub> = 1.65 V	0.27	-	0.66	V
		V <sub>CC</sub> = 2.3 V	0.53	-	0.92	V
		V <sub>CC</sub> = 3.0 V	0.79	-	1.31	V

## 14. Waveforms transfer characteristics





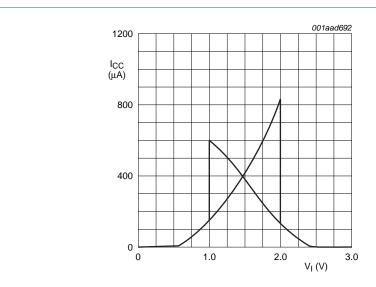


Fig 13. Typical transfer characteristics;  $V_{CC} = 3.0 \text{ V}$ 

# 15. Application information

The slow input rise and fall times cause additional power dissipation, this can be calculated using the following formula:

 $P_{ad} = f_i \times (t_r \times I_{CC(AV)} + t_f \times I_{CC(AV)}) \times V_{CC}$  where:

 $P_{ad}$  = additional power dissipation ( $\mu W$ );

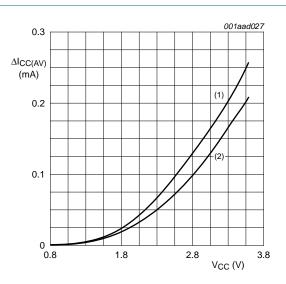
 $f_i = input frequency (MHz);$ 

 $t_r$  = input rise time (ns); 10 % to 90 %;

 $t_f$  = input fall time (ns); 90 % to 10 %;

 $I_{CC(AV)}$  = average additional supply current ( $\mu$ A).

Average I<sub>CC</sub> differs with positive or negative input transitions, as shown in Figure 14.



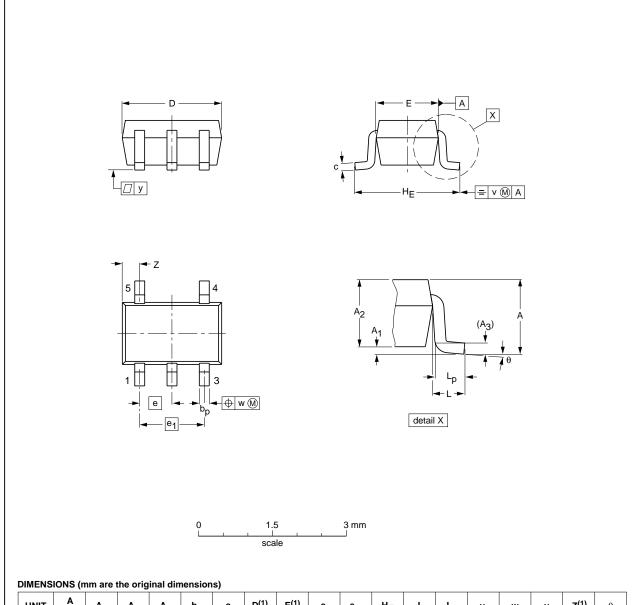
- (1) Positive-going edge.
- (2) Negative-going edge. Linear change of  $V_I$  between 0.8 V and 2.0 V. All values given are typical, unless otherwise specified.

Fig 14. Average  $I_{\text{CC}}$  as a function of  $V_{\text{CC}}$ 

# 16. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1



UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	bp	С	D <sup>(1)</sup>	E <sup>(1)</sup>	е	e <sub>1</sub>	HE	L	Lp	v	w	у	Z <sup>(1)</sup>	θ
mm	1.1	0.1 0	1.0 0.8	0.15	0.30 0.15	0.25 0.08	2.25 1.85	1.35 1.15	0.65	1.3	2.25 2.0	0.425	0.46 0.21	0.3	0.1	0.1	0.60 0.15	7° 0°

#### Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT353-1		MO-203	SC-88A			<del>-00-09-01</del> 03-02-19

Fig 15. Package outline SOT353-1 (TSSOP5)

74AUP1G17

All information provided in this document is subject to legal disclaimers.

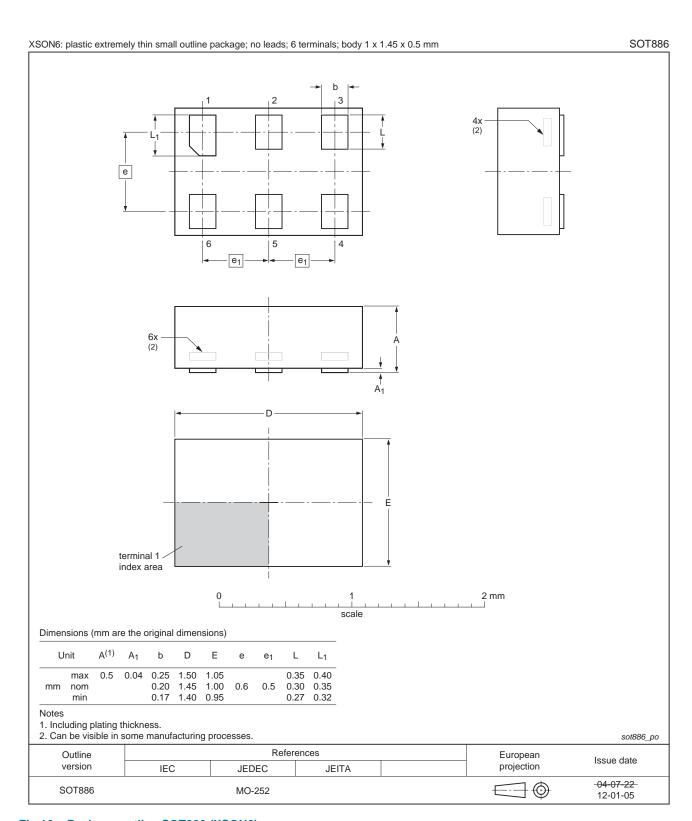


Fig 16. Package outline SOT886 (XSON6)

74AUP1G17 All information provided in this document is subject to legal disclaimers.

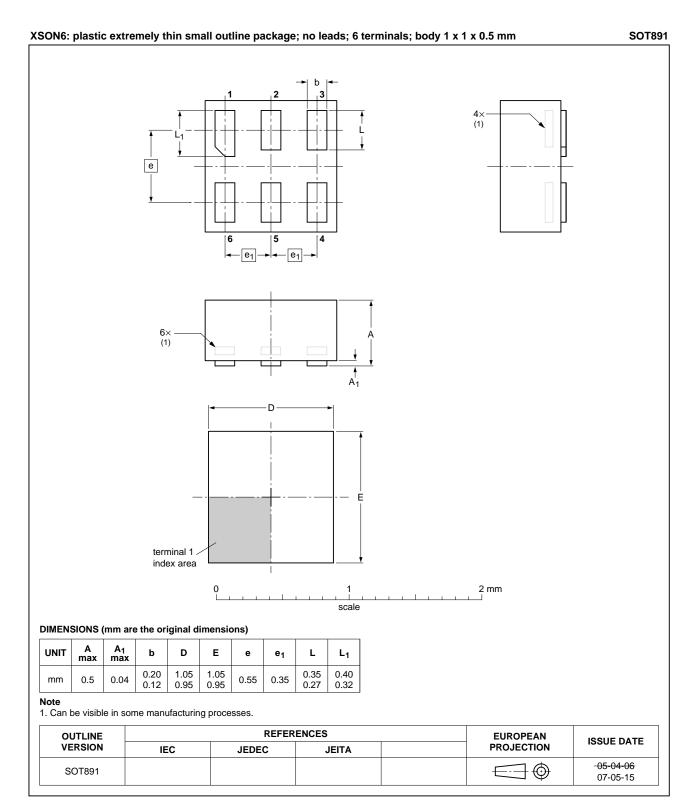


Fig 17. Package outline SOT891 (XSON6)

All information provided in this document is subject to legal disclaimers.

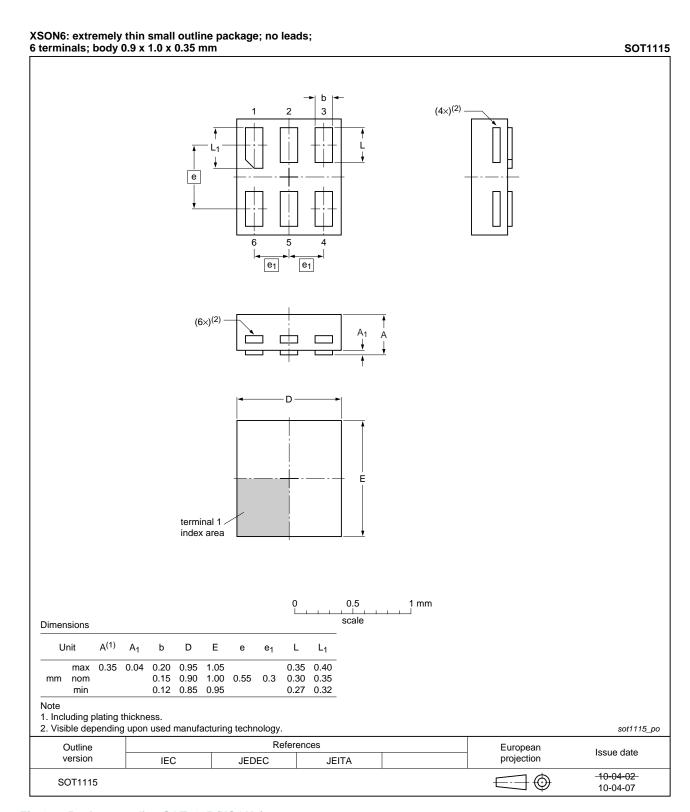


Fig 18. Package outline SOT1115 (XSON6)

74AUP1G17 All information provided in this document is subject to legal disclaimers.

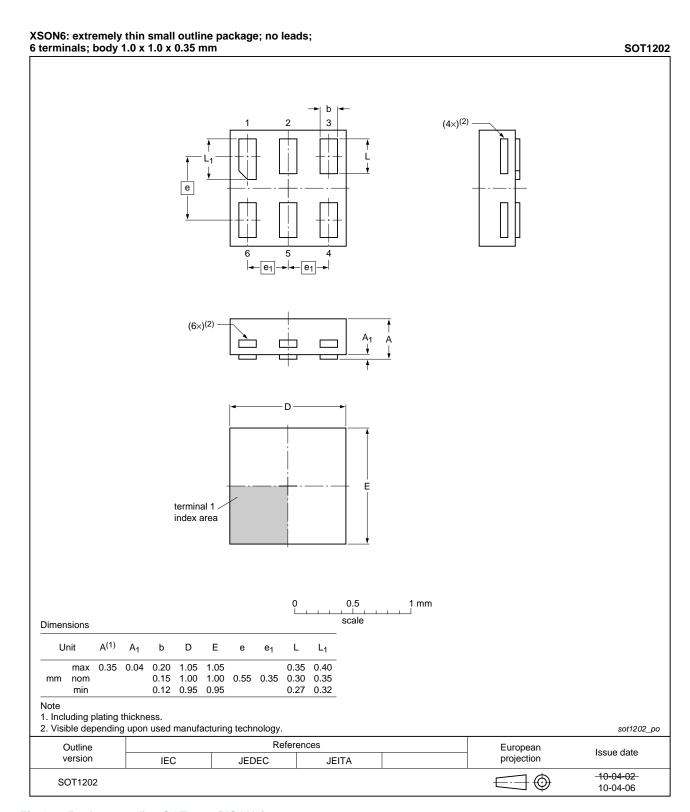


Fig 19. Package outline SOT1202 (XSON6)

74AUP1G17 All information provided in this document is subject to legal disclaimers.

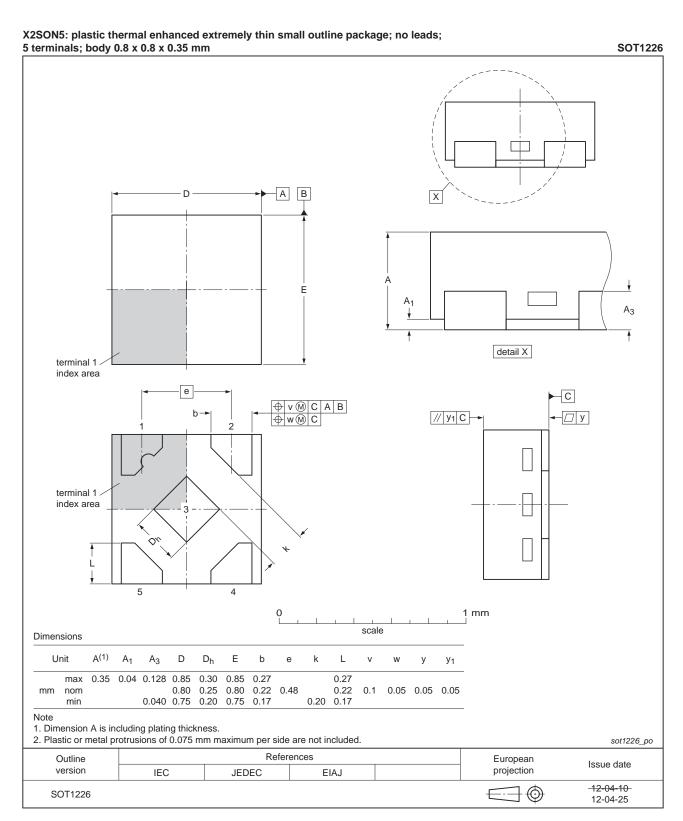


Fig 20. Package outline SOT1226 (X2SON5)

74AUP1G17 All information provided in this document is subject to legal disclaimers.

## 17. Abbreviations

#### Table 12. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model

# 18. Revision history

#### Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1G17 v.7	20120716	Product data sheet	-	74AUP1G17 v.6
Modifications:	<ul> <li>Package outline</li> </ul>	ine drawing of SOT1226 ( <mark>Fig</mark> u	ure 20) modified.	
74AUP1G17 v.6	20120412	Product data sheet	-	74AUP1G17 v.5
Modifications:	<ul> <li>Added type n</li> </ul>	umber 74AUP1G17GX (SOT	1226)	
	<ul> <li>Package outl</li> </ul>	ine drawing of SOT886 ( <u>Figur</u>	re 16) modified.	
74AUP1G17 v.5	20111124	Product data sheet	-	74AUP1G17 v.4
Modifications:	<ul> <li>Legal pages</li> </ul>	updated.		
74AUP1G17 v.4	20100715	Product data sheet	-	74AUP1G17 v.3
74AUP1G17 v.3	20090710	Product data sheet	-	74AUP1G17 v.2
74AUP1G17 v.2	20060727	Product data sheet	-	74AUP1G17 v.1
74AUP1G17 v.1	20050726	Product data sheet	-	-

### 19. Legal information

#### 19.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

#### 19.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### 19.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

74AUP1G17

All information provided in this document is subject to legal disclaimers.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

#### 19.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

#### 20. Contact information

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

# **74AUP1G17**

#### **Low-power Schmitt trigger**

## 21. Contents

1	General description
2	Features and benefits
3	Ordering information
4	Marking
5	Functional diagram 2
6	Pinning information
6.1	Pinning
6.2	Pin description
7	Functional description 4
8	Limiting values 4
9	Recommended operating conditions 4
10	Static characteristics 5
11	Dynamic characteristics
12	Waveforms
13	Transfer characteristics 10
14	Waveforms transfer characteristics 12
15	Application information
16	Package outline
17	Abbreviations
18	Revision history
19	Legal information
19.1	Data sheet status
19.2	Definitions
19.3	Disclaimers
19.4	Trademarks
20	Contact information
21	Contents 24

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.