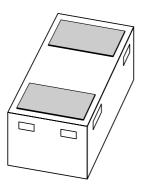
DISCRETE SEMICONDUCTORS

DATA SHEET



BAT54LSchottky barrier diode

Product data sheet 2003 Jun 23



Schottky barrier diode

BAT54L

FEATURES

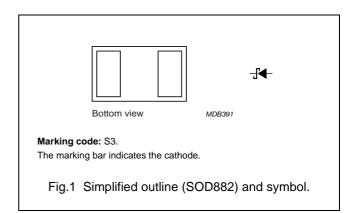
- Low forward voltage
- Leadless ultra small plastic package (1 mm × 0.6 mm × 0.5 mm)
- Boardspace 1.17 mm² (approx. 10% of SOT23)
- Power dissipation comparable to SOT23.

APPLICATIONS

- · Ultra high-speed switching
- · Voltage clamping
- · Protection circuits
- Mobile communication, digital (still) cameras, PDAs and PCMCIA cards.

DESCRIPTION

Planar Schottky barrier diode encapsulated in a SOD882 leadless ultra small plastic package.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_R	continuous reverse voltage		_	30	V
I _F	continuous forward current		_	200	mA
I _{FRM}	repetitive peak forward current	$t_p \le 1s; \ \delta \le 0.5$	_	300	mA
I _{FSM}	non-repetitive peak forward current	t _p < 10 ms	_	600	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
T _i	junction temperature		_	150	°C

Note

1. Refer to SOD882 standard mounting conditions (footprint), FR4 printed-circuit board with 60 μm copper strip line.

Schottky barrier diode

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ELECTRICAL CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V _F	forward voltage	see Fig.2		
		I _F = 0.1 mA	240	mV
		I _F = 1 mA	320	mV
		I _F = 10 mA	400	mV
		I _F = 30 mA	500	mV
		I _F = 100 mA	800	mV
I_R	continuous reverse current	V _R = 25 V; see Fig.3; note 1	2	μΑ
C_d	diode capacitance	V _R = 1 V; f = 1 MHz; see Fig.4	10	pF

Note

1. Pulse test: t_p = 300 μ s; δ = 0.02.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Refer to SOD882 standard mounting conditions (footprint), FR4 printed-circuit board with 60 μ m copper strip line.

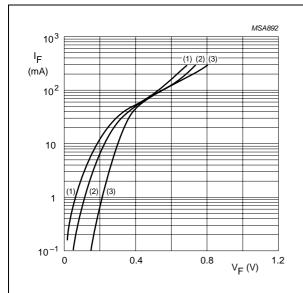
Soldering

Reflow soldering is the only recommended soldering method.

Schottky barrier diode

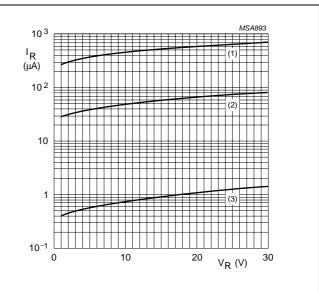
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GRAPHICAL DATA



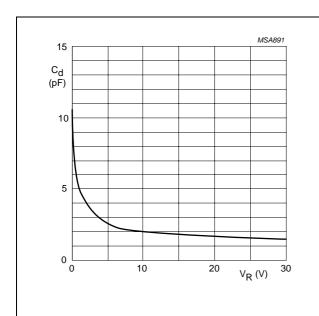
- (1) $T_{amb} = 125 \, ^{\circ}C$.
- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.

Fig.2 Forward current as a function of forward voltage; typical values.



- (1) $T_{amb} = 125 \, ^{\circ}C$.
- (2) $T_{amb} = 85 \, ^{\circ}C$.
- (3) $T_{amb} = 25 \, ^{\circ}C$.

Fig.3 Reverse current as a function of reverse voltage; typical values.



 $f = 1 \text{ MHz}; T_{amb} = 25 \,^{\circ}\text{C}.$

Fig.4 Diode capacitance as a function of reverse voltage; typical values.

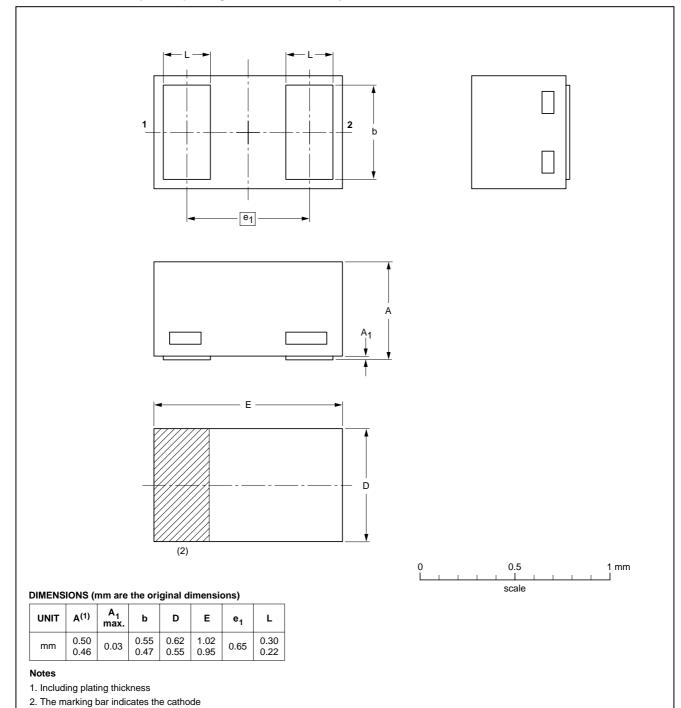
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PACKAGE OUTLINE

Leadless ultra small plastic package; 2 terminals; body 1.0 x 0.6 x 0.5 mm

SOD882



REFERENCES

JEITA

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JEDEC

EUROPEAN

PROJECTION

ISSUE DATE

03-04-16 03-04-17

2003 Jun 23

IEC

OUTLINE

VERSION

SOD882

Schottky barrier diode

BAT54L

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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