## **BUK763R1-40B**



# N-channel TrenchMOS standard level FET Rev. 03 — 8 February 2011

**Product data sheet** 

## **Product profile**

## 1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

## 1.2 Features and benefits

- AEC Q101 compliant
- Low conduction losses due to low on-state resistance
- Suitable for standard level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

## 1.3 Applications

- 12 V loads
- Automotive systems

- General purpose power switching
- Motors, lamps and solenoids

## 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C		-	-	40	V
I <sub>D</sub>	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C};$ see <u>Figure 3</u> ; see <u>Figure 1</u>	[1]	-	-	75	Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>		-	-	300	W
Static characteristics							
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 11}}{\text{Figure 12}};$ see $\frac{\text{Figure 12}}{\text{Figure 12}}$		-	2.6	3.1	mΩ



 Table 1.
 Quick reference data ...continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Avalanche	ruggedness					
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$I_D$ = 75 A; $V_{sup} \le 40$ V; $R_{GS}$ = 50 $\Omega$ ; $V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; unclamped	-	-	1.6	J
Dynamic ch	naracteristics					
$Q_{GD}$	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $V_{DS} = 32 \text{ V}; T_j = 25 ^{\circ}\text{C};$ see Figure 13	-	29	-	nC

<sup>[1]</sup> Continuous current is limited by package.

## 2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	D	drain[1]	mb	D
3	S	source		g (EA)
mb	D	mounting base; connected to drain		mbb076 S
			SOT404 (D2PAK)	

<sup>[1]</sup> It is not possible to make a connection to pin 2.

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BUK763R1-40B	D2PAK	plastic single-ended surface-mounted package (D2PAK); 3 leads (one lead cropped)	SOT404

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

		,			
Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C	-	40	V
$V_{DGR}$	drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	40	V
$V_{GS}$	gate-source voltage		-20	20	V
I <sub>D</sub>	drain current	$T_{mb} = 100  ^{\circ}\text{C};  V_{GS} = 10  \text{V};  \text{see}  \frac{\text{Figure 1}}{}$	<u>[1]</u> _	75	Α
		$T_{mb} = 25  ^{\circ}\text{C};  V_{GS} = 10  \text{V};  \text{see}  \frac{\text{Figure 3}}{\text{Sign}};$	[2] _	225	Α
		see Figure 1	<u>[1]</u> _	75	Α
I <sub>DM</sub>	peak drain current	$T_{mb}$ = 25 °C; pulsed; $t_p \le 10 \mu s$ ; see Figure 3	-	902	Α
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> = 25 °C; see <u>Figure 2</u>	-	300	W
T <sub>stg</sub>	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
Source-drai	n diode				
Is	source current	T <sub>mb</sub> = 25 °C	[2] _	225	Α
			[1] -	75	Α
I <sub>SM</sub>	peak source current	pulsed; $t_p \le 10 \ \mu s$ ; $T_{mb} = 25 \ ^{\circ}C$	-	902	Α
Avalanche r	ruggedness				
E <sub>DS(AL)S</sub>	non-repetitive drain-source avalanche energy	$I_D$ = 75 A; $V_{sup} \le 40$ V; $R_{GS}$ = 50 $\Omega$ ; $V_{GS}$ = 10 V; $T_{j(init)}$ = 25 °C; unclamped	-	1.6	J

- [1] Continuous current is limited by package.
- [2] Current is limited by power dissipation chip rating.

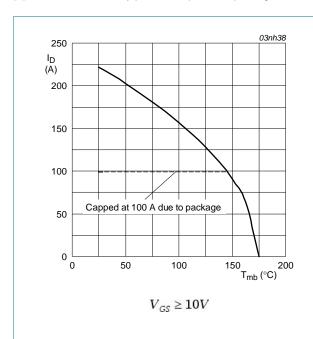


Fig 1. Continuous drain current as a function of mounting base temperature

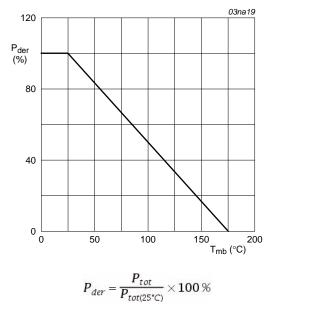
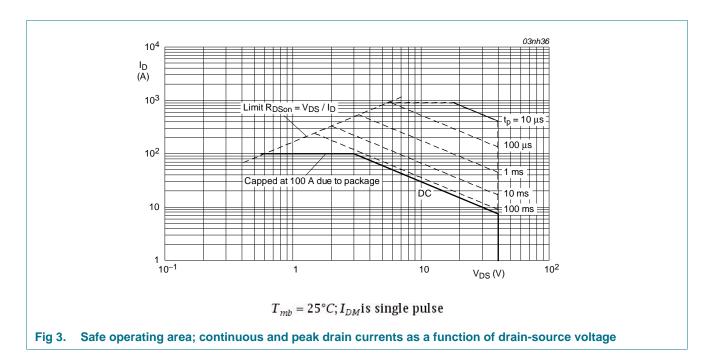


Fig 2. Normalized total power dissipation as a function of mounting base temperature

BUK763R1-40B

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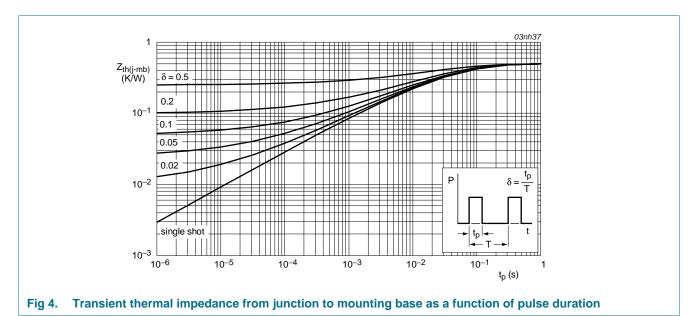
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## 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 4	-	-	0.5	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	minimum footprint; mounted on a printed-board circuit	-	50	-	K/W



## 6. Characteristics

Table 6. Characteristics

Table 0.	-					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
	nracteristics					
$V_{(BR)DSS}$	drain-source	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	40	-	-	V
	breakdown voltage	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ °C}$	36	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}$ ; $V_{DS} = V_{GS}$ ; $T_j = 175 \text{ °C}$ ; see <u>Figure 10</u>	1	-	-	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$ ; $T_j = 25$ °C; see <u>Figure 10</u>	2	3	4	V
		$I_D = 1$ mA; $V_{DS} = V_{GS}$ ; $T_j = -55$ °C; see <u>Figure 10</u>	-	-	4.4	V
I <sub>DSS</sub>	drain leakage current	$V_{DS} = 40 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	0.02	1	μA
		V <sub>DS</sub> = 40 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C	-	-	500	μΑ
I <sub>GSS</sub>	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C}$	-	2	100	nA
		$V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 175 °C;$ see <u>Figure 11</u> ; see <u>Figure 12</u>	-	-	5.9	mΩ
		$V_{GS}$ = 10 V; $I_D$ = 25 A; $T_j$ = 25 °C; see <u>Figure 11</u> ; see <u>Figure 12</u>	-	2.6	3.1	mΩ
Dynamic	characteristics					
Q <sub>G(tot)</sub>	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 32 \text{ V}; V_{GS} = 10 \text{ V};$	-	94	-	nC
$Q_{GS}$	gate-source charge	T <sub>j</sub> = 25 °C; see <u>Figure 13</u>	-	18	-	nC
$Q_{GD}$	gate-drain charge		-	29	-	nC
C <sub>iss</sub>	input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$	-	5106	6808	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C; see <u>Figure 14</u>	-	1390	1667	pF
C <sub>rss</sub>	reverse transfer capacitance		-	530	722	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$	-	38	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 10 \Omega$ ; $T_j = 25 °C$	-	82	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	141	-	ns
t <sub>f</sub>	fall time		-	90	-	ns
L <sub>D</sub>	internal drain inductance	from upper edge of drain mounting base to center of die; T <sub>j</sub> = 25 °C	-	2.5	-	nΗ
		from drain lead 6 mm from package to center of die; T <sub>j</sub> = 25 °C	-	4.5	-	nΗ
L <sub>S</sub>	internal source inductance	from source lead to source bond pad; $T_j = 25 ^{\circ}\text{C}$	-	7.5	-	nΗ
Source-d	rain diode					
$V_{SD}$	source-drain voltage	$I_S = 40 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 15</u>	-	0.85	1.2	V
t <sub>rr</sub>	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s};$	-	65	-	ns
Q <sub>r</sub>	recovered charge	$V_{GS} = -10 \text{ V}; V_{DS} = 20 \text{ V}; T_j = 25 \text{ °C}$	-	103	-	nC

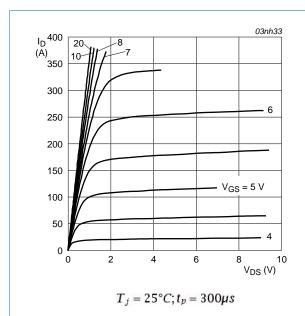


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values

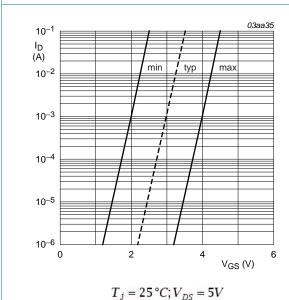


Fig 7. Sub-threshold drain current as a function of gate-source voltage

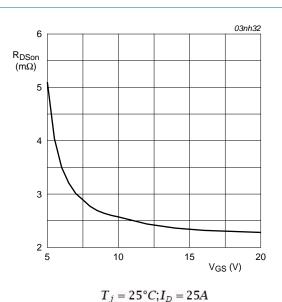


Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

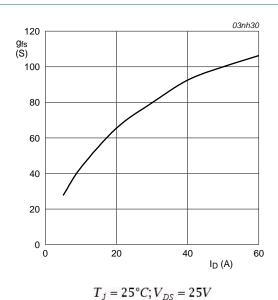


Fig 8. Forward transconductance as a function of drain current; typical values

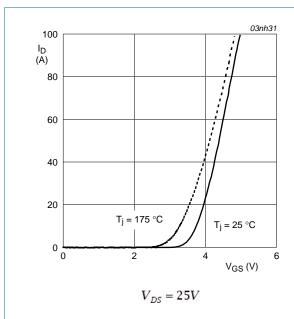


Fig 9. Transfer characteristics: drain current as a function of gate-source voltage; typical values

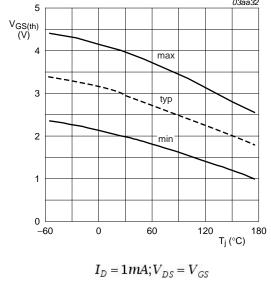


Fig 10. Gate-source threshold voltage as a function of junction temperature

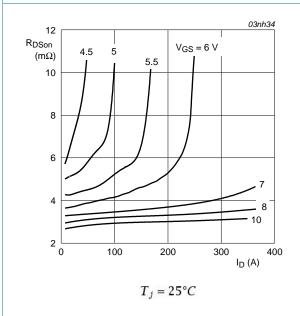


Fig 11. Drain-source on-state resistance as a function of drain current; typical values

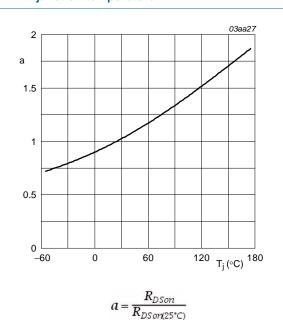


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature

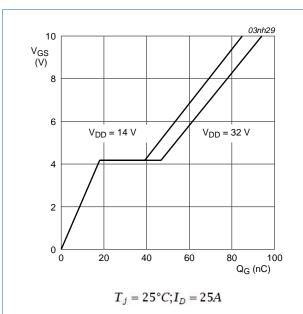
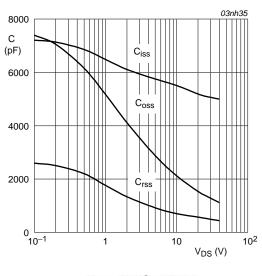


Fig 13. Gate-source voltage as a function of turn-on gate charge; typical values



 $V_{GS} = 0V; f = 1MHz$ 

Fig 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

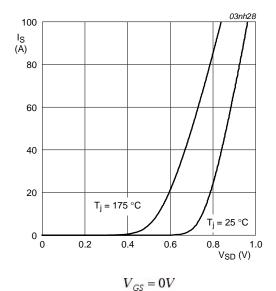


Fig 15. Reverse diode current as a function of reverse diode voltage; typical values

## 7. Package outline

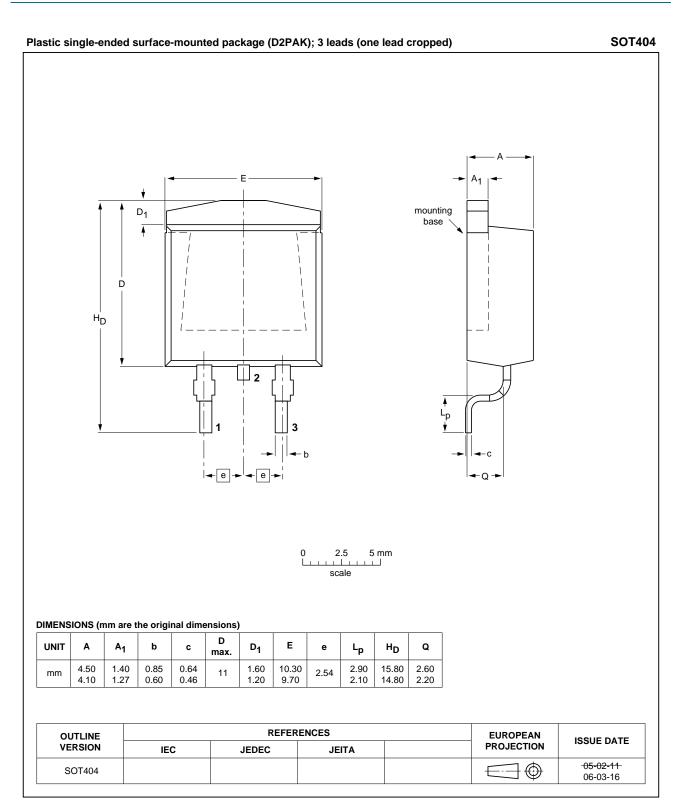


Fig 16. Package outline SOT404 (D2PAK)

## 8. Revision history

## Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BUK763R1-40B v.3	20110208	Product data sheet	-	BUK75_763R1_40B v.2
Modifications:	<ul> <li>The format of this of NXP Semicon</li> </ul>		esigned to comply with	the new identity guidelines
	<ul> <li>Legal texts have</li> </ul>	been adapted to the new	company name where	appropriate.
	<ul> <li>Type number BU</li> </ul>	K763R1-40B separated f	rom data sheet BUK75	_763R1_40B v.2.
BUK75_763R1_40B v.2	20021016	Product data	-	-

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#### 9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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