5LN01SP

ON Semiconductor®

http://onsemi.com

N-Channel Small Signal MOSFET 50V, 0.1A, 7.8Ω, Single SPA

Features

- · Low ON-resistance
- · Ultrahigh-speed switching
- 1.5V drive

Specifications

Absolute Maximum Ratings at Ta=25°C

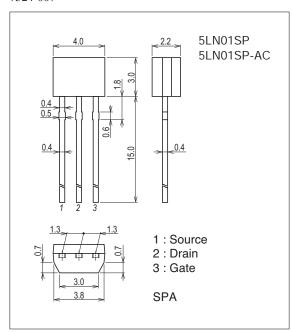
Parameter	Symbol	Conditions	Ratings	Unit
Drain to Source Voltage	VDSS		50	V
Gate to Source Voltage	V _{GSS}		±10	V
Drain Current (DC)	ID		0.1	А
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	0.4	Α
Allowable Power Dissipation	PD		0.25	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

This product is designed to "ESD immunity < 200V", so please take care when handling.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ) 7524-007



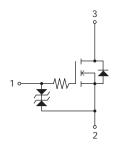
Ordering & Package Information

Device	Package	Shipping	memo	
5LN01SP	SPA SC-72	500pcs./bag	- Pb-Free	
5LN01SP-AC	SPA SC-72	2,500pcs./box		

Marking



Electrical Connection



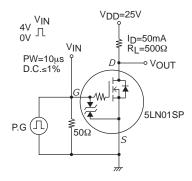
^{*} Machine Model

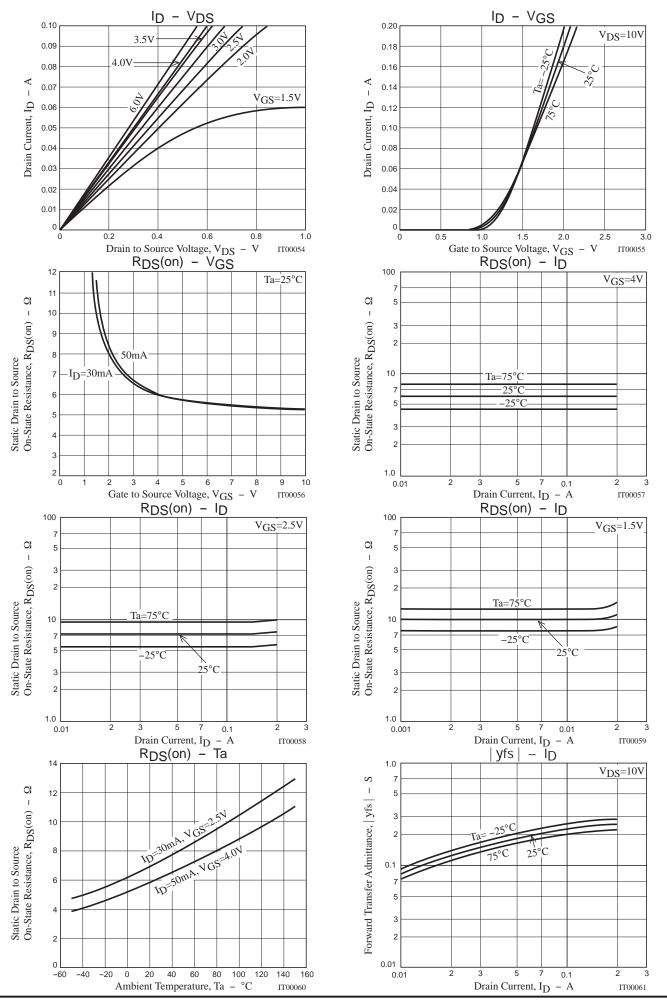
5LN01SP

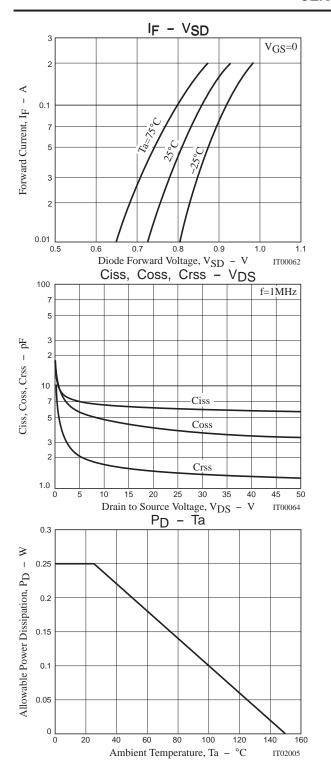
Electrical Characteristics at Ta=25°C

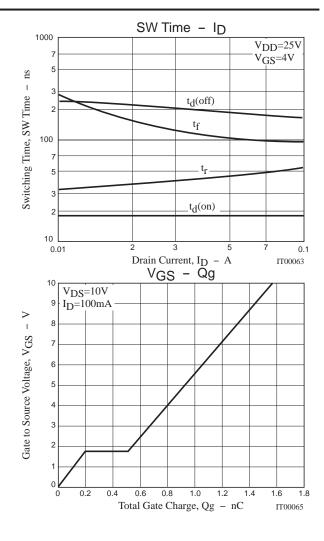
Parameter	Symbol	Conditions	Ratings			Unit
Parameter		Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0V	50			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =50V, V _{GS} =0V			10	μΑ
Gate to Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0V			±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{DS} =10V, I _D =100μA	0.4		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =50mA	0.13	0.18		S
	R _{DS} (on)1	ID=50mA, VGS=4V		6	7.8	Ω
Static Drain to Source On-State Resistance	R _{DS} (on)2	I _D =30mA, V _G S=2.5V		7.1	9.9	Ω
	R _{DS} (on)3	I _D =10mA, V _{GS} =1.5V		10	20	Ω
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		6.6		pF
Output Capacitance	Coss			4.7		pF
Reverse Transfer Capacitance	Crss			1.7		pF
Turn-ON Delay Time	t _d (on)	6 15 17 10 1		18		ns
Rise Time	t _r			42		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		190		ns
Fall Time	tf			105		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =100mA		1.57		nC
Gate to Source Charge	Qgs			0.20		nC
Gate to Drain "Miller" Charge	Qgd			0.32		nC
Diode Forward Voltage	V _{SD}	I _S =100mA, V _{GS} =0V		0.85	1.2	V

Switching Time Test Circuit



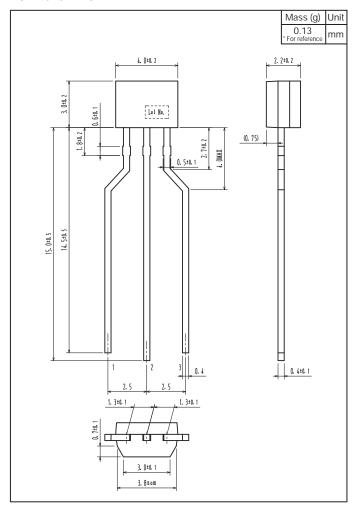






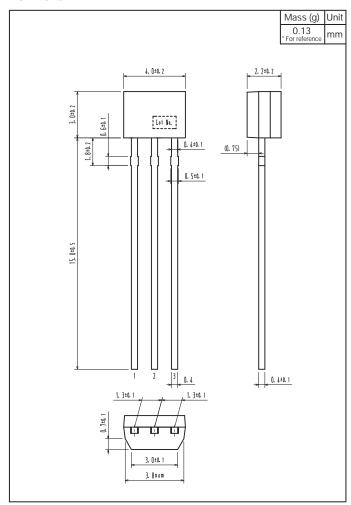
Outline Drawing

5LN01SP-AC



Outline Drawing

5LN01SP



Note on usage: Since the 5LN01SP is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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