TOSHIBA Field Effect Transistor Silicon P Channel MOS Type ($L^2-\pi$ -MOSV)

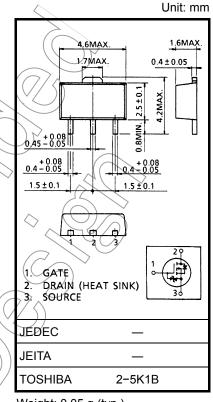
2SJ511

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance : $R_{DS (ON)} = 0.32 \Omega$ (typ.)
- High forward transfer admittance : |Y_{fs}| = 1.4 S (typ.)
- Low leakage current : $I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -30 \ V)$
- Enhancement mode : $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_D = -1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	-30	$\langle \psi \rangle$
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	-30	V
Gate-source voltage		V _{GSS}	±20	v
Drain current	DC (Note 1)	I _D	-2	∼ A
Drain current	Pulse (Note 1)	I _{DP}	-6	А
Drain power dissipation	1	PD <	0.5	W
Drain power dissipation (Note 2)		PD	1.5	< <w)<="" td=""></w>
Single pulse avalanche energy (Note 3)		EAS	55	mJ
Avalanche current		TAR	-2 <	A
Repetitive avalanche e	nergy (Note 4)	EAR	0.05	Lm
Channel temperature	((Tch	150	୍କି
Storage temperature ra	inge	Tstg	-55~150	∽°C



Weight: 0.05 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch-a)}	250	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

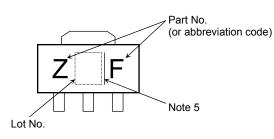
Note 2: Mounted on a ceramic substrate (25.4 mm × 25.4 mm × 0.8 mm)

Note 3: $V_{DD} = -25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 10 mH, $R_G = 25 \Omega$, $I_{AR} = -2 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Marking



Note 5: A line to the right of a Lot No. identifies the indication of product Labels. Without a line: [[Pb]]/INCLUDES > MCV With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

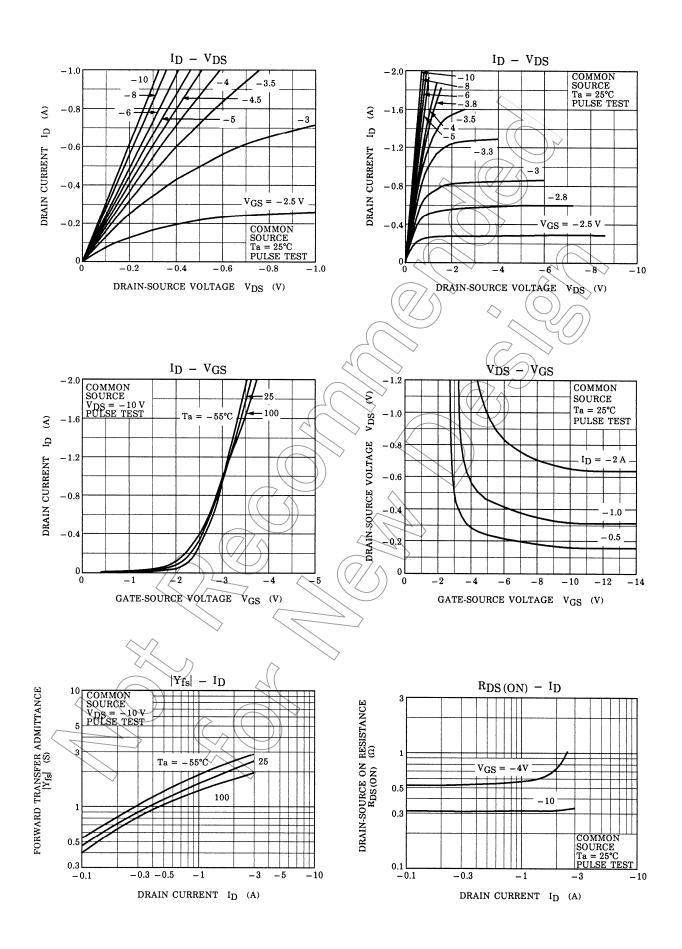
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	- [\sum	±10	μA
Drain cut-off cu	rrent	I _{DSS}	$V_{DS} = -30 V, V_{GS} = 0 V$	-(($) \rightarrow ($	-100	μA
Drain-source br voltage	reakdown	V _(BR) DSS	$I_{\rm D}$ = -10 mA, V _{GS} = 0 V	-30	Y) _	V
Gate threshold v	voltage	V _{th}	$V_{DS} = -10 V, I_D = -1 mA$	-0.8	~ _	-2.0	V
Drain-source ON resistance		6	$V_{GS} = -4 V, I_D = -1 A$	$\overline{\mathcal{T}}$	0.55	0.76	Ω
		R _{DS (ON)}	$V_{GS} = -10 V, I_{D} = -1 A$	-	0.32	0.45	
Forward transfe	r admittance	Y _{fs}	V _D s(=-10, V, I _D = -1 A	0.7	1.4		S
Input capacitance	ce	C _{iss}		-	160	_	
Reverse transfe	r capacitance	C _{rss}	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	30	_	pF
Output capacita	nce	Coss		_	85	_	
Switching time	Rise time	t	$V_{\text{GS}_{0V}}^{-10V}$		30	_	
	Turn-on time	ton	$V_{\text{GS}} = V_{\text{OUT}}$ $R_{\text{L}} = 15\Omega$	_	45	_	
	Fall time	tf		_	30	_	ns
	Turn-off time	t _{off}	$V_{DD} = -15V$ Duty $\leq 1\%$, $t_w = 10\mu s$		120	_	
Total gate char plus gate-drain)	rge (Gate-source	Qg	\sim	_	5.5	_	
Gate-source ch	arge	Qgs	V _{DD} ≈ −24 V, V _{GS} = −10 V, I _D = −2 A	_	4.3	_	nC
Gate-drain ("mi	ller") charge	Qgd	\triangleright	_	1.2	_	

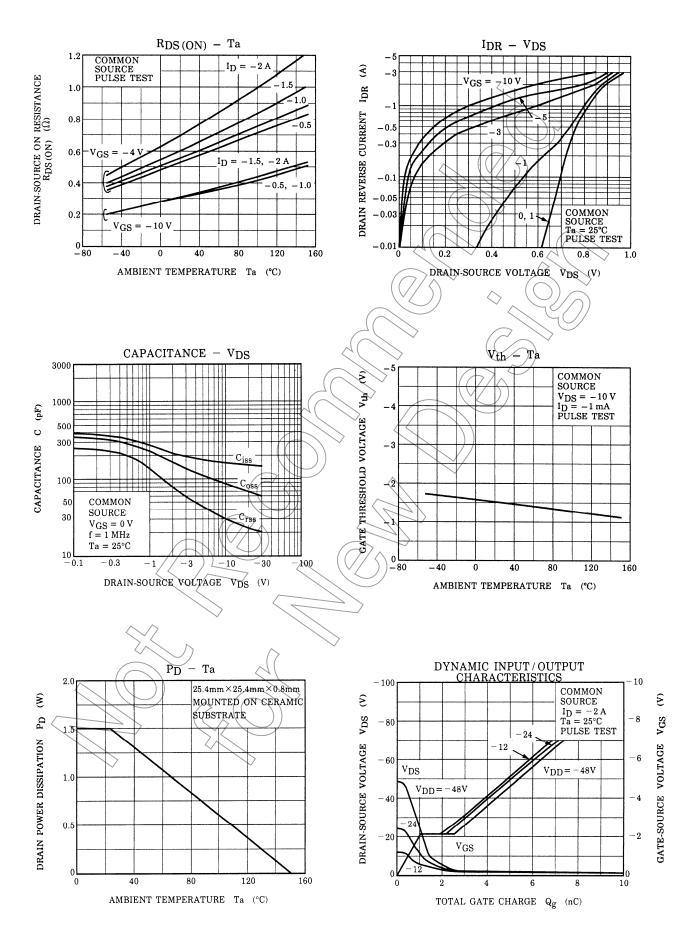
Source-Drain Ratings and Characteristics (Ta = 25°C)

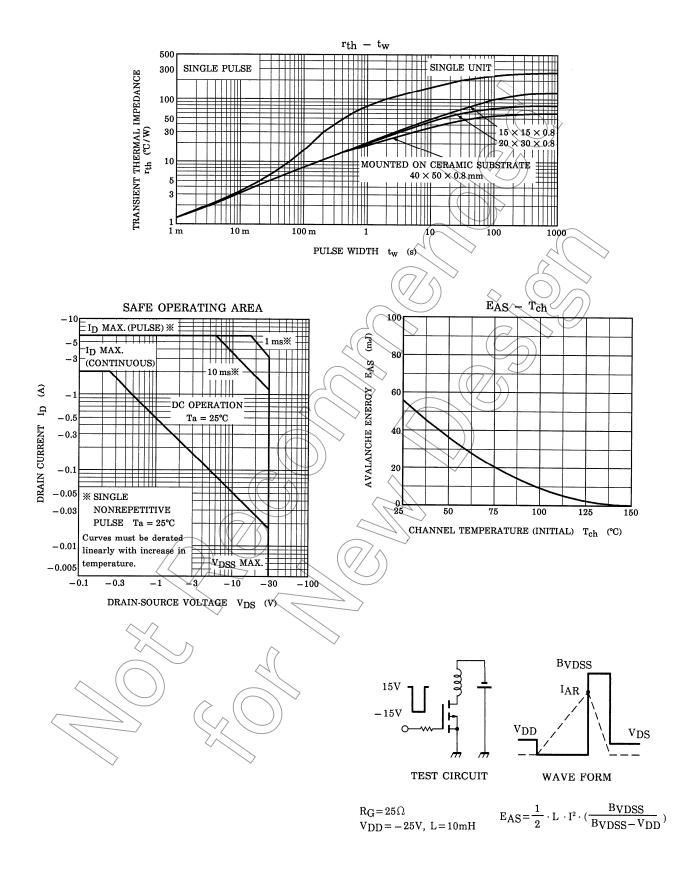
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR} (Note 1)	—	_	_	-2	А
Pulse drain reverse current (Note 1)	I _{DRP} (Note 1)	—	_	_	-6	A
Forward voltage (diode)	V _{DSF}	I _{DR} = -2 A, V _{GS} = 0 V	_	_	1.5	V
Reverse recovery time	t _{rr}	I _{DR} = -2 A, V _{GS} = 0 V	_	40	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} / dt = 50 A / μs	_	18	_	nC

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