TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

SSM3K05FU

High Speed Switching Applications

· Small package

• Low on resistance: $R_{on} = 0.8 \Omega \max (@V_{GS} = 4 V)$

: $R_{on} = 1.2 \Omega \text{ max } (@V_{GS} = 2.5 \text{ V})$

• Low gate threshold voltage

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DS}	20	V	
Gate-source voltage		V _{GSS}	±12	V	
Drain current	DC	I _D	400	mA	
	Pulse	I _{DP}	800		
Drain power dissipation (Ta = 25°C)		P _D (Note 1)	150	mV/	
Channel temperature		T _{ch}	150	(C)	
Storage temperature range		T _{stg}	−55~150	ွဲ	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

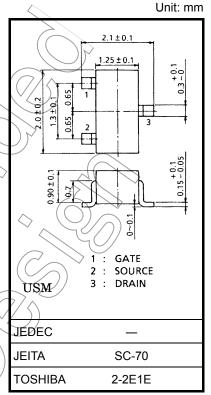
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure

rate, etc).

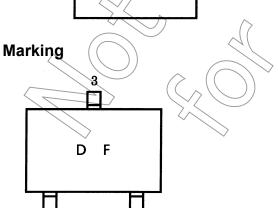
Note 1: Mounted on FR4 board.

(25.4 mm × 25.4 mm × 1.6 t, Qu-pad;/0.6 mm² × 3)

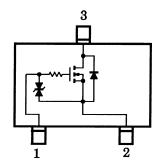
1.0 m



Weight: 0.006 g (typ.)



Equivalent Circuit



Handling Precaution

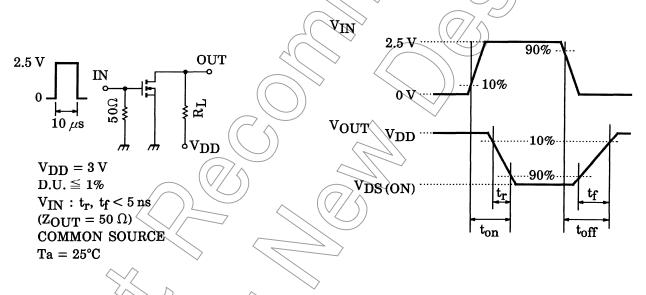
When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$	_	_	±1	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 1$ mA, $V_{GS} = 0$	20	_	_	V
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0		_	1	μА
Gate threshold vo	oltage	V _{th}	V _{DS} = 3 V, I _D = 0.1 mA	0.6	_	1.1	V
Forward transfer	admittance	Y _{fs}	$V_{DS} = 3 \text{ V}, I_D = 200 \text{ mA}$ (Note 2)	350) >-	_	mS
Drain-source ON resistance		R _{DS} (ON)	$I_D = 200 \text{ mA}, V_{GS} = 4 \text{ V}$ (Note 2)	> <u>~</u>	0.6	0.8	Ω
			I _D = 200 mA, V _{GS} = 2.5 V (Note 2)))	0.85	1.2	
Input capacitance	;	C _{iss}	V _{DS} = 3 V, V _{GS} = 0, f = 1 MHz		22		pF
Reverse transfer	capacitance	C _{rss}	V _{DS} = 3 V, V _{GS} = 0, f = 1 MHz	_	9	_	pF
Output capacitan	ce	Coss	V _{DS} = 3 V, V _{GS} = 0, f = 1 MHz	_	21	_	pF
Switching time	Turn-on time	t _{on}	V _{DD} = 3 V, I _D = 100 mA, V _{GS} = 0~2.5 V		6 0	\rightarrow	no
	Turn-off time	t _{off}		-	70	> —	ns

Note 2: Pulse test

Switching Time Test Circuit

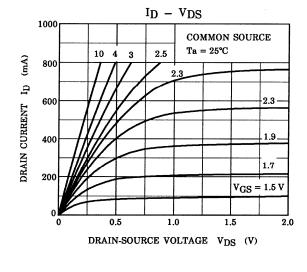


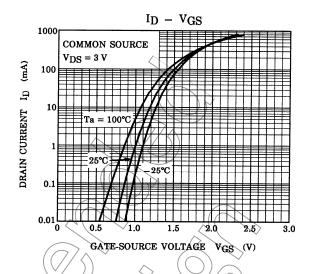
Precaution

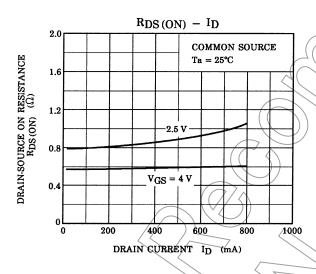
 V_{th} can be expressed as voltage between gate and source when low operating current value is $I_D = 100~\mu A$ for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} .

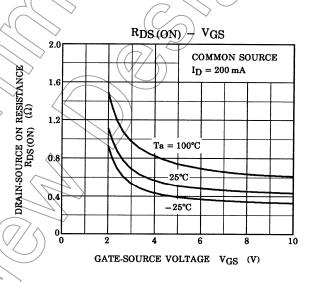
(Relationship can be established as follows: V_{GS} (off) $< V_{th} < V_{GS}$ (on))

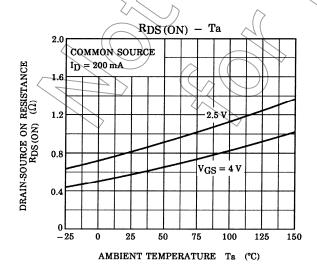
Please take this into consideration for using the device.

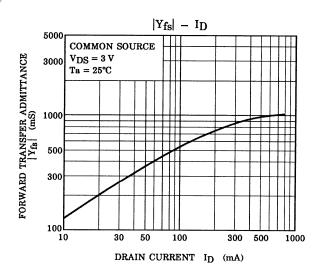




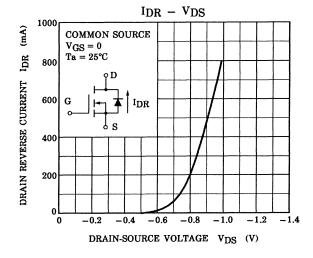


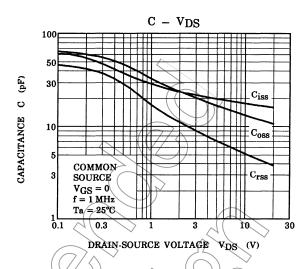


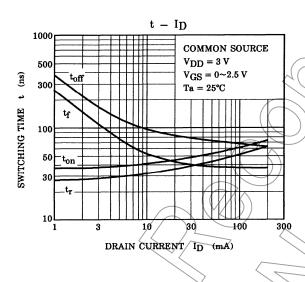


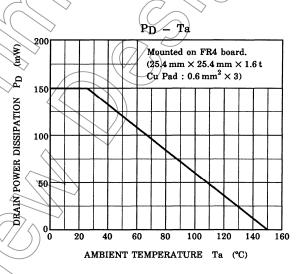


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