TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

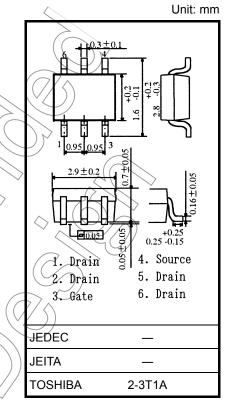
TPC6004

Notebook PC Applications Portable Equipment Applications

- Low drain-source ON resistance: R_{DS} (ON) = 19 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 11 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 20 \ V)$
- Enhancement mode: V_{th} = 0.5 to 1.2 V (VDS = 10 V, ID = 200 $\mu A)$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	20	× V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	20	y	
Gate-source voltage		V _{GSS}	±12	> v	
Drain current	DC	(Note 1)	I _D	6	A
	Pulse	(Note 1)	I _{DP}	24	A
Drain power dissipation (t = 5 s) (Note 2a)		PD <	2.2	W	
Drain power dissipation (t = 5 s) (Note 2b)		PD	0.7	W	
Single pulse avalanche energy (Note 3)		EAS	5.8	mJ	
Avalanche current		$\left(\left(I_{AR} \right) \right)$	3	A	
Repetitive avalanche energy (Note 4)		EAR	0.22	mJ	
Channel temperature		() T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



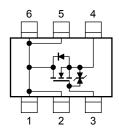
Weight: 0.011 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data/(i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 5 s)$ (Note 2a)	R _{th (ch-a)}	56.8	°C/W
$\begin{array}{l} \mbox{Thermal resistance, channel to ambient} \\ (t=5 \mbox{ s}) & (\mbox{Note 2b}) \end{array}$	R _{th (ch-a)}	178.5	°C/W

Circuit Configuration

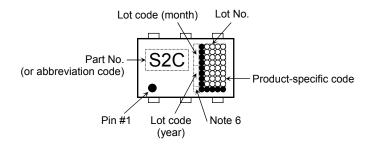


Note: (Note 1), (Note 2), (Note 3), (Note 4) and (Note 5): See the next page.

This transistor is an electrostatic-sensitive device. Please handle with caution.

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Marking (Note 5)



Note 6: A dot marking for identifying the indication of product Labels. Without a dot: [[Pb]]/INCLUDES > MCV With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Electrical Characteristics (Ta = 25°C)

			$\langle \cap \rangle$			
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	IGSS	$V_{GS} = \pm 10 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$) —		±10	μA
Drain cut-OFF current	IDSS	$V_{DS} \neq 20 V, V_{GS} = 0 V$	_	_	10	μA
Drain-source breakdown voltage	V (BR) DSS	$1_{D} = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	20	_		v
Drain-source breakdown voltage	V (BR) DSX	$I_{D} = 10 \text{ mA}, V_{GS} = -12 \text{ V}$	8	_	_	
Gate threshold voltage	V _{th}	$V_{DS} = 10 \text{ V}, I_D = 200 \mu\text{A}$	0.5	—	1.2	V
	\bigcirc	$V_{GS} = 2.0 V, I_D = 3 A$		30	37	
Drain-source ON resistance	RDS (ON)	$V_{GS} = 2.5 \text{ V}, I_D = 3 \text{ A}$		25	32	mΩ
))	$V_{GS} = 4.5 V, I_{D} = 3 A$	—	19	24	
Forward transfer admittance	Y _{fs}	$V_{DS} = 10 V, I_{D} = 3 A$	5.5	11	_	S
Input capacitance	C _{iss}			1400		
Reverse transfer capacitance	Crss	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		165		pF
Output capacitance	C _{oss}			180		
Rise time	tr	√ 5 V □ I _D = 3 A	_	5		
Switching time	ton	$V_{GS} \stackrel{5 V}{\underset{0 V}{\overset{0}{\overset{0}{\overset{0}{\overset{0}{\overset{0}{\overset{0}{\overset{0}{$	_	10		ns
Fall time	t _f	R 0 4 4 7 0 R = 3.5	_	14		115
Turn-OFF time	t _{off}	$V_{DD} \simeq 10 \ V \label{eq:VDD}$ Duty \leq 1%, $t_w = 10 \ \mu s$	_	60		
Total gate charge (gate-source plus gate-drain)	Qg		_	17	_	
Gate-source charge	Q _{gs}	$V_{DD} \simeq 16 \text{ V}, \text{ V}_{GS} = 5 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		13		nC
Gate-drain ("miller") charge	Q _{gd}			4		

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Source-Drain Ratings and Characteristics (Ta = 25°C)

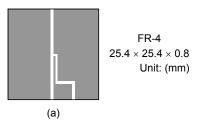
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Pulse drain reverse current (Note	1) I _{DRP}	—	_	_	24	А
Forward voltage (Diode)	V _{DSF}	$I_{DR} = 6 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$	_	_	-1.2	V

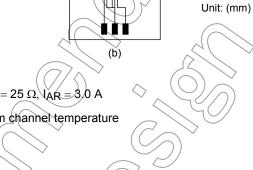
Note 1: Ensure that the channel temperature does not exceed 150°C.

- Note 2: (a) Device mounted on a glass-epoxy board (a)
- (b) Device mounted on a glass epoxy board (b)

FR-4

 $25.4\times25.4\times0.8$



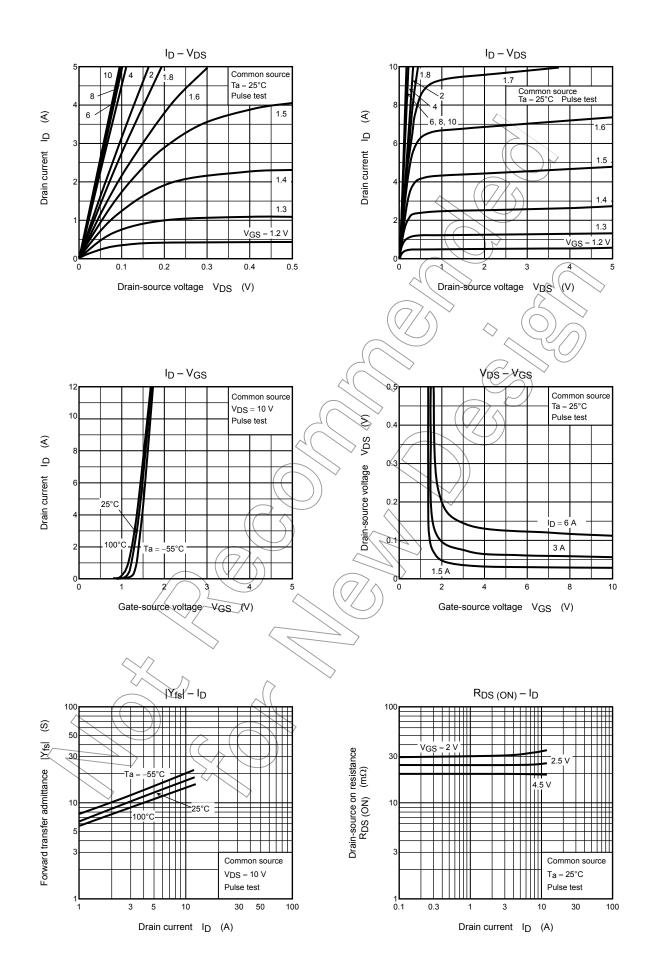


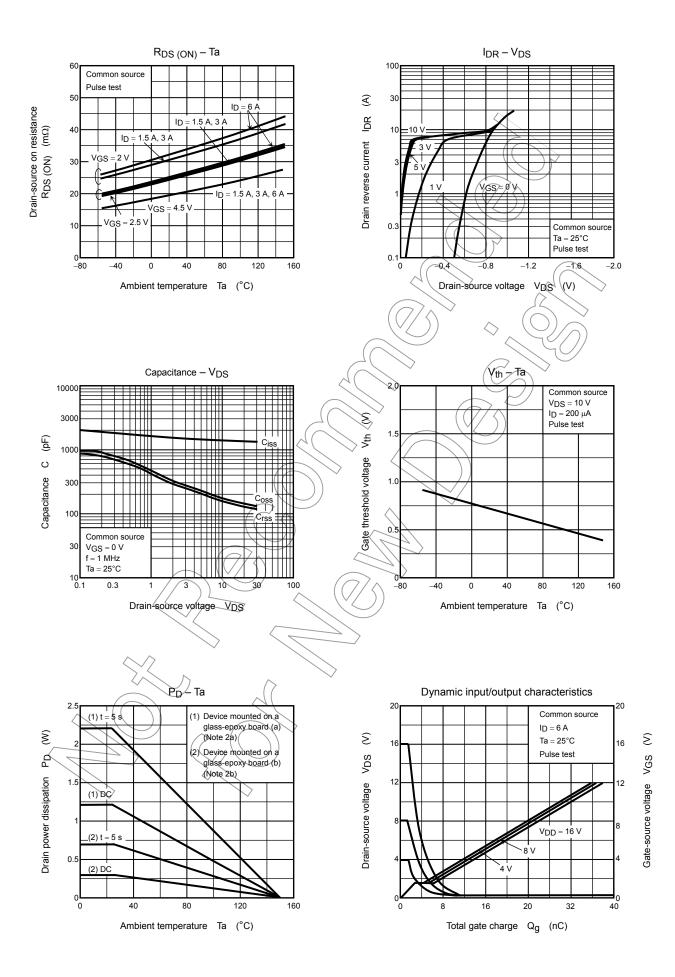
Note 3: $V_{DD} = 16 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, R_G = 25 Ω , V_{AR} = 3.0 A

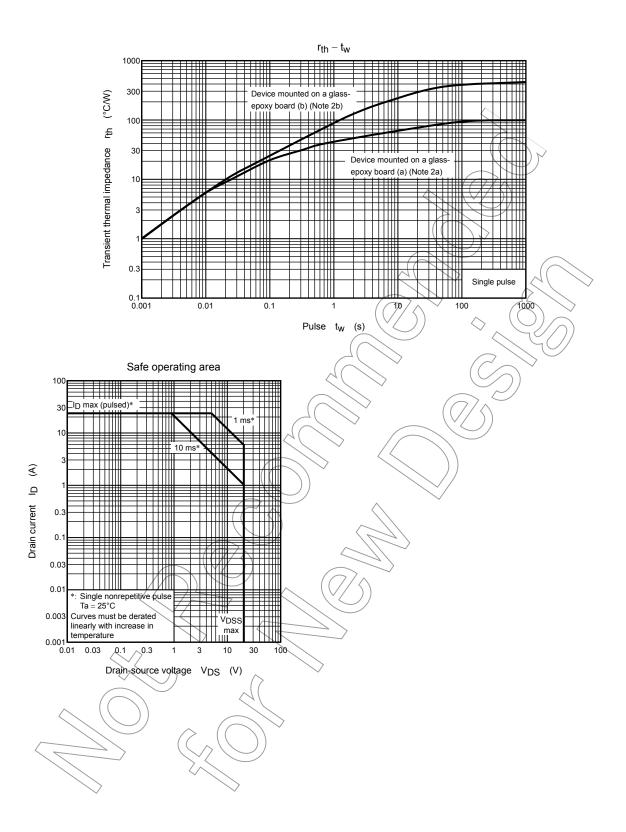
Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

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