TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS V-H)

TPC8037-H

High-Efficiency DC-DC Converter Applications

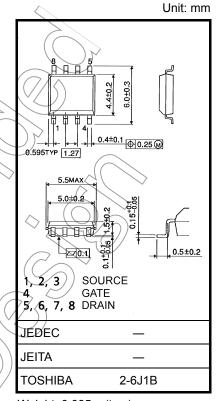
Notebook PC Applications

Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 5.0 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = 7.6 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 36 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 30 V)$
- Enhancement mode: $V_{th} = 1.5$ to 2.5 V ($V_{DS} = 10$ V, $I_{D} = 1$ mA)/

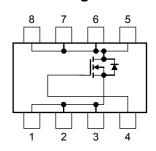
Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	V
Gate-source voltage		V _{GSS}	±20	< <u>v</u>
Drain current	DC (Note 1)	ID((12	A
	Pulsed (Note 1)	JDP	48	,
Drain power dissipation (t = 10 s) (Note 2a)		PD	1.9	M
Drain power dissipation (t = 10 s) (Note 2b)		D	1.0	w
Single-pulse avalanche energy (Note 3)		EAS	94	mJ
Avalanche current		IAR	12	Α
Repetitive avalanche energy (Note 2a) (Note 4)		EAR	0.18	mJ
Channel temperature		₹ch	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight: 0.085 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

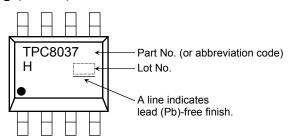
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t=10\;s) \eqno(Note\;2a)$	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

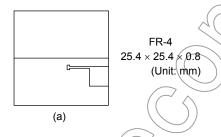
Marking (Note 5)

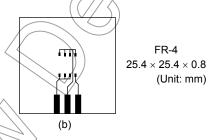


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = 24 \text{ V}$, $T_{Ch} = 25^{\circ}\text{C}$ (initial), $L = 500 \mu\text{H}$, $R_{G} = 25 \Omega$, $I_{AR} = 12 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

* Weekly code: (Three digits)

Week of manufacture

(01 for the first week of the year, continuing up to 52 or 53)

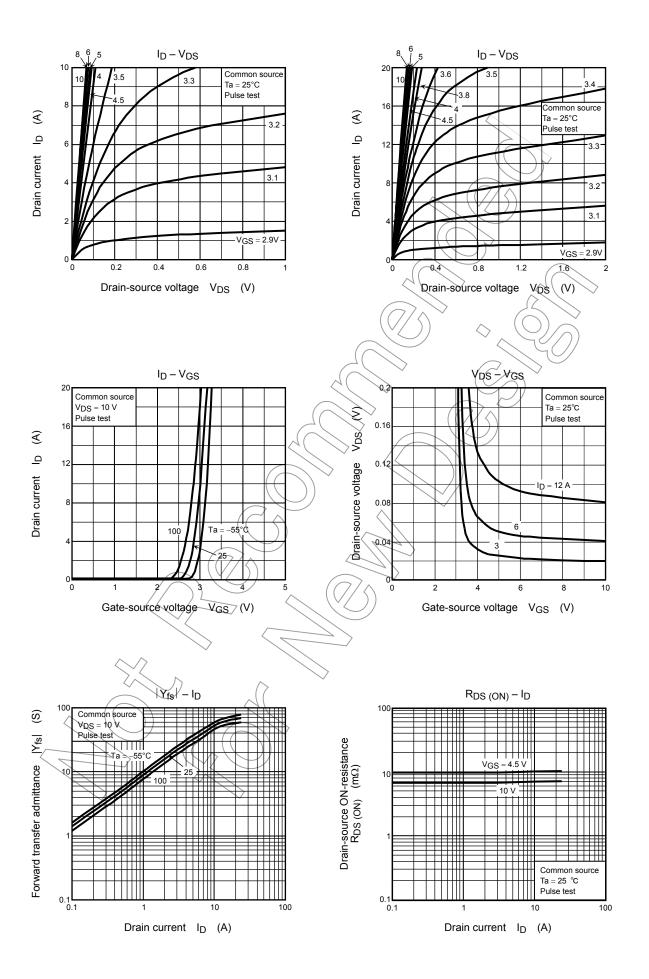
Year of manufacture (The last digit of the year)

Electrical Characteristics (Ta = 25°C)

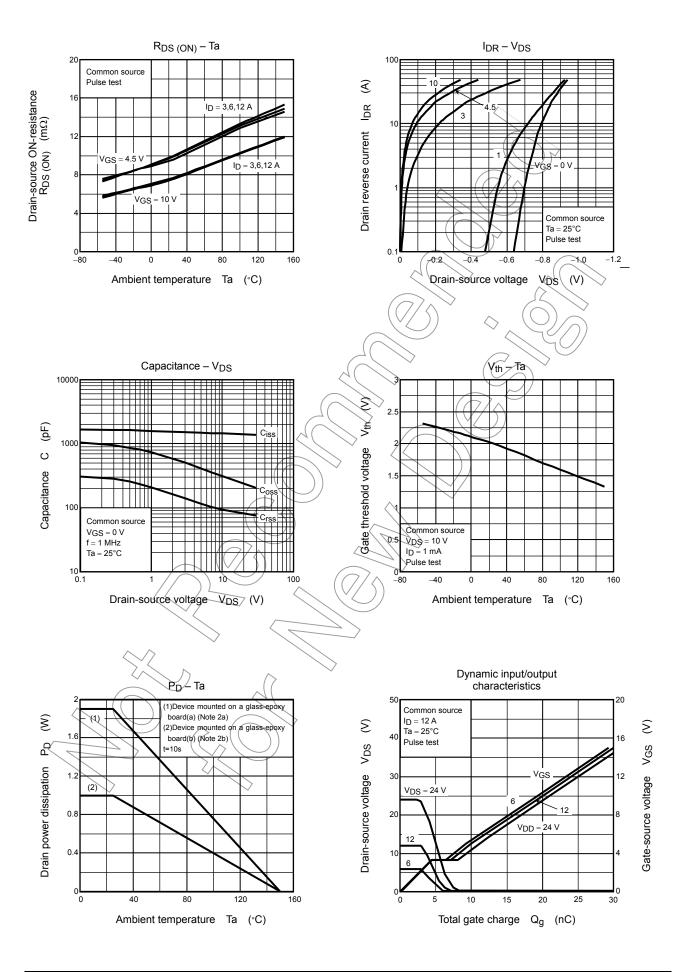
Cha	racteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage curre	ent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA	
Drain cutoff curren	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА	
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	30	_	_	V	
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_		
Gate threshold vol	tage	V_{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.5) >_	2.5	V	
Drain-source ON-resistance		Б	V _{GS} = 4.5 V, I _D = 6 A	<u> </u>	9.9	13.9	- mΩ	
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 6 A))	7.6	11.4		
Forward transfer a	dmittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	18	36	_	S	
Input capacitance		C _{iss}		² —	1433	2150		
Reverse transfer of	apacitance	C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	83	125	pF	
Output capacitance		C _{oss}			303	\downarrow		
Gate resistance		Rg	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	-	1.0) 1.5	Ω	
Switching time	Rise time	t _r	ACS 0 A C C C C C C C C C C C C C C C C C C		3)) —		
	Turn-on time	t _{on}) 10		ns	
	Fall time	t _f			3.9		113	
	Turn-off time	t _{off}	Duty ≤ 1%, t _w ≠ 10 μs	_	23			
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 12 \text{ A}$		21			
			$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, V_{D} \neq 12 \text{ A}$		11			
Gate-source charg	ge 1 /	$Q_{\hat{g}s_1}$		_	4.4	_	nC	
Gate-drain ("Miller") charge		Qgd	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$	_	3.7	_		
Gate switch charg	e (7)	Q _{SW}		_	5.0	_		

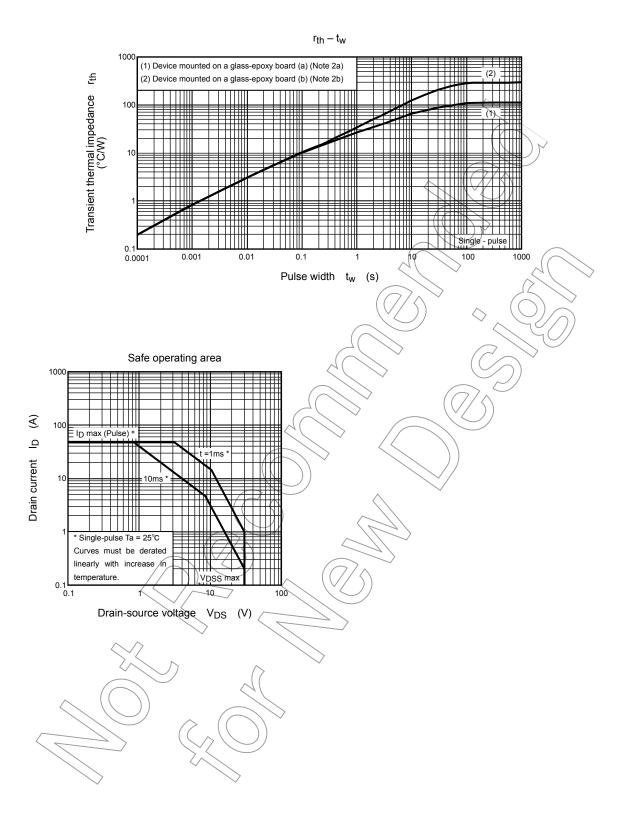
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP} —	_	_	48	Α
Forward voltage (diode)	V_{DSF} $I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

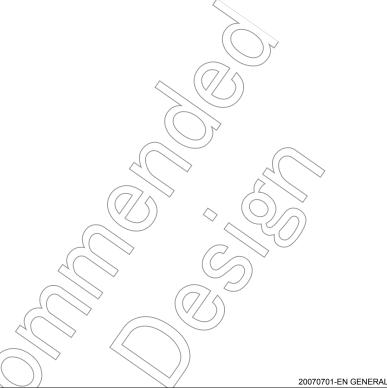


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