TOSHIBA Field Effect Transistor with Built-in Schottky Barrier Diode

Silicon N-Channel MOS Type (U-MOS V-H)

TPC8A06-H

High Efficiency DC-DC Converter Applications Notebook PC Applications Portable Equipment Applications

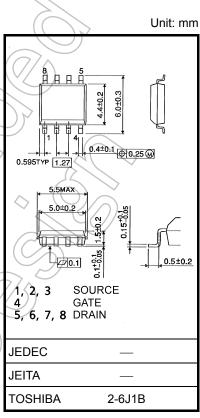
- Built-in schottky barrier diode Low forward voltage: $V_{DSF} = 0.6 \text{ V (max)}$
- High-speed switching
- Small gate charge: QSW = 4.5 nC (typ.)
- Low drain-source ON-resistance:

 R_{DS} (ON) = 9.2 m Ω (typ.)(V_{GS} = 4.5 V)

- High forward transfer admittance: $|Y_{fs}| = 37 \mathrm{\ S}$ (typ.)
- Low leakage current: $IDSS = 100 \mu A (max) (VDS = 30 V)$
- Enhancement mode: $V_{th} = 1.3 \text{ to } 2.3 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

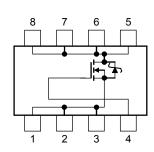
Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol (Rating	Unit
Drain-source voltage		V _{DS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR})) 30	V
Gate-source voltage		VGSS	±20	\ v
Drain current	DC (Note 1)	(D))	12	A
Diam current	Pulsed (Note 1)	DP	48	$\langle \hat{r} \rangle$
Drain power dissipation	on (t = 10 s) (Note 2a)	PD	1.9	>>
Drain power dissipation $(t = 10 \text{ s})$ (Note 2b)		PD	1.0	W
Single-pulse avalanche energy (Note 3)		EAS	94	mJ
Avalanche current		IAR	12	Α
Repetitive avalanche energy (Tc=25°C) (Note 4)		EAR	0.10	mJ
Channel temperature		Teh	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight: 0.085g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

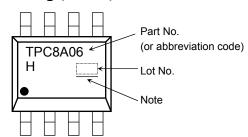
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient $(t=10 \; s) \eqno(Note \; 2b)$	R _{th (ch-a)}	125	°C/W

Marking (Note 5)



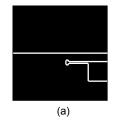
Note: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment

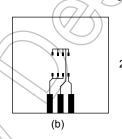
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



FR-4 25.4 × 25.4 × 0.8 (Unit: mm)

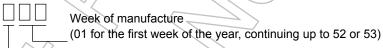


FR-4 $25.4 \times 25.4 \times 0.8$ (Unit: mm)

Note 3: $V_{DD} = 24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), $L = 500 \mu\text{H}$, $R_G = 25 \Omega$, $I_{AR} = 12 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: * Weekly code: (Three digits)



Year of manufacture (The last digit of the year)

2

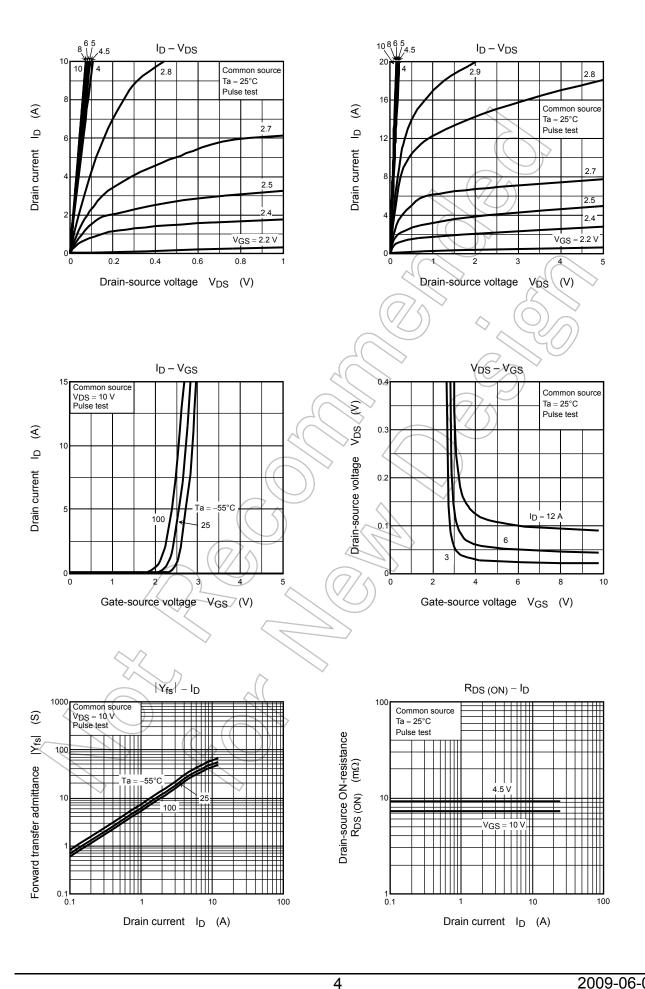
Electrical Characteristics (Ta = 25°C)

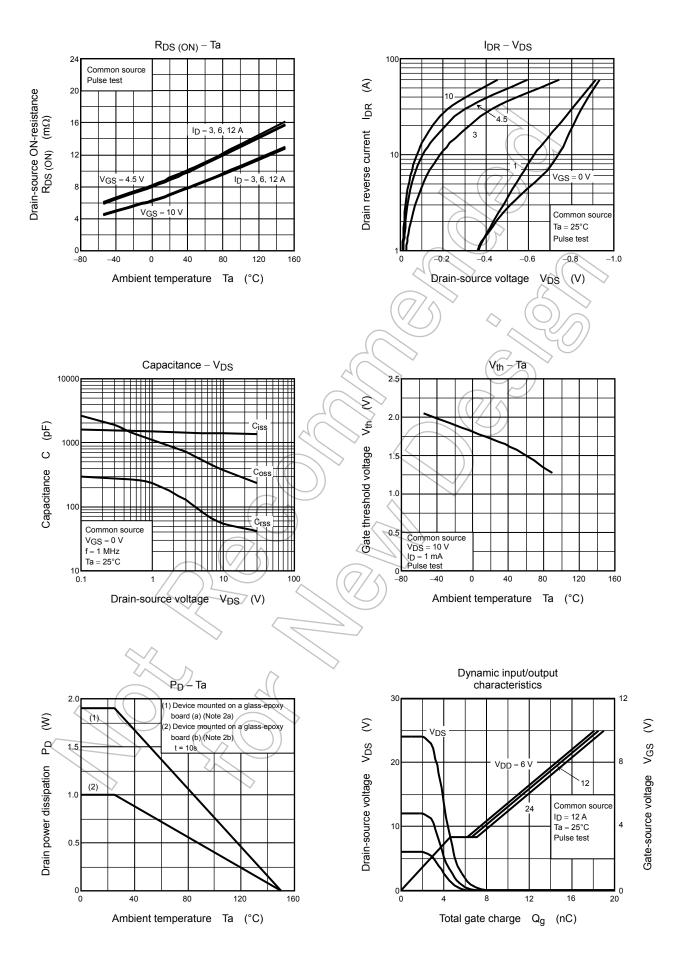
Cha	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage curr	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA	
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	100	μΑ	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V	
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_		
Gate threshold vo	ltage	V_{th}	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	1.3) /_	2.3	٧	
Drain-source ON-resistance		Б	V _{GS} = 4.5 V, I _D = 6 A) 	9.2	12.9	- mΩ	
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 6 A	\rightarrow	7.2	10.1		
Forward transfer a	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	19	37		S	
Input capacitance		C _{iss}		_	1400	1800		
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	54	80	pF	
Output capacitance		C _{oss}		/	380	\nearrow		
Gate resistance		rg	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 5 \text{ MHz}$	-(1.6	2.4	Ω	
Switching time	Rise time	t _r	V _{GS} 10 V I _D = 6 A V _{OUT}	4	2.4) _	ns	
	Turn-on time	t _{on}		$\sqrt{2}$	8.6	_		
	Fall time	t _f	4.77 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2. 2) –	3.5			
	Turn-off time	t _{off}	V _{DD} ≈ 15 V Duty ≤ 1%, t _w = 10 μs	_	22	_		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$	_	19		-	
			$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 12 \text{ A}$	_	9.6	_		
Gate-source charge 1		Q _{gs1}		_	4.6	_	nC	
Gate-drain ("Miller") charge		Q_{gd}	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 12 \text{ A}$		2.5	_		
Gate switch charge		Q _{SW}		_	4.5	_		

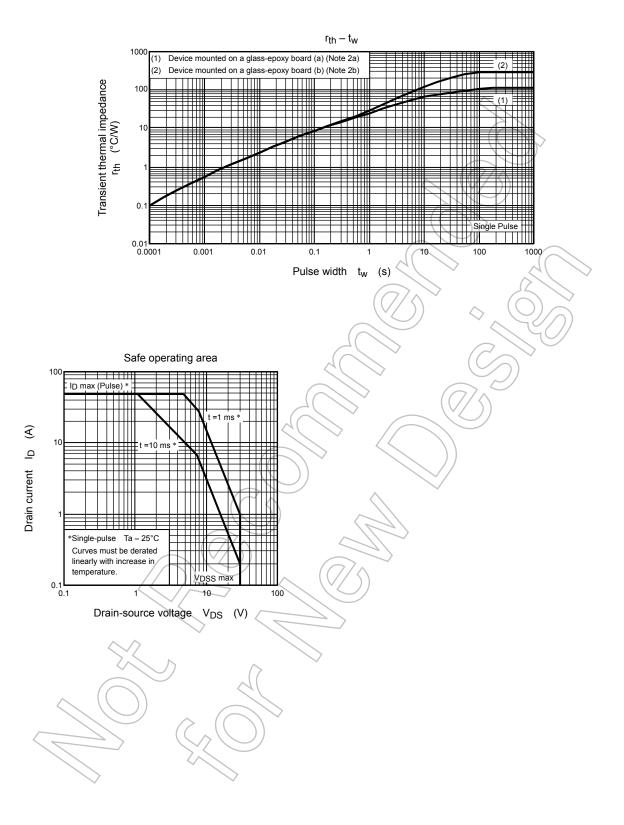
Source-Drain Ratings and Characteristics (Ta = 25°C)

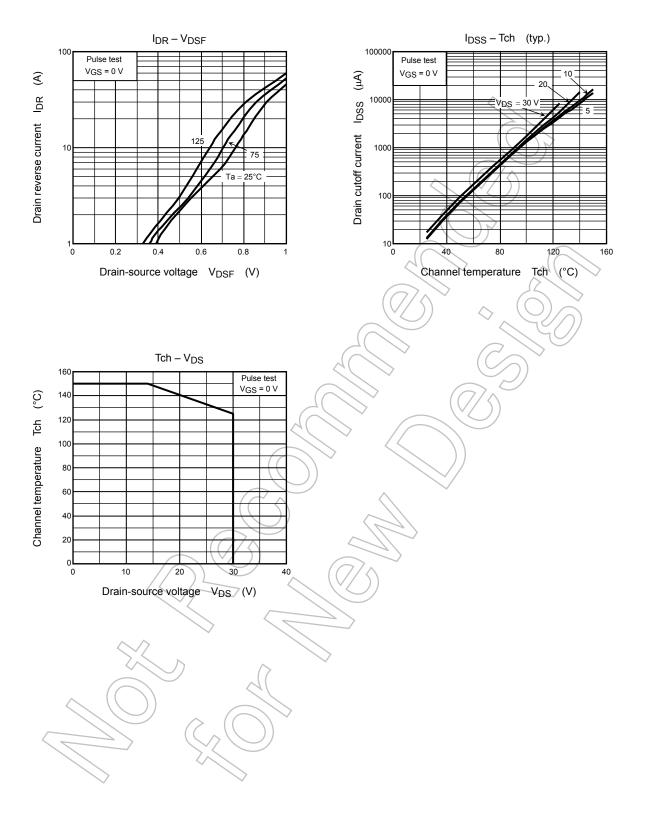
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Peak forward current Pulse (Note 1)	I _{FP}	> -	_	_	48	Α
Forward voltage (diode)		I _{DR} = 1 A, V _{GS} = 0 V	_	-0.4	-0.6	V
Torward voltage (diode)	VDSF	$I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V

3 2009-06-05









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