TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

# **TPCA8040-H**

High-Efficiency DC-DC Converter Applications

Notebook PC Applications

Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: Qsw = 5.7 nC (typ.)
- Low drain-source ON-resistance: R<sub>DS</sub> (ON) = 6.1 m $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 70 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 10 \,\mu A \,(max) \,(V_{DS} = 30 \,V)$
- Enhancement mode:  $V_{th} = 1.3 \text{ to } 2.3 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 0.2 \text{ m/s})$

### **Absolute Maximum Ratings (Ta = 25°C)**

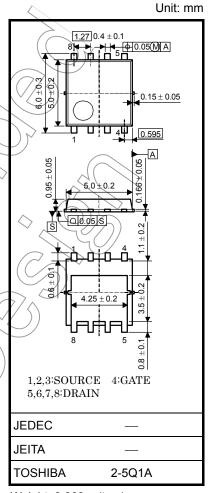
Characteristic		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	30	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub>	30	V	
Gate-source voltage		V <sub>GSS</sub>	±20	<< <u>v</u>	
Drain current	DC (Note 1)	ID((	23	A	
	Pulsed (Note 1)		69	,	
Drain power dissipation	on (Tc = 25°C)	$(P_D )$	30	\/ w	
Drain power dissipation	on (t = 10 s) (Note 2a)	PO	2.8	*	
Drain power dissipation (t = 10 s) (Note 2b)		PD	(1.6/)	W	
Single-pulse avalanche energy (Note 3)		EAS	6	mJ	
Avalanche current		I <sub>AR</sub>	23	Α	
Repetitive avalanche energy (Tc = 25°C) (Note 4)		EAR 2.1		mJ	
Channel temperature		Tch	150	°C	
Storage temperature range		Tstg	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

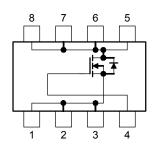
operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions")" Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.069 g (typ.)

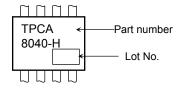
#### **Circuit Configuration**



#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R <sub>th (ch-c)</sub>	4.17	°C/W
Thermal resistance, channel to ambient $(t=10\ s) \eqno(Note\ 2a)$	R <sub>th (ch-a)</sub>	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R <sub>th (ch-a)</sub>	78.1	°C/W

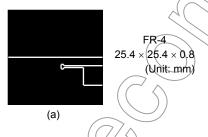
#### Marking (Note 5)

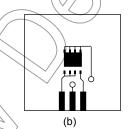


Note 1: Ensure that the channel temperature does not exceed 150°C.

(b) Device mounted on a glass-epoxy board (b) Note 2: (a) Device mounted on a glass-epoxy board (a)







FR-4  $25.4\times25.4\times0.8$ (Unit: mm)

Note 3:  $V_{DD} = 24 \text{ V}$ ,  $V_{Ch} = 25^{\circ}\text{C}$  (initial),  $L = 100 \mu\text{H}$ ,  $R_{G} \neq 25^{\circ}\Omega$ ,  $I_{AR} = 23 \text{ A}$ 

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)

Week of manufacture

(01) for the first week of the year, continuing up to 52 or 53)

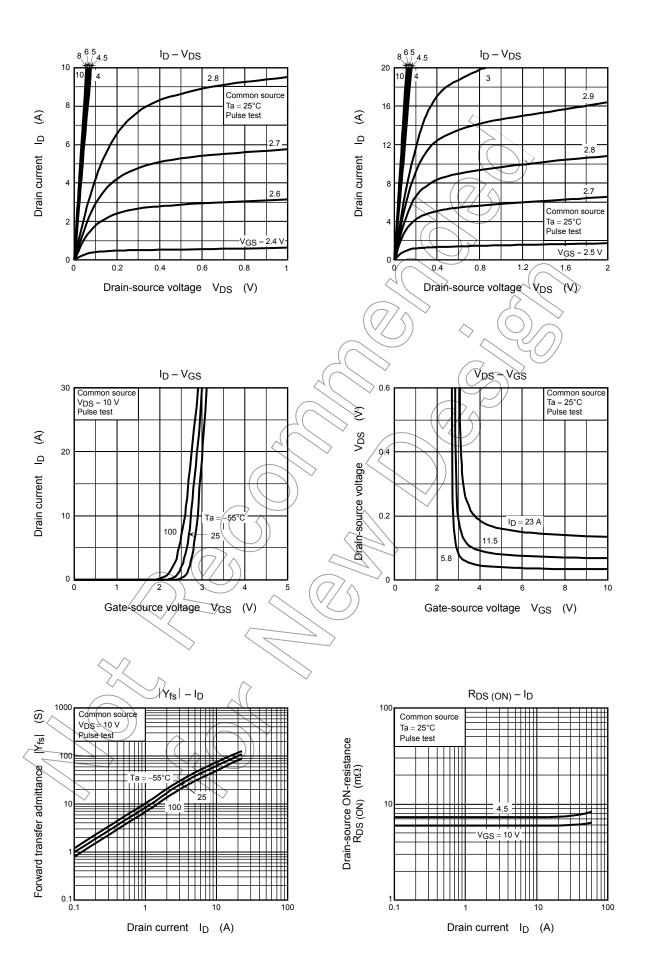
Year of manufacture (The last digit of the year)

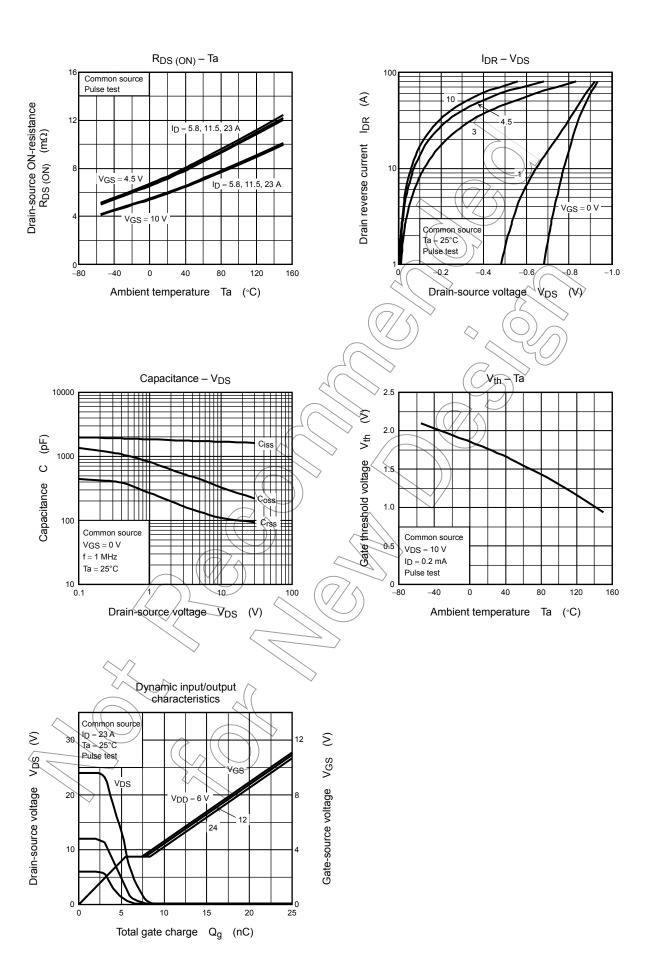
### **Electrical Characteristics (Ta = 25°C)**

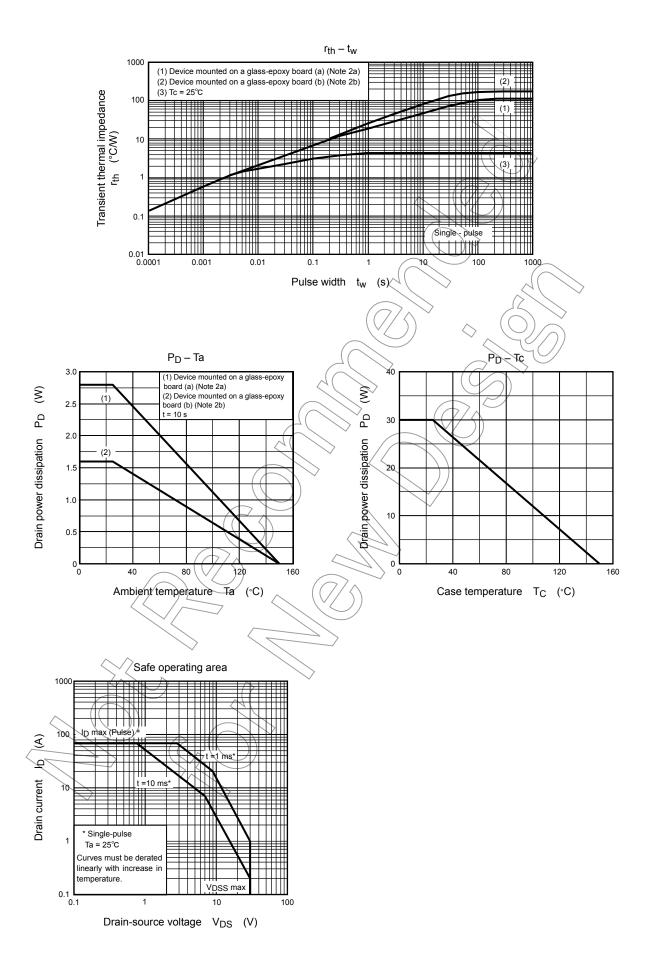
Cha	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	rent	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V (BR) DSX	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V		_	_	V
Gate threshold vo	ltage	$V_{th}$	$V_{DS} = 10 \text{ V}, I_D = 0.2 \text{ mA}$	1.3	) >	2.3	V
Drain-source ON-resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 11.5 A		7.4	10.8	mΩ
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 11.5 A	)	6.1	9.4	
Forward transfer a	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 12 A	35	70	_	S
Input capacitance		C <sub>iss</sub>		_	1700	2200	pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	110	170	
Output capacitance		Coss			330	$\rightarrow$	
Gate resistance		rg	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 5 MHz	-	2.3	3.5	Ω
Switching time	Rise time	t <sub>r</sub>	V <sub>GS</sub> 10 V   I <sub>D</sub> = 11.5 A   V <sub>OUT</sub>   G	7	5.4	) _	
	Turn-on time	t <sub>on</sub>		\(\int\)	15		ns
	Fall time	tf	4. w w o M		8.2		113
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>w</sub> = 10 μs	_	37		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 23 \text{ A}$	_	23	_	
			$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, V_{D} \neq 23 \text{ A}$		12	_	
Gate-source charge 1		Q <sub>gs1</sub>			5.4	_	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>	$V_{DD} \approx 24 \text{ V, V}_{GS} = 10 \text{ V, I}_{D} = 23 \text{ A}$		3.0	_	
Gate switch charg	ge (7/	Q <sub>SW</sub>		_	5.7	_	

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I <sub>DRP</sub>	_	_	_	69	Α
Forward voltage (diode)	$V_{DSF}$ $I_{DR} = 2$	3 A, V <sub>GS</sub> = 0 V			-1.2	V







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