TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (Ultra High speed U-MOSIII)

TPCP8001-H

High Efficiency DC / DC Converter Applications
Notebook PC Applications
Portable Equipment Applications

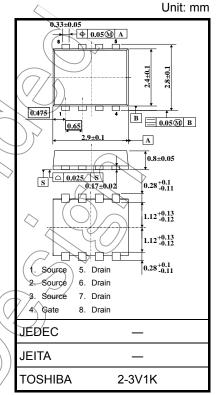
- Small footprint due to a small and thin package
- High speed switching
- Small gate charge: QSW = 3.6 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = $13 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 16 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{V)}$
- Enhancement mode: $V_{th} = 1.1 \text{ to } 2.3 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	< <u>v</u>	
Drain current	DC (Note 1)	ID((7.2	A	
Diain current	Pulsed (Note 1)	JDP	28.8	^	
Drain power dissipation (t = 5 s) (Note 2a)		PD	1.68	/w	
Drain power dissipation (t = 5 s) (Note 2b)		D	0.84	w	
Single-pulse avalanche energy (Note 3)		EAS	33.6	mJ	
Avalanche current		IAR	7.2	Α	
Repetitive avalanche energy (Note 2a) (Note 4)		EAR	0.066	mJ	
Channel temperature		√T ch	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

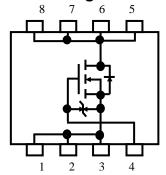
Note: For Notes 1 to 5, refer to the next page.

This transistor is an electrostatic-sensitive device. Handle with care.

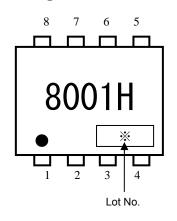


Weight: 0.017 g (typ.)

Circuit Configuration

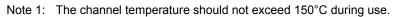


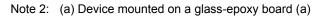
Marking (Note 5)

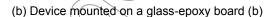


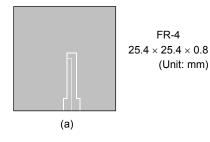
Thermal Characteristics

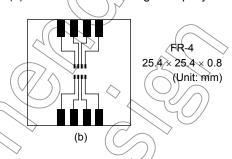
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 5 s)$ (Note 2a)	R _{th (ch-a)}	74.4	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R _{th (ch-a)}	148.8	°C/W











Note 3: $V_{DD} = 24 \text{ V}$, $T_{Ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_{G} = 25 \Omega$, $I_{AR} = 7.2 A$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: • on the lower left of the marking indicates Pin 1.

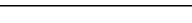
* Weekly code: (Three digits)

Week of manufacture

(01 for first week of the year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)



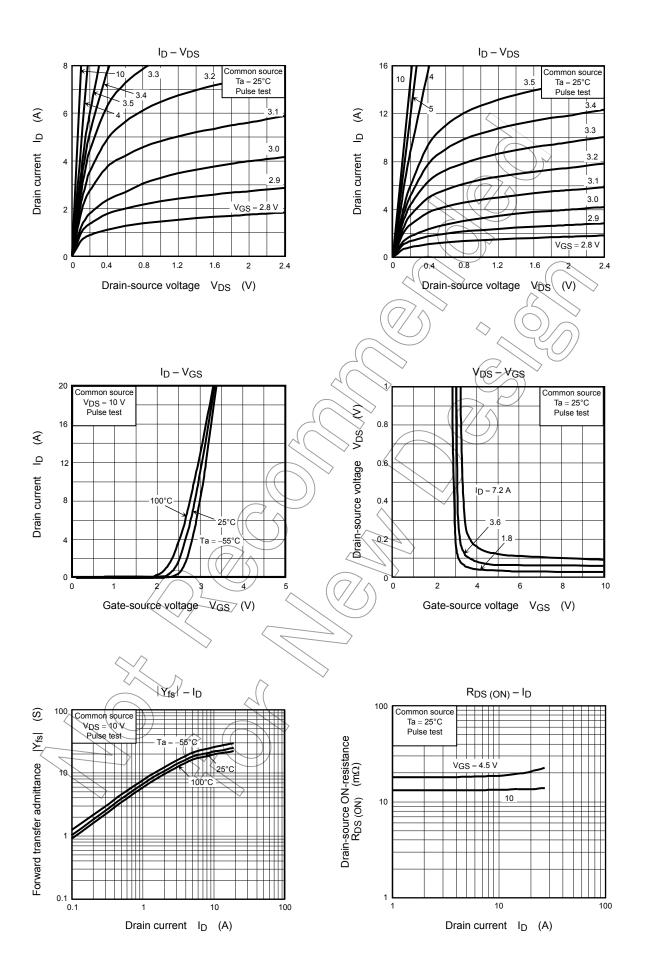
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Electrical Characteristics (Ta = 25°C)

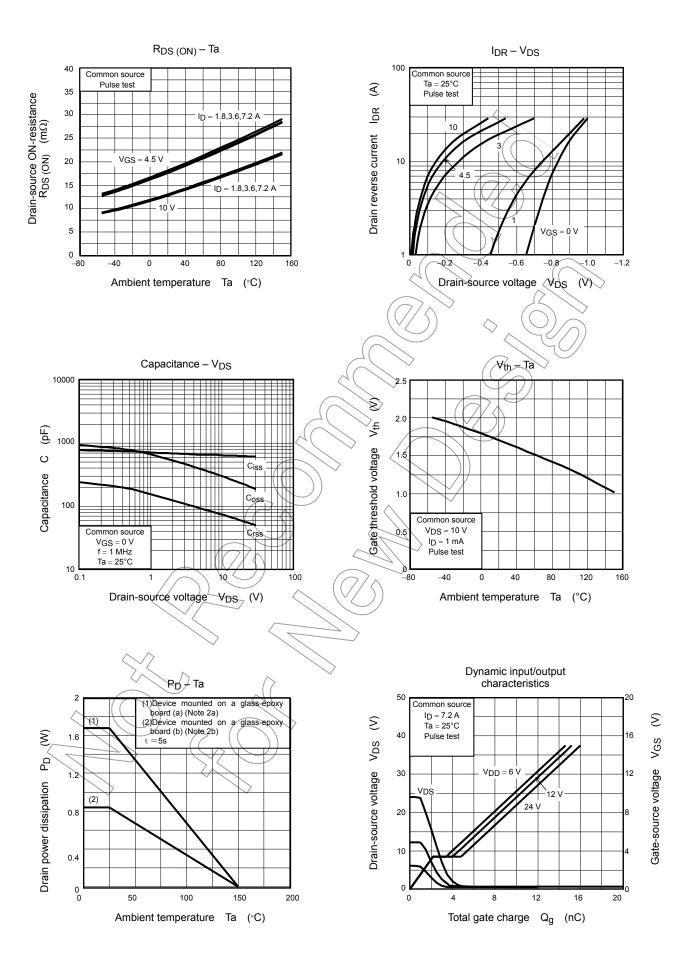
Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rrent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cutoff curre	ent	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V _{(BR)DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	V
Gate threshold ve	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	\11) >	2.3	V
Drain-source ON-resistance		R _{DS} (ON)	$V_{GS} = 4.5 \text{ V}, I_D = 3.6 \text{ A}$		19	25	- mΩ
		TVDS (ON)	V _{GS} = 10 V, I _D = 3.6 A	\rightarrow	13	16	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3.6 A	8	16	_	S
Input capacitance	е	C _{iss}			640	_	
Reverse transfer	capacitance	C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		75	_	pF
Output capacitance		Coss	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		300	\searrow	
Switching time Fa	Rise time	t _r	10 V \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	-(4	> _	
	Turn-on time	t _{on}	VGS 0V VOUT		(m)	_	ns
	Fall time	t _f	4 7 7 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	\bigcirc	4	_	115
	Turn-off time	t _{off}	V _{DD} ≃ 15 V Duty ≦ 1%, t _W = 10 μs) —	18	_	
Total gate charge	9		$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 7.2 \text{ A}$	_	11	_	
(gate-source plus gate-drain)		Qg	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 72 \text{ A}$	_	6.3		
Gate-source cha	rge 1	Q _{gs1}		_	2.2		nC
Gate-drain ("Mille	er") charge	Qgd	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7.2 \text{ A}$	_	2.6		
Gate switch char	ge	Qsw		_	3.6	_	

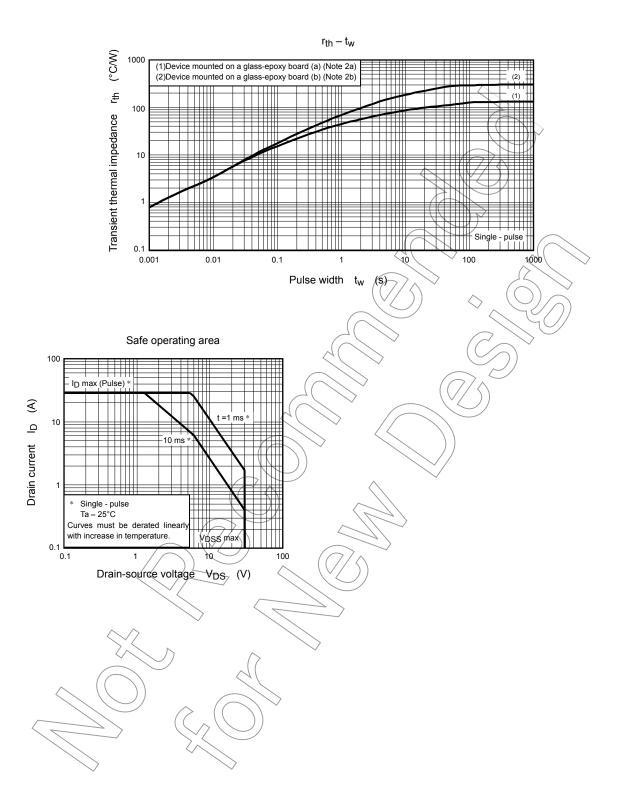
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	TORP -	_	_	28.8	Α
Forward voltage (diode)	V_{DSF} $I_{QR} = 7.2 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V



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