TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS V-H)

TPCP8005-H

High-Efficiency DC/DC Converter Applications Notebook PC Applications Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 5.0 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = 9.8 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 30 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{V)}$
- Enhancement mode: $V_{th} = 1.5 \text{ to } 2.5 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA})$

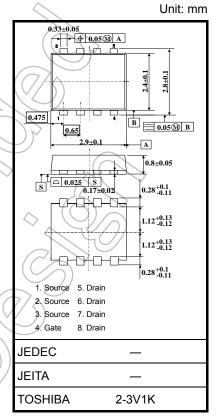
Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	y	
Gate-source voltage		V _{GSS}	±20	\\\\	
Drain current	DC (Note 1)	ID((11	A	
Diam current	Pulsed (Note 1)	IDP	44	^	
Drain power dissipation (t = 5 s)		$\left(\begin{array}{c} P_{D} \end{array}\right)$	1.68	\\w	
(Note 2a)			1.00		
Drain power dissipation $(t = 5 s)$		PD	0.84	w	
(Note 2b)			(O/Δ)	7	
Single-pulse avalanche energy (Note 3)		EAS	78.7	mJ	
Avalanche current		I _{AR}	11	Α	
Repetitive avalanche energy		E _{AR}	0.137	mJ	
(Note 2a) (Note 4)		\sim	*		
Channel temperature		Tch	150	°C	
Storage temperature range		T _{stg}	–55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

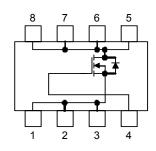
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

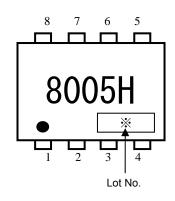


Weight: 0.017 g (typ.)

Circuit Configuration

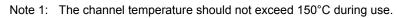


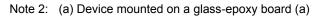
Marking (Note 5)

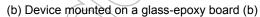


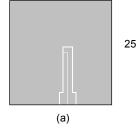
Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t=5\;s) \eqno(Note\;2a)$	R _{th (ch-a)}	74.4	°C/W
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2b)	R _{th (ch-a)}	148.8	°C/W

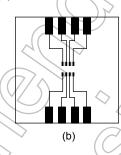








FR-4 $25.4 \times 25.4 \times 0.8$ (Unit: mm)

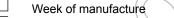


FR-4 25.4 × 25.4 × 0.8 (Unit: mm)

Note 3: $V_{DD}=24~V,~T_{ch}=25^{\circ}C$ (initial), $L=0.5~mH,~R_{G}=25~\Omega,~I_{AR}=11A$

Note 4: Repetitive rating: pulse width limited by max channel temperature

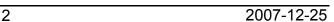
Note 5: * Weekly code: (Three digits)



(01 for first week of year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)

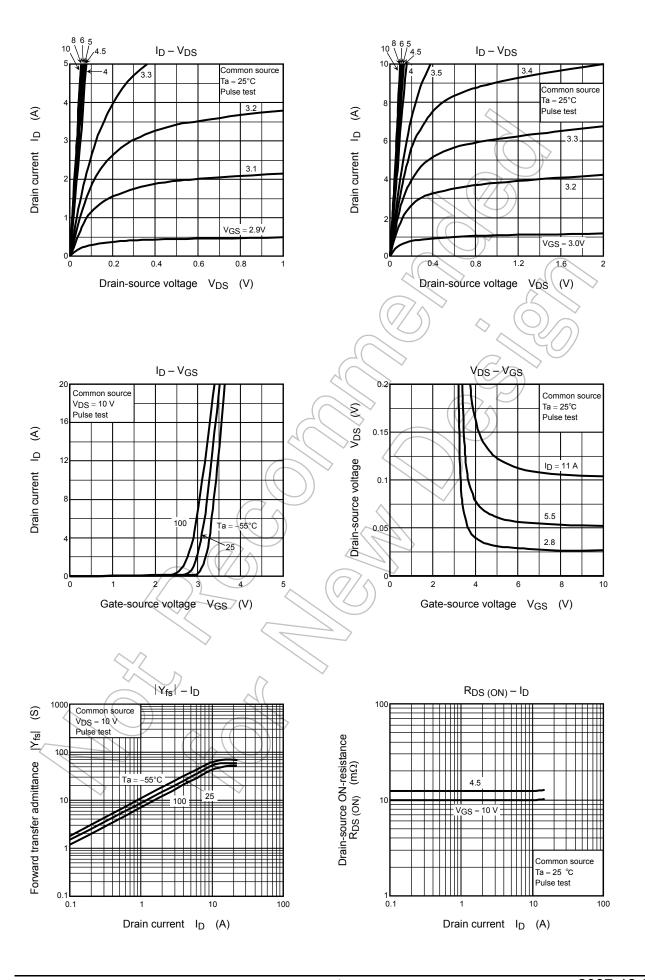


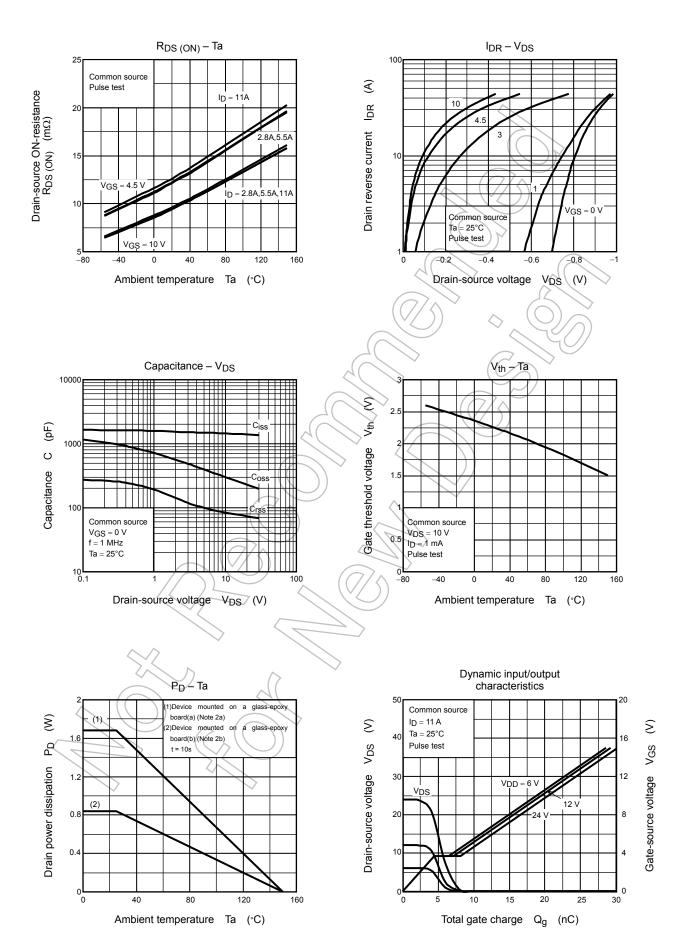
Electrical Characteristics (Ta = 25°C)

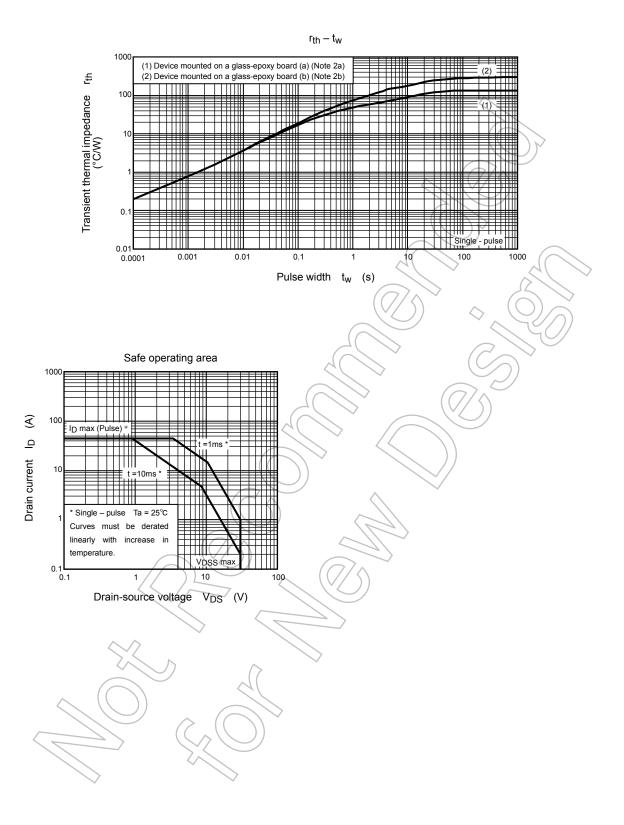
Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μΑ
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	v
Gate threshold vo	oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	1.5))^_	2.5	V
Drain-source ON-resistance		_	V _{GS} = 4.5 V, I _D = 5.5 A) <u> </u>	12.1	15.7	m()
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 5.5 A	\mathcal{D}	9.8	12.9	mΩ
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5.5 A	15	30	_	S
Input capacitance		C _{iss}		⁷ —	1433	2150	
Reverse transfer	capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	83	125	pF
Output capacitance		Coss			303	\nearrow	
Gate resistance		Rg	$V_{DS} = 10 \text{ V}, V_{GS} \neq 0 \text{ V}, f = 5 \text{ MHz}$	-6	1.0	1.5	Ω
Switching time	Rise time	t _r	VGS 10 V ID = 5.5 A OVOUT		3.0) —	
	Turn-on time	t _{on}			10	_	ne
	Fall time	t _f	2.7.7.9.2.2.7.5.2.2.7.5.2.2.7.5.2.2.7.5.2.2.7.5.2.2.7.5.2.2.7.5.2.2.7.5.2.2.2.2		4.0	_	ns ns
	Turn-off time	t _{off}	V _{DD} ≃ 15 V Duty ≦ 1%, t _w = 10 μs		22	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 11 \text{ A}$		20	_	
			$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 11 \text{ A}$	_	11	_	
Gate-source char	ge 1	Q _{gs1}		_	4.8	_	nC
Gate-drain ("Miller") charge		Q_{gd}	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 11 \text{ A}$		3.0		
Gate switch charge		Q _{SW}		_	5.0	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP}	_		_	44	Α
Forward voltage (diode)	/V _{DSF}	$I_{DR} = 11 \text{ A}, V_{GS} = 0 \text{ V}$		_	-1.2	V







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