TOSHIBA Diode Silicon Epitaxial Pin Type

# JDP2S01E

### UHF~VHF Band RF Attenuator Applications

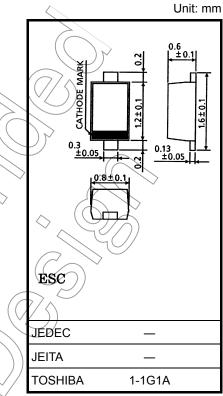
- Suitable for reducing set's size as a result from enabling high-density mounting due to 2-pin small packages.
- Low series resistance:  $r_s = 0.65 \Omega$  (typ.)
- Low capacitance:  $C_T = 0.65 \text{ pF}$  (typ.)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Reverse voltage	VR	30	$(\mathcal{N} \land)$
Forward current	١ <sub>F</sub>	50	mA
Junction temperature	Тј	125	℃
Storage temperature range	T <sub>stg</sub>	-55~125	°℃

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual

reliability data (i.e. reliability test report and estimated failure rate, etc).



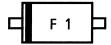
Weight: 0.0014 g (typ.)

## Electrical Characteristics (Ta = 25°C)

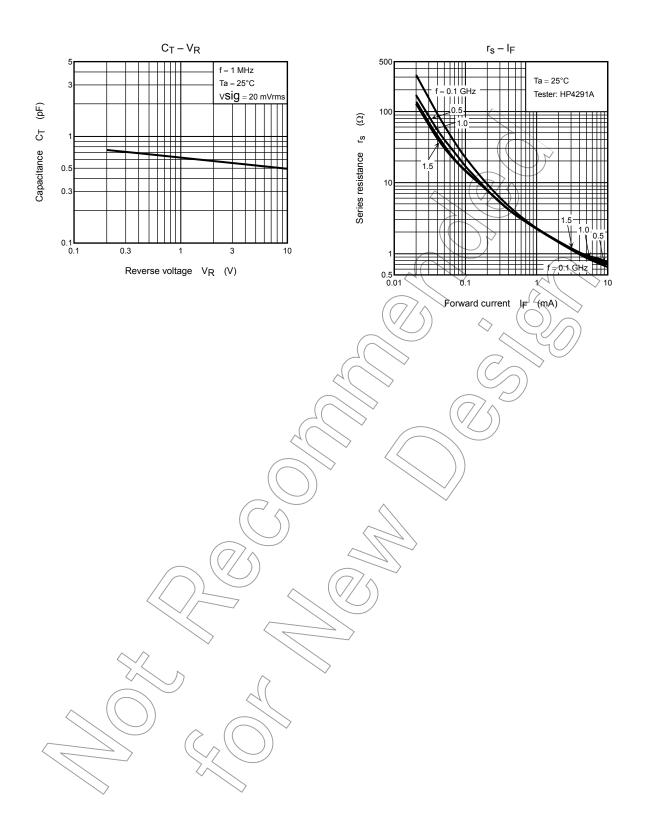
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse voltage	VR	$I_R = 10 \ \mu A$	30	_	_	V
Reverse current	Ι <sub>R</sub>	V <sub>R</sub> = 30 V	_	_	0.1	μA
Forward voltage	VF	I <sub>F</sub> = 50 mA	_	0.9	0.95	V
Capacitance	CŢ	V <sub>R</sub> = 1 V, f = 1 MHz	_	0.65	0.8	pF
Series resistance	ſs	I <sub>F</sub> = 10 mA, f = 100 MHz	_	0.65	1.0	Ω

Note: Signal level when capacitance is measured: Vsig = 20 mVrms

### Marking



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