## TOSHIBA Photocoupler GaAs IRED + Photo-Triac

## TLP260J

## Triac Drivers

## Programmable Controllers

## AC-Output Modules

## Solid-State Relays

The TOSHIBA mini-flat coupler TLP260J is a small-outline coupler suitable for surface mount assembly.

The TLP260J consists of a photo-triac optically coupled to a gallium arsenide infrared-emitting diode.

- Peak off-state voltage
- Trigger LED current
- On-state current
- Isolation voltage
- UL-recognized
- Option (V4) type

VDE-approved
: 600 V (min)
: 10 mA (max)
: 70 mA (max)
: 3000 Vrms (min)
: UL1577, file No. E67349

EIN EN 60747-5-2 satisfied
Maximum operating insulation voltage : 565 VpK
Highest permissible overvoltage :6000 Vpk
Note: When an EN 60747-5-2 approved type is needed, be sure to specify "Option (V4)".

- Construction Mechanical Rating

Creepage distance $: 4.0 \mathrm{~mm}(\mathrm{~min})$
Clearance $\quad: 4.0 \mathrm{~mm}(\mathrm{~min})$
Insulation thickness $\quad: 0.4 \mathrm{~mm}$ (min)

## Trigger LED Current

| Classification* | Trigger LED Current (mA) |  | Product <br> Classification <br> Marking |
| :---: | :---: | :---: | :---: |
|  | $V_{T}=6 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}$ |  |  |
|  | Min | 10 | Blank |

Note: Be sure to use standard product type names when submitting type names for safety certification testing, i.e., TLP260J.


Weight: 0.09 g (typ.)

## Pin Configuration



1. Anode
2. Cathode
3. Terminal 1
4. Terminal 2

Absolute Maximum Ratings ( $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )

| Characteristic |  |  | Symbol | Rating | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Forward current |  | $\mathrm{I}_{\mathrm{F}}$ | 50 | mA |
|  | Forward current derating ( $\mathrm{Ta} \geq 53^{\circ} \mathrm{C}$ ) |  | $\Delta \mathrm{I}_{\mathrm{F}} /{ }^{\circ} \mathrm{C}$ | -0.7 | $\mathrm{mA} /{ }^{\circ} \mathrm{C}$ |
|  | Peak forward current (100 $\mu$ s pulse, 100 pps ) |  | IFP | 1 | A |
|  | Reverse voltage |  | $V_{R}$ | 5 | V |
|  | Junction temperature |  | $\mathrm{T}_{\mathrm{j}}$ | 125 | ${ }^{\circ} \mathrm{C}$ |
| $\begin{aligned} & \bar{\circ} \\ & \text { O} \\ & \text { む̀ } \end{aligned}$ | Off-state output terminal voltage |  | V ${ }_{\text {DRM }}$ | 600 | V |
|  | On-state RMS current | $\mathrm{Ta}=25^{\circ} \mathrm{C}$ | $\mathrm{I}_{\mathrm{T} \text { (RMS) }}$ | 70 |  |
|  |  | $\mathrm{Ta}=70^{\circ} \mathrm{C}$ |  | 40 |  |
|  | On-state current derating ( $\mathrm{Ta} \geq 25^{\circ} \mathrm{C}$ ) |  | $\Delta \mathrm{T}_{\mathrm{T}} /{ }^{\circ} \mathrm{C}$ | -0.67 | $\mathrm{mA} /{ }^{\circ} \mathrm{C}$ |
|  | Peak on-state current ( $100 \mu$ s pulse, 120 pps ) |  | ITP |  | A |
|  | Peak nonrepetitive surge current$\text { (PW = } 10 \mathrm{~ms} \text { ) }$ |  | ITSM | $1.2$ | A |
|  | Junction temperature |  | $\mathrm{T}_{\mathrm{j}}$ | 100 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range |  |  | $\mathrm{T}_{\text {stg }}$ | 55~125 | ${ }^{\circ} \mathrm{C}$ |
| Operating temperature range |  |  | Topr | -40~100 | ${ }^{\circ} \mathrm{C}$ |
| Lead soldering temperature (10 s) |  |  | Tsol | 260 | ${ }^{\circ} \mathrm{C}$ |
| Isolation voltage (AC, 1 min., R.H. $\leq 60 \%$ ) (Note |  |  | $B V_{S}$ | 2500 | Vrms |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered as a two-terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

## Recommended Operating Conditions

| Characteristic | Symbol | Min | Typ. | Max | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{AC}}$ | - | - | 240 | Vac |
| Forward current | $\mathrm{I}_{\mathrm{F}}$ | 15 | 20 | 25 | mA |
| Peak on-state current |  | $\mathrm{I}_{\mathrm{TP}}$ | - | - | 1 |
| Operating temperature |  | $\mathrm{T}_{\mathrm{opr}}$ | -25 | - | 85 |
| ${ }^{\circ} \mathrm{C}$ |  |  |  |  |  |

Note. Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

## Individual Electrical Characteristics ( $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )

| Characteristic |  | Symbol | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 씀 | Forward voltage | $V_{F}$ | $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | 1.0 | 1.15 | 1.3 | V |
|  | Reverse current | $\mathrm{I}_{\mathrm{R}}$ | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | - | - | 10 | $\mu \mathrm{A}$ |
|  | Capacitance | $\mathrm{C}_{\top}$ | $V=0, f=1 \mathrm{MHz}$ |  | 30 | - | pF |
| $\grave{\circ}$ <br> 0 <br> ÓO <br> 0 | Peak off-state current | IDRM | $V_{\text {DRM }}=600 \mathrm{~V}$ |  | 10 | 1000 | nA |
|  | Peak on-state voltage | $V_{\text {TM }}$ | $\mathrm{I}_{\text {TM }}=70 \mathrm{~mA}$ |  | 1.7 | 2.8 | V |
|  | Holding current | ${ }_{\mathrm{I}}^{\mathrm{H}}$ | - |  | 1.0 | - | mA |
|  | Critical rate of rise of off-state voltage | $\mathrm{dv} / \mathrm{dt}$ | $240 \mathrm{Vrms}, \mathrm{Ta}=85^{\circ}$ |  | 500 | - | $\mathrm{V} / \mu \mathrm{s}$ |
|  | Critical rate of rise of commutating voltage | dv / dt(c) | $\mathrm{I}_{\mathrm{T}}=15 \mathrm{~mA}, \mathrm{~V}_{\mathrm{in}}=60 \mathrm{Vrn}$ | - |  | - | $\mathrm{V} / \mu \mathrm{s}$ |

Coupled Electrical Characteristics ( $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )


Isolation Characteristics $\left(\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}\right)$

| Characteristic | Symbol | Test Condition | Min | Typ. | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Capacitance input to output | CS | $\mathrm{V}_{\mathrm{S}}=0, \mathrm{f}=1 \mathrm{MHz}$ | - | 0.8 | - | pF |
| Isolation resistance | RS | $V_{S}=500 \mathrm{~V}, \mathrm{R} . \mathrm{H} . \leq 60 \%$ | $5 \times 10^{10}$ | $10^{14}$ | - | $\Omega$ |
| Isolation voltage | $B V_{S}$ | AC, 1 minute | 3000 | - | - | Vrms |
|  |  | AC, 1 second, in oil | - | 5000 | - |  |
|  |  | DC, 1 minute, in oil | - | 5000 | - | Vdc |

Fig. 1: $\mathrm{dv} / \mathrm{dt}$ test circuit



$I_{T(R M S)}-\mathrm{Ta}$




Normalized IDRM - Ta


Normalized $\mathrm{I}_{\mathrm{H}}-\mathrm{Ta}$



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