TOSHIBA Photocoupler GaAs Ired & Photo-MOSFET

TLP3110

Measurement Instruments
Logic IC Testers / Memory Testers
Board Testers / Scanners

The TOSHIBA mini flat photo relay TLP3110 is a small outline photo relay, suitable for surface mount assembly.

The TLP3110 consists of a GaAs infrared emitting diode optically coupled to a photo–MOSFET in a 4 pin lead package, and has characteristics of small off–state current and small output terminal capacitance, which enable the TLP3110 to be applied to measurement instruments.

1-form-A

• Peak off-state voltage: 60 V (min)

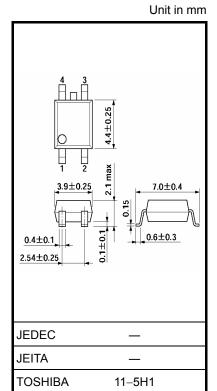
• Trigger LED current: 4 mA (max)

• On-state current: 350 mA (max)

• On-state resistance: 1.2 Ω (max)

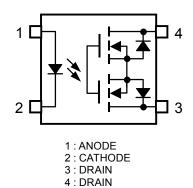
• Isolation voltage: 1500 V_{rms} (min)

• UL recognized: UL1577, file No. E67349

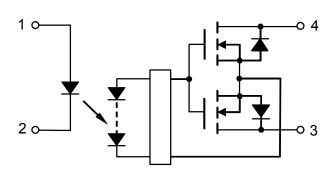


Weight: 0.1 g (typ.)

Pin Configuration (top view)



Schematic



Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	l _F	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C
l۳	Reverse voltage	V _R	6	V
	Junction temperature	Tj	125	°C
	Off-state output voltage	V _{OFF}	60	V
Detector	On-state current	I _{ON}	350	mA
Dete	On-State Current Derating (Ta ≥ 25°C)	Δl _{ON} /°C	-3.5	mA/°C
	Junction temperature	Tj	125	°C
Stor	rage temperature	T _{stg}	-40 to 125	°C
Оре	erating temperature	T _{opr}	–20 to 85	°C
Solo	dering temperature (10 s)	T _{sol}	260	°C
Isola	ation voltage (AC, 1 min., R.H. ≤ 60%) (Note 1)	B _{VS}	1500	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1): Device considered a two-terminal device: Pins 1 and 3 shorted together, and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _{OFF}	_	_	48	V
Forward current	lF	10	_	30	mA
On–state current	I _{ON}	_	_	350	mA
Operating temperature	T _{opr}	-20	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V _F	I _F = 20 mA	1.0	1.2	1.4	V
ED	Reverse voltage	I _R	V _R = 6 V	-	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	15	_	pF
Detector	Off-state current	I _{OFF}	V _{OFF} = 30 V, Ta = 50 °C		0.4	1	nA
Dete	Capacitance	C _{OFF}	V = 0, f = 1 MHz	_	100	150	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	I _{ON} = 350 mA	_	_	4	mA
On-state resistance	R _{ON}	I _{ON} = 350 mA, I _F = 5 mA	_	0.7	1.2	Ω
Return LED Current	I _{FC}	I _{OFF} =10 μA	0.2	_	_	mA

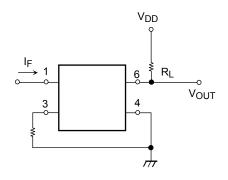
Isolation Characteristics (Ta = 25°C)

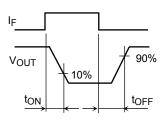
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	5×10 ¹⁰	10 ¹⁴	_	Ω
	B _{VS}	AC, 1 minute	1500	_	_	V
Isolation voltage		AC, 1 second (in oil)	_	3000	_	V _{rms}
		DC, 1 minute (in oil)	_	3000	_	V _{dc}

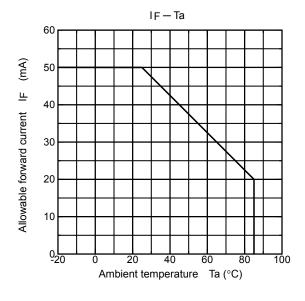
Switching Characteristics (Ta = 25°C)

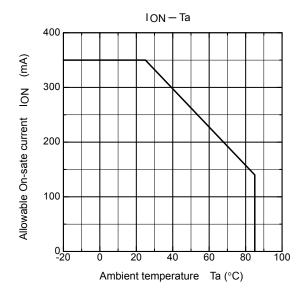
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Turn-on time	t _{ON}	$R_L = 200 \Omega$ (Note 2)	_	_	1	ms
Turn-off time	t _{OFF}	$V_{DD} = 20 \text{ V}, I_F = 10 \text{ mA}$	_	_	1	1115

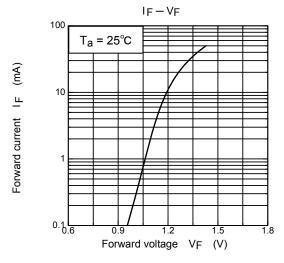
(Note 2): Switching time test circuit

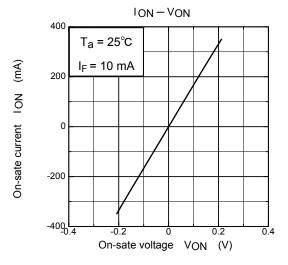


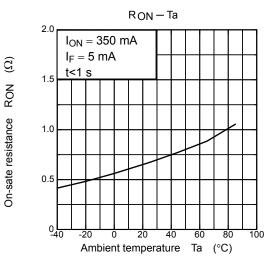


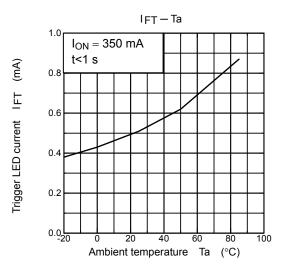


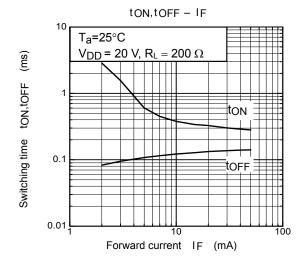


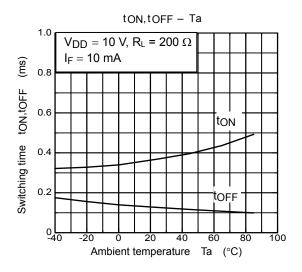


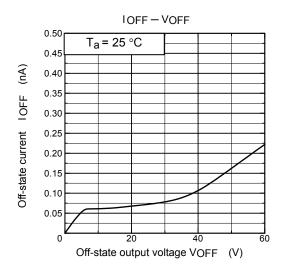


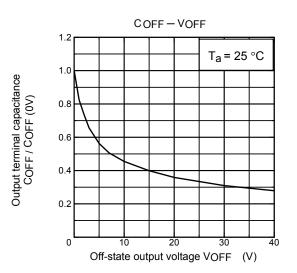












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