TOSHIBA Photocoupler Photorelay

TLP4206G

PBX

Telecommunication

Modem·FAX Cards, Modems In PC

Measurement Instrumentation

The TOSHIBA TLP4206G consists of an aluminum gallium arsenide infrared emitting diode optically coupled to a photo-MOSFET in a SOP, which is suitable for surface mount assembly.

The TLP4206G is a 2-form-B switch, which is suitable for replacement of mechanical relays in many applications.

• 8 pin SOP (2.54SOP8): 2.1 mm high, 2.54 mm pitch

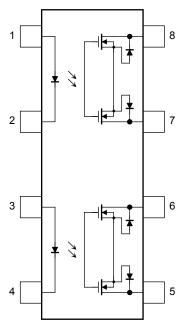
• 2-form-B

Peak off-state voltage: 350 V (min)Trigger LED current: 3 mA (max)

• On-state current: 120 mA (max)

On state resistance: 25 Ω (max)
Isolation voltage: 1500 Vrms (min)

Pin Configuration (top view)



1, 3: ANODE

2, 4: CATHODE 5 : DRAIN D1

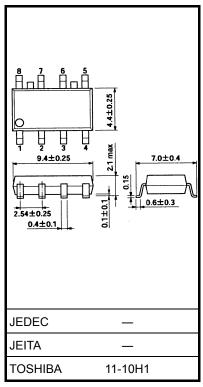
: DRAIN D2

: DRAIN D3

8 : DRAIN D4

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Unit: mm



Weight: 0.2 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

	Characteristics	Symbol	Rating	Unit
LED	Forward current	lF	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C
	Peak forward current (100 μs pulse, 100 pps)	I _{FP}	1	А
	Reverse voltage	V_{R}	5	V
	Junction temperature	Tj	125	°C
	Off-state output terminal voltage	V _{OFF}	350	V
for	On-state current	I _{ON}	120	mA
Detector	On-state current derating (Ta ≥ 25°C)	Δl _{ON} /°C	-1.2	mA/°C
	Junction temperature	Tj	125	°C
Operating temperature range		T _{opr}	-40 to 85	°C
Storage temperature range		T _{stg}	-55 to 125	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Isolation voltage (AC, 1 min, R.H. ≤ 60%) (Note 1)		BVS	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: LED side pins shorted together, and DETECTOR side pins shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V_{DD}	_	_	280	V
Forward current	lF	5	_	25	mA
On-state current	I _{ON}	_	_	120	mA
Operating temperature	T _{opr}	-20	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μА
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
Detec- tor	Off-state current	loff	$V_{OFF} = 350 \text{ V}, I_F = 5 \text{ mA}$	_	_	1	μΑ
Det tc	Capacitance	C _{OFF}	$V = 0$, $f = 1$ MHz, $I_F = 5$ mA	_	65		pF

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Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FC}	I _{OFF} = 10 μA	_	1	3	mA
Return LED current	I _{FT}	I _{ON} = 120 mA	0.1	_	_	mA
On-state resistance	R _{ON}	I _{ON} = 120 mA	_	15	25	Ω

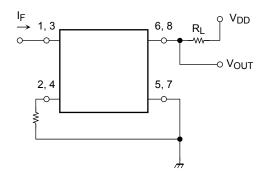
Isolation Characteristics (Ta = 25°C)

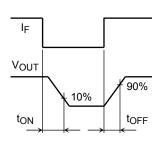
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0, f = 1 MHz	_	8.0	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5×10^{10}	10 ¹⁴	_	Ω
	AC, 1 min BVs AC, 1 s, in oil	AC, 1 min	1500	_	_	Vrms
Isolation voltage		_	3000	_	VIIIIS	
		DC, 1 min, in oil	_	3000	_	Vdc

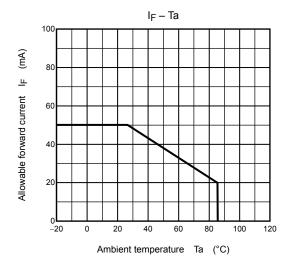
Switching Characteristics (Ta = 25°C)

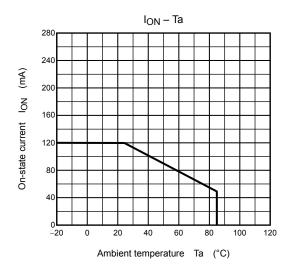
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	toN	$R_L = 200 \Omega$ (Note 2)	_	_	1	ms
Turn-off time	t _{OFF}	$V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$	_	_	3	ms

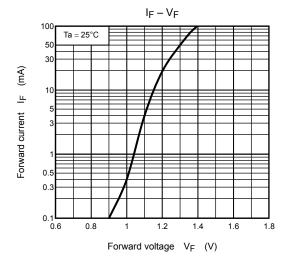
Note 2: Switching time test circuit

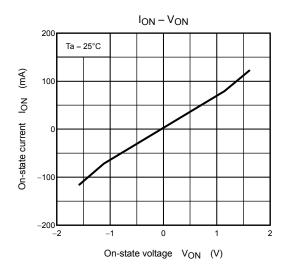


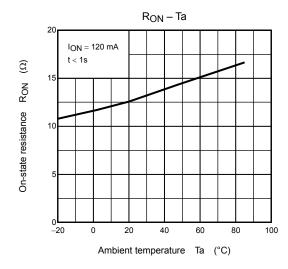


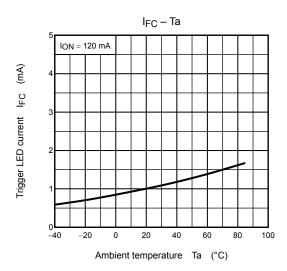


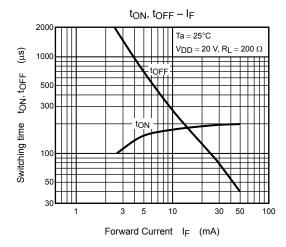


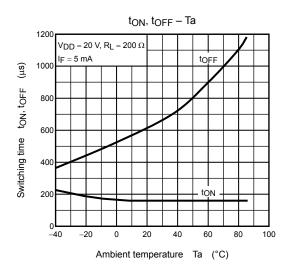


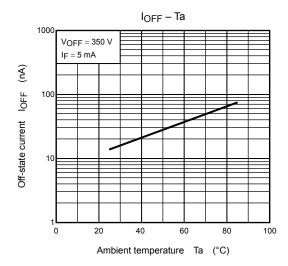












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