

TOSHIBA Photocoupler GaAs IRed & Photo-Triac

TLP525G, TLP525G-2, TLP525G-4

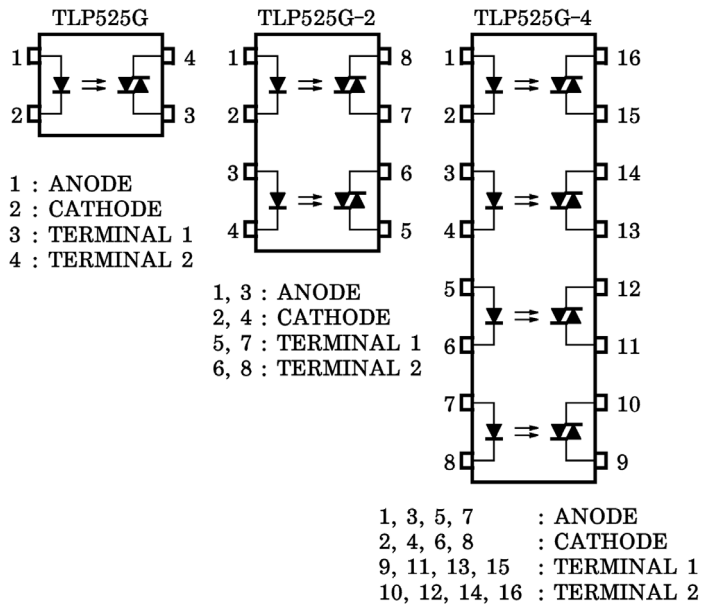
- Triac Drive
- Programmable Controllers
- AC-Output Module
- Solid State Relay

The TOSHIBA TLP525G, -2 and -4 consist of a photo-triac optically coupled to a gallium arsenide infrared emitting diode.

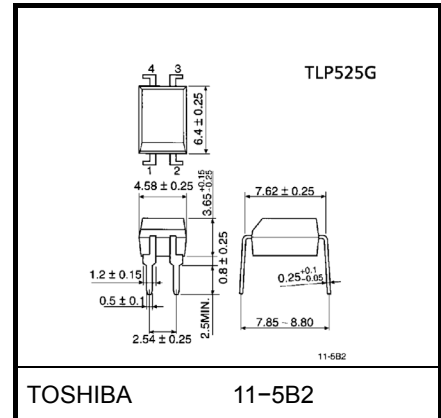
The TLP525G-2 offers two isolated channels in an eight lead plastic DIP package, while the TLP525G-4 provides four isolated channels in a sixteen lead plastic DIP package.

- Peak off-stage voltage: 400V (min.)
- Trigger LED current: 10mA (max.)
- Peak on-stage current: 2Apk (max.)
- Isolation voltage: 2500V_{rms} (min.)
- UL recognized: File no.E67349

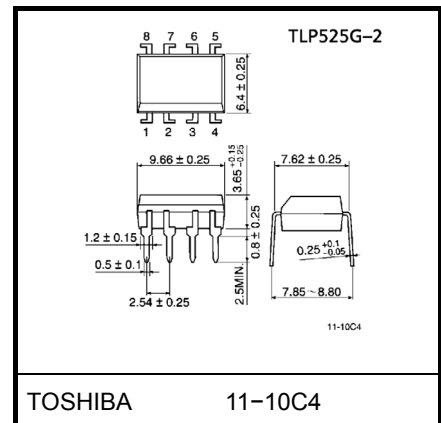
Pin Configurations (top view)



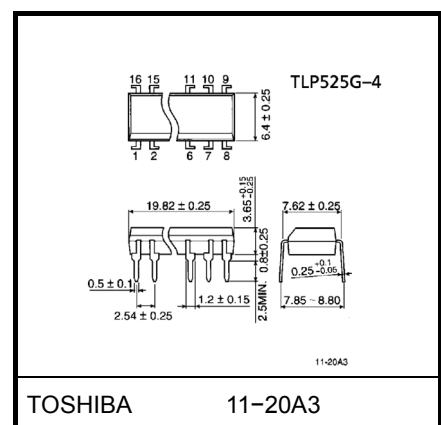
Unit in mm



Weight: 0.26g (typ.)



Weight: 0.54g (typ.)



Weight: 1.1g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating		Unit	
			TLP525G	TLP525G-2 TLP525G-4		
LED	Forward current	I_F	50	50	mA	
	Forward current derating	$I_F / ^\circ\text{C}$	-0.7 (Ta ≥ 53°C)	-0.5 (Ta ≥ 25°C)	mA / °C	
	Pulse forward current	I_{FP}	1 (100µs pulse, 100pps)		A	
	Reverse voltage	V_R	5		V	
	Junction temperature	T_j	125		°C	
Detector	Off-state output terminal voltage	V_{DRM}	400		V	
	On-state RMS current	I_T (RMS)	Ta = 25°C	100	80	mA
			Ta = 70°C	50	40	
	On-state current derating (Ta ≥ 25°C)	$I_T / ^\circ\text{C}$	-1.1	-0.9	mA / °C	
	Peak on state current	I_{TP}	2 (100µs pulse, 120pps)		A	
	Peak nonrepetitive surge current (PW = 10ms)	I_{TSM}	1.2		A	
	Junction temperature	T_j	115		°C	
Storage temperature range	T_{stg}	-55 to 125		°C		
Operating temperature range	T_{opr}	-40 to 100		°C		
Lead soldering temperature	T_{sol}	260 (10s)		°C		
Isolation voltage	(Note) BV_S	2500 (AC, 1min., R.H. ≤ 60%)		V_{rms}		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	V_{AC}	—	—	120	Vac
Forward current	I_F	15	20	25	mA
Peak on-state current	I_{TP}	—	—	1	A
Operating temperature	T_{opr}	-25	—	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

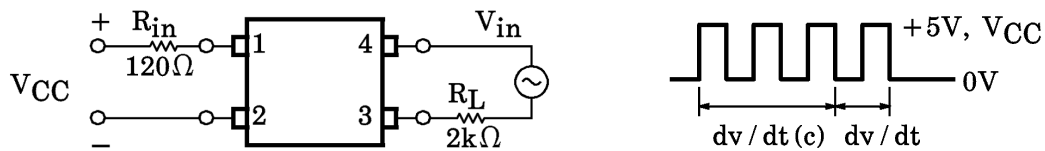
Individual Electrical Characteristics (Ta = 25°C)

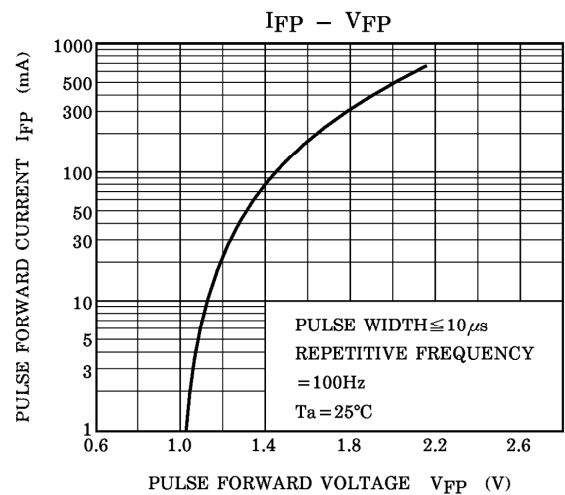
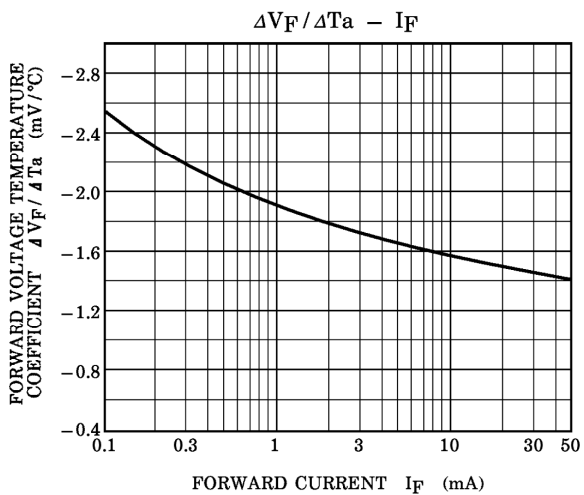
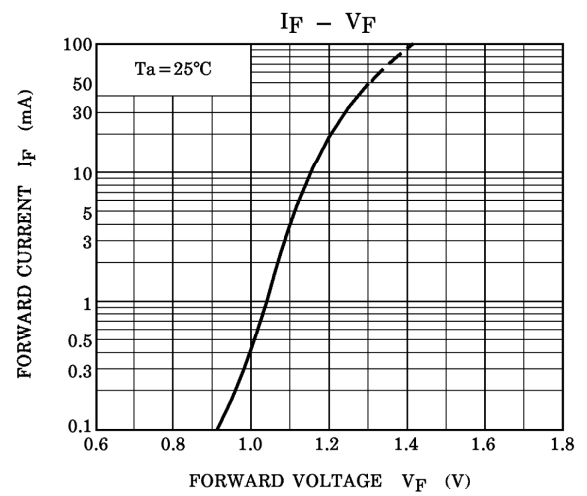
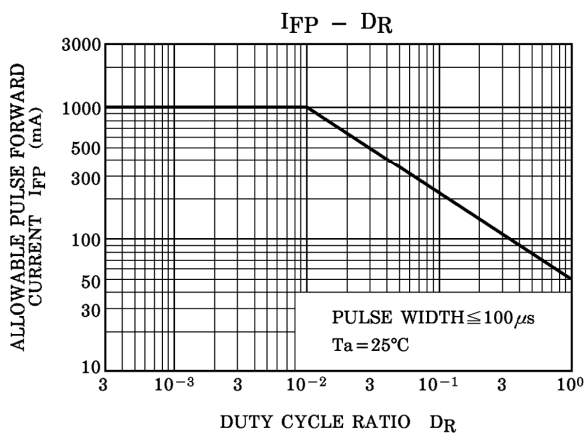
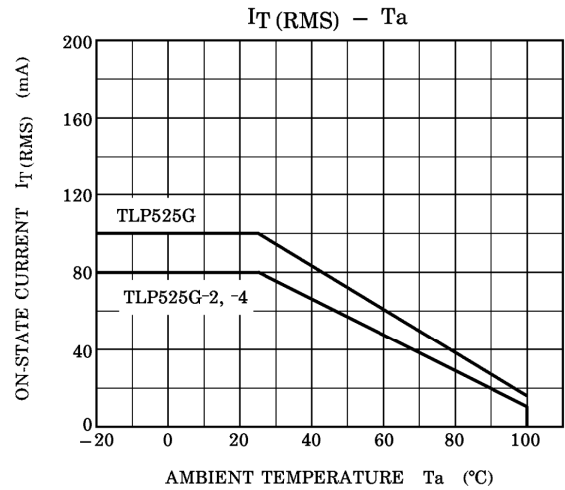
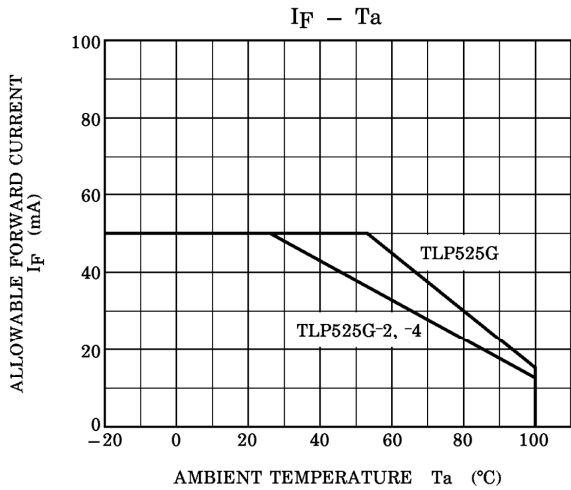
Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
LED	Forward voltage	V_F	$I_F = 10\text{mA}$	1.0	1.15	1.3	V
	Reverse current	I_R	$V_R = 5\text{V}$	—	—	10	μA
	Capacitance	C_T	$V = 0, f = 1\text{MHz}$	—	30	—	pF
Detector	Peak off-state current	I_{DRM}	$V_{DRM} = 400\text{V}$	—	10	100	nA
	Peak on-state voltage	V_{TM}	$I_{TM} = 100\text{mA}$	—	1.7	3.0	V
	Holding current	I_H	—	—	0.2	—	mA
	Critical rate of rise of off-state voltage	dv/dt	$V_{in} = 120\text{V}_{rms}, T_a = 85^\circ\text{C}$ (Figure 1)	200	500	—	V/ μs
	Critical rate of rise of commutating voltage	$dv/dt(c)$	$V_{in} = 30\text{V}_{rms}, I_T = 15\text{mA}$ (Figure 1)	—	0.2	—	V/ μs

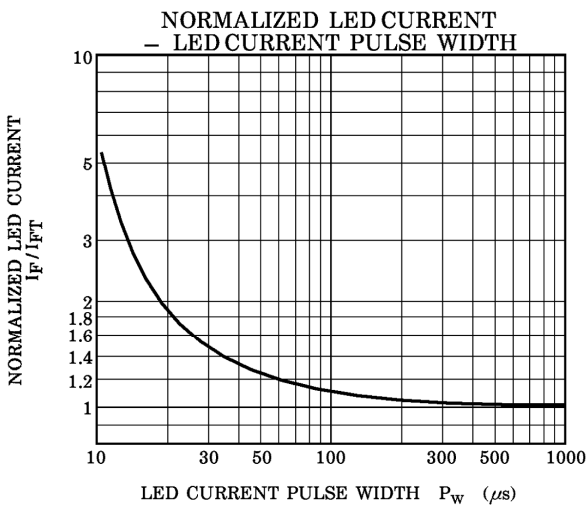
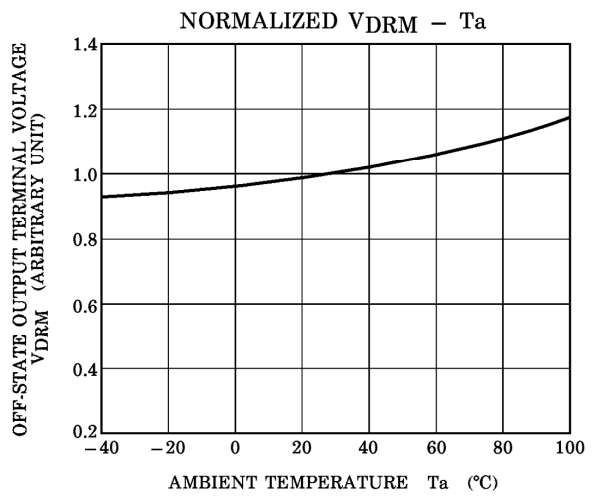
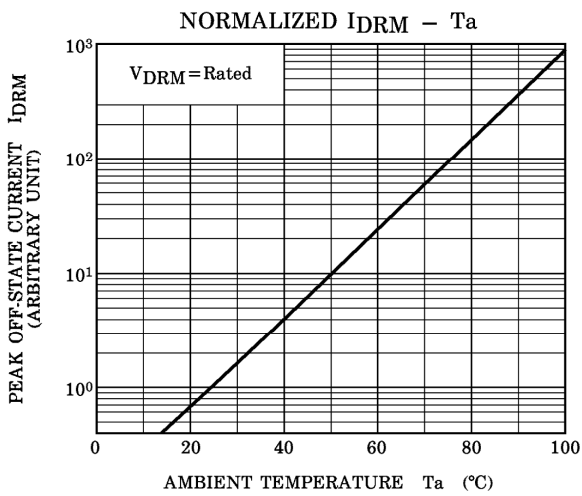
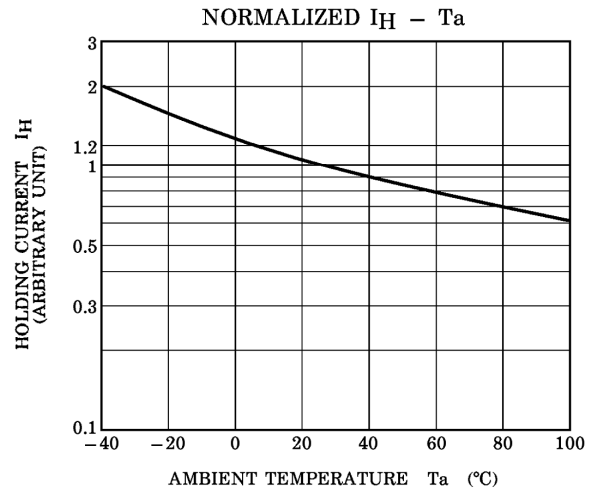
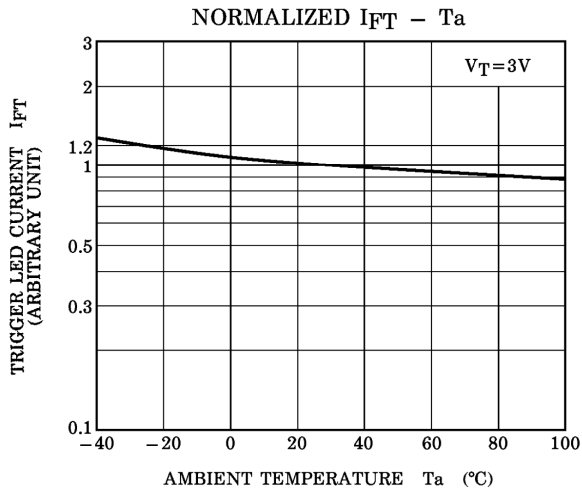
Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Trigger LED current	I_{FT}	$V_T = 3\text{V}$	—	5	10	mA
Capacitance input to output	C_S	$V_S = 0, f = 1\text{MHz}$	—	0.8	—	pF
Isolation resistance	R_S	$V_S = 500\text{V}, \text{R.H.} \leq 60\%$	5×10^{10}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 1 minute	2500	—	—	Vrms
		AC, 1 second, in oil	—	5000	—	
		DC, 1 minute, in oil	—	5000	—	Vdc

Fig.1 dv/dt Test Circuit







RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. **TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.**
- Product is intended for use in general electronics applications (e.g., computers, personal equipment, office equipment, measuring equipment, industrial robots and home electronics appliances) or for specific applications as expressly stated in this document. Product is neither intended nor warranted for use in equipment or systems that require extraordinarily high levels of quality and/or reliability and/or a malfunction or failure of which may cause loss of human life, bodily injury, serious property damage or serious public impact ("Unintended Use"). Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. Do not use Product for Unintended Use unless specifically permitted in this document.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- **ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.**
- GaAs (Gallium Arsenide) is used in Product. GaAs is harmful to humans if consumed or absorbed, whether in the form of dust or vapor. Handle with care and do not break, cut, crush, grind, dissolve chemically or otherwise expose GaAs in Product.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA assumes no liability for damages or losses occurring as a result of noncompliance with applicable laws and regulations.