SERIES: VASD0.25-SIP | **DESCRIPTION:** DC-DC CONVERTER

FEATURES

- 0.25 W isolated output
- industry standard 6 pin SIP package
- dual unregulated outputs
- 1,000 V isolation
- short circuit protection
- UL safety approvals
- wide temperature (-40~85°C)
- efficiency up to 66%

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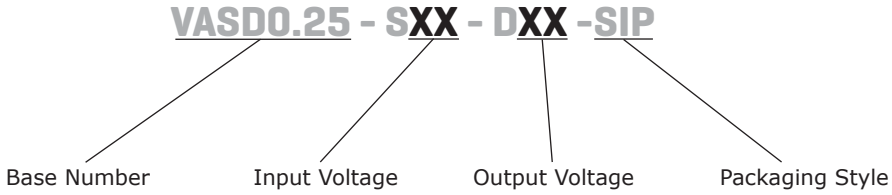
MODEL

MODEL	input voltage typ (Vdc)	input voltage range (Vdc)	output voltage (Vdc)	output current max (mA)	output power max (W)	ripple and noise[1] max (mVp-p)	efficiency typ (%)
VASD0.25-S5-D9-SIP	5	4.5~5.5	±9	±13.8	0.25	75	64
VASD0.25-S5-D15-SIP	5	4.5~5.5	±15	±8.3	0.25	75	65
VASD0.25-S12-D9-SIP	12	10.8~13.2	±9	±13.8	0.25	75	63
VASD0.25-S12-D12-SIP	12	10.8~13.2	±12	±10.4	0.25	75	64
VASD0.25-S24-D5-SIP	24	21.6~26.4	±5	±25	0.25	75	63

Notes: 1. ripple and noise are measured at 20 MHz BW
2. unbalanced load: ±5%

PART NUMBER KEY



INPUT

parameter	conditions/description	min	typ	max	units
operating input voltage	5 V model	4.5	5	5.5	Vdc
	12 V model	10.8	12	13.2	Vdc
	24 V model	21.6	24	26.4	Vdc

OUTPUT

parameter	conditions/description	min	typ	max	units
line regulation	for Vin change of 1%			1.2	%
load regulation	measured from 10% load to full load				
	5 V model		10.5	15	%
	9 V model		8.3	15	%
	12 V model		6.8	15	%
	15 V model		6.3	15	%
voltage accuracy	see derating curves				
switching frequency	100% load, input voltage range		100		kHz
temperature coefficient			±0.03		%/°C

Notes: 1. test ripple and noise by "parallel cable" method.

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection				1	s

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
isolation voltage	for 1 minute at 1 mA max.	1,000			Vdc
isolation resistance	at 500 Vdc	1,000			MΩ
safety approvals	UL 60950-1 (E222736)				
MTBF		3,500,000			hours
RoHS compliant	yes				

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature		-40		85	°C
storage temperature		-55		125	°C
storage humidity	non-condensing			95	%
temperature rise	at full load		15	25	°C
lead temperature	1.5 mm from case for 10 seconds			300	°C

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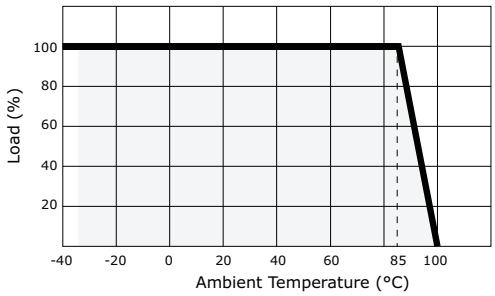
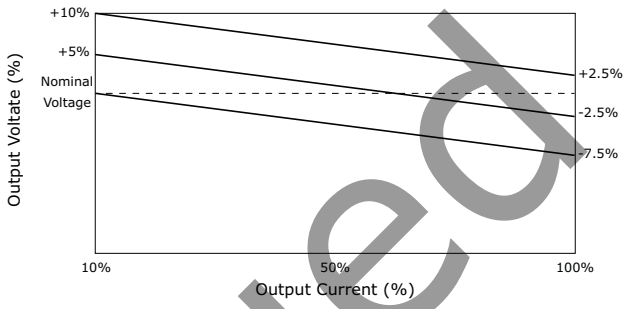
DERATING CURVES

1. output power vs. ambient temperature



2. output voltage vs. output current

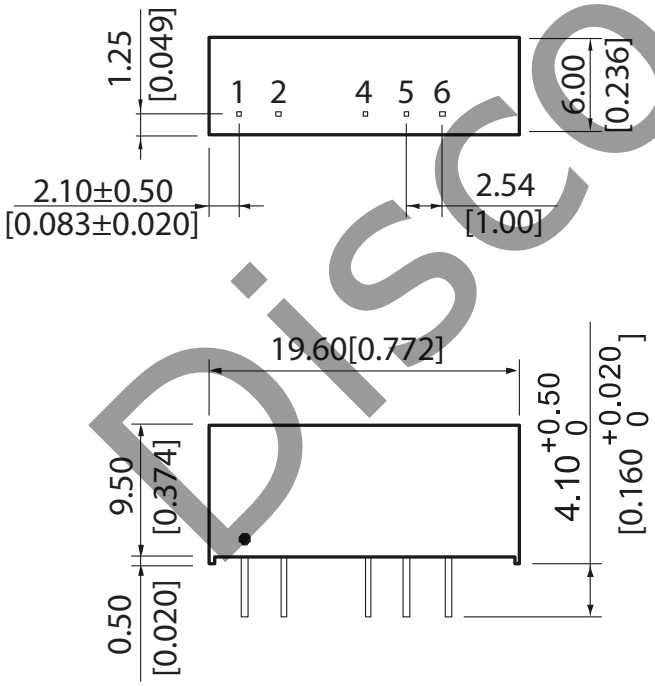


MECHANICAL

parameter	conditions/description	min	typ	max	units
dimensions	0.772 x 0.236 x 0.374 (19.60 x 6.00 x 9.50 mm)				inch
case material	plastic (UL94-V0)				
weight			2.1		g

MECHANICAL DRAWING

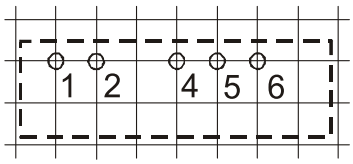
units: mm [inches]
tolerance: ±0.25 [±0.010]
pin section tolerance: ±0.10 [±0.004]



First Angle Projection

RECOMMENDED FOOTPRINT
Top view,grid:2.54mm(0.1inch), diameter:1.00mm

Dual Output



PIN CONNECTIONS	
PIN	FUNCTION
1	Vin
2	GND
4	-Vo
5	0V
6	+Vo

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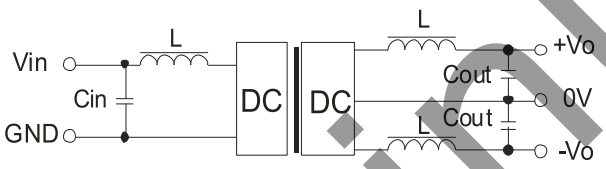
APPLICATION NOTES

1. **Requirement on Output Load**
   In order to ensure the product operates efficiently and reliably, make sure the specified range of input voltage is not exceeded and the minimum output load is not less than 10% load. If the actual load is less than the specified minimum load, the output ripple may increase sharply while its efficiency and reliability will reduce greatly. If the actual output power is very small, please add an appropriate resistor as extra loading.

2. **Overload Protection**
   Under normal operating conditions, the output circuit of these products has no protection against over-current and short-circuits. The simplest method is to connect a self-recovery fuse in series at the input end or add a circuit breaker to the circuit.

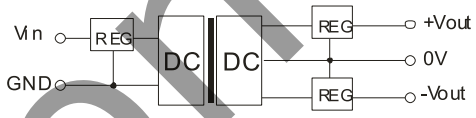
3. **Filtering**
   In some circuits which are sensitive to noise and ripple, a filtering capacitor may be added to the DC/DC output end and input end to reduce the noise and ripple. However, the capacitance of the output filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor sees the external capacitor table. To get an extremely low ripple, an "LC" filtering network may be connected to the input and output ends of the DC/DC converter, which may produce a more significant filtering effect. It should also be noted that the inductance and the frequency of the "LC" filtering network should be staggered with the DC/DC frequency to avoid mutual interference (Figure 1).

Figure 1



4. **Output Voltage Regulation and Over-voltage Protection Circuit**
   The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (Figure 2).

Figure 2



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REVISION HISTORY

rev.	description	date
1.0	initial release	02/19/2008
1.01	new template applied; V-Infinity branding removed; VASD0.25-S5-D5-SIP, VASD0.25-S5-D12-SIP, VASD0.25-S12-D5-SIP, VASD0.25-S12-D15-SIP, VASD0.25-S24-D9-SIP, VASD0.25-S24-D12-SIP and VASD0.25-S24-D15-SIP discontinued	08/06/2012
1.02	template updated	09/04/2012
1.03	discontinued	09/25/2012

The revision history provided is for informational purposes only and is believed to be accurate.

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CUI INC®

Headquarters
20050 SW 112th Ave.
Tualatin, OR 97062
800.275.4899

Fax 503.612.2383
cui.com
techsupport@cui.com

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