

40 V, 100 mA PNP general-purpose transistors Rev. 1 – 23 March 2012

Product data sheet

1. **Product profile**

1.1 General description

PNP general-purpose transistors in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

Table 1. **Product overview**

Type number	Package	Package		
	NXP	JEITA	JEDEC	
2PA1774QMB	SOT883B	-	-	2PC4617QMB
2PA1774RMB	SOT883B	-	-	2PC4617RMB
2PA1774SMB	SOT883B	-	-	-

1.2 Features and benefits

- Leadless ultra small SMD plastic Power dissipation comparable to SOT23 package
- Low package height of 0.37 mm
- AEC-Q101 qualified

1.3 Applications

- General-purpose switching and amplification
- Mobile applications

1.4 Quick reference data

Quick reference data					
Parameter	Conditions	Min	Тур	Max	Unit
collector-emitter voltage	open base	-	-	-40	V
collector current		-	-	-100	mA
DC current gain	$V_{CE} = -6 \text{ V}; \text{ I}_{C} = -1 \text{ mA}$				
2PA1774QMB		120	-	270	
2PA1774RMB		180	-	390	
2PA1774SMB		270	-	560	
	Parametercollector-emitter voltagecollector currentDC current gain2PA1774QMB2PA1774RMB	ParameterConditionscollector-emitter voltageopen basecollector currentDC current gainV _{CE} = -6 V; I _C = -1 mA2PA1774QMB2PA1774RMB	ParameterConditionsMincollector-emitter voltageopen base-collector currentDC current gain $V_{CE} = -6 \text{ V}; I_C = -1 \text{ mA}$ 1202PA1774RMB180	ParameterConditionsMinTypcollector-emitter voltageopen basecollector currentDC current gain $V_{CE} = -6 V; I_C = -1 mA$ 120-2PA1774QMB180-	ParameterConditionsMinTypMaxcollector-emitter voltageopen base40collector current100DC current gain $V_{CE} = -6 V; I_C = -1 mA$ 120-2702PA1774QMB180-390



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2. Pinning information

Pinning	
Description	Simplified outline Graphic symbol
base	
emitter	
collector	
	Transparent 2
	top view 2 sym013
	Description base emitter

3. Ordering information

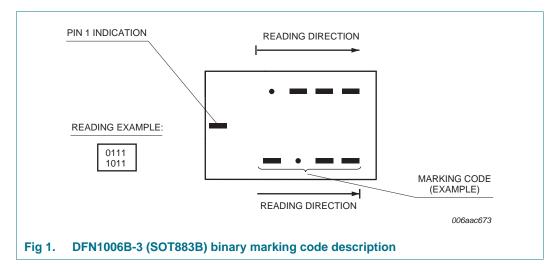
Table 4. Ordering	information		
Type number	Package		
	Name	Description	Version
2PA1774xMB series	DFN1006B-3	leadless ultra small plastic package; 3 solder lands; body $1.0 \times 0.6 \times 0.37$ mm	SOT883B

4. Marking

Table 5.Marking codes	
Type number	Marking code ^[1]
2PA1774QMB	0100 0000
2PA1774RMB	0000 1101
2PA1774SMB	0000 1110

[1] For DFN1006B-3 (SOT883B) binary marking code description see Figure 1.

4.1 Binary marking code description



2PA1774XMB_SER
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5. Limiting values

Table 6. In accordar	Limiting values ace with the Absolute Maximu	ım Rating System (1EC 60	134).		
Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-40	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-100	mA
I _{CM}	peak collector current	single pulse; $t_p \leq 1 \text{ ms}$		-	-200	mA
I _{BM}	peak base current	single pulse; $t_p \leq 1 \text{ ms}$		-	-100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	[1][2]	-	250	mW
			[3][2]	-	590	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	+150	°C
T _{stg}	storage temperature			-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

[3] Device mounted on an FR4 PCB, single-sided copper, mounting pad for collector 1 cm².

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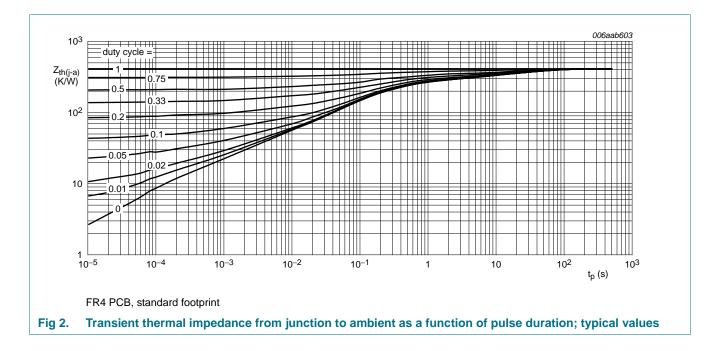
6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	<u>[1][2]</u>	-	500	K/W
	junction to ambient		[3][2]	-	212	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Reflow soldering is the only recommended soldering method.

[3] Device mounted on an FR4 PCB, single-sided copper, mounting pad for collector 1 cm².



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7. Characteristics

		pecified.				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
000	collector-base	$V_{CB} = -30 \text{ V}; \text{ I}_{E} = 0 \text{ A}$	-	-	-100	nA
	cut-off current	$\label{eq:VCB} \begin{array}{l} V_{CB} = -30 \ V; \ I_{E} = 0 \ A; \\ T_{j} = 150 \ ^{\circ}C \end{array}$	-	-	-5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -4 \text{ V; } I_C = 0 \text{ A}$	-	-	-100	nA
h _{FE}	DC current gain	$V_{CE} = -6 \text{ V}; \text{ I}_{C} = -1 \text{ mA}$				
2PA1774QN	2PA1774QMB		120	-	270	
	2PA1774RMB		180	-	390	
	2PA1774SMB		270	-	560	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -50 \text{ mA}; I_B = -5 \text{ mA}$	<u>[1]</u> _	-	-200	mV
f _T	transition frequency	$V_{CE} = -12 \text{ V}; I_C = -2 \text{ mA};$ f = 100 MHz	100	-	-	MHz
C _c	collector capacitance	$\label{eq:VCB} \begin{array}{l} V_{CB} = -12 \ V; \ I_E = i_e = 0 \ A; \\ f = 1 \ MHz \end{array}$	-	-	2.2	pF

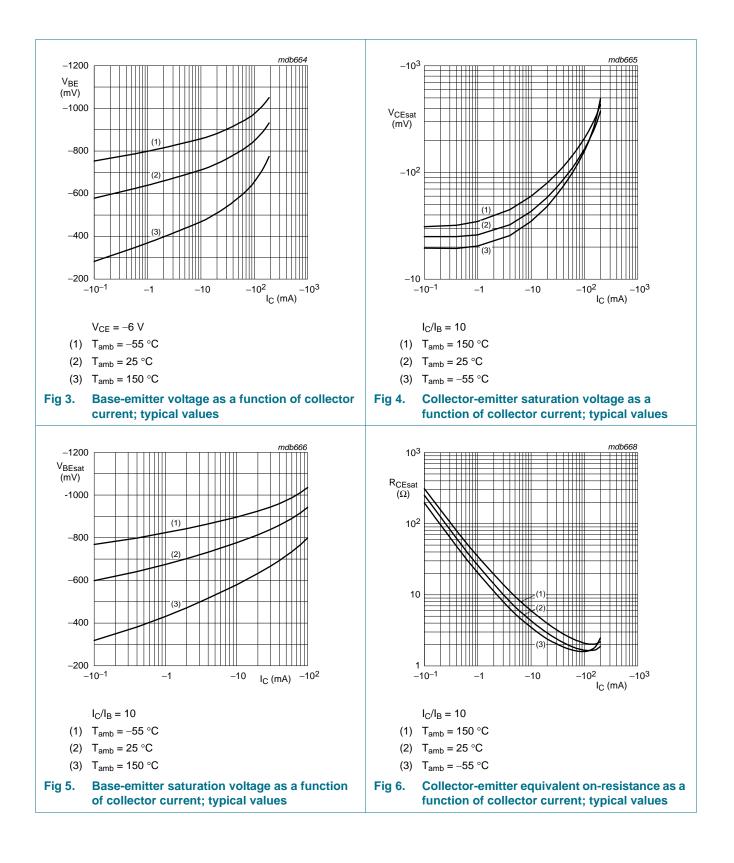
[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

2PA1774XMB_SER

NXP Semiconductors

2PA1774xMB series

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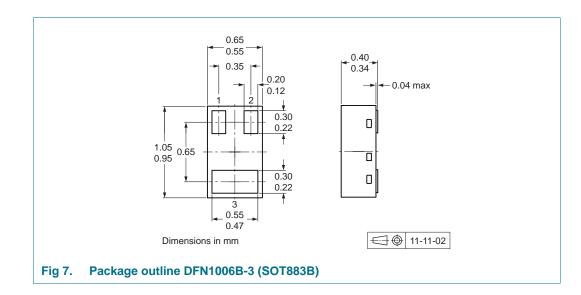
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8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



10. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

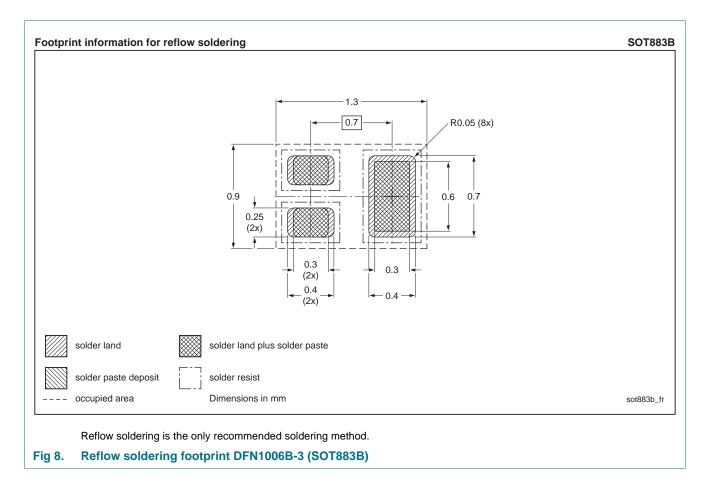
		0	0	
Type number	e number Package Description			Packing quantity
				10000
2PA1774xMB series	DFN1006B-3 (SOT883B)	2 mm pitch, 8 mm ta	pe and reel	-315

[1] For further information and the availability of packing methods, see Section 14.

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11. Soldering



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12. Revision history

Table 10. Revision hist	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
2PA1774XMB_SER v.1	20120323	Product data sheet	-	-

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13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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