Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

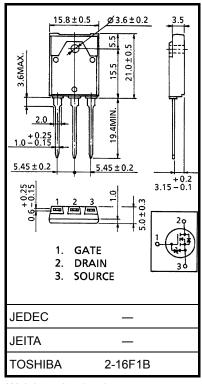
2SK2917

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance : $R_{DS}(ON) = 0.21 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 17 \text{ S (typ.)}$
- Low leakage current : I_{DSS} = 100 µA (max) (V_{DS} = 500 V)
- Enhancement mode : V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	500	V	
Drain-gate voltage (R	_{GS} = 20 kΩ)	V _{DGR}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	18	А	
	Pulse (Note 1)	I _{DP}	72	A	
Drain power dissipation	n (Ta = 25°C)	PD	90	W	
Single pulse avalanche energy (Note 2)		E _{AS}	915	mJ	
Avalanche current		I _{AR}	18	А	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	9	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch–c)}	1.39	°C / W	
Thermal resistance, channel to ambient	R _{th (ch–a)}	41.6	°C / W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.8 mH, R_G = 25 Ω , I_{AR} = 18 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

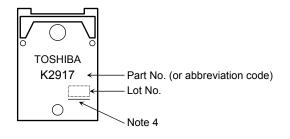
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	_	_	±10	μA
Gate-source bro	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	—	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	—	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 10 A	_	0.21	0.27	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 10 A	10	17	_	S
Input capacitance	e	C _{iss}			3720	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	340	_	
Output capacitance		C _{oss}		_	1165	_	
Switching time	Rise time	tr	$I_{D} = 10.0 \text{ A}$ $V_{GS_{0}V} \int_{U} \int$	_	30	_	
	Turn-on time	t _{on}		_	70		ns
	Fall time	t _f		_	50	_	
	Turn-off time	t _{off}		_	290		
Total gate charge (gate-source plus gate-drain)		Qg			80		
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 18 A		48	_	nC
Gate-drain ("miller") Charge		Q _{gd}			32	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	18	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	72	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 18 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 18 A, V _{GS} = 0 V		540		ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 A / μs		5.4		μC

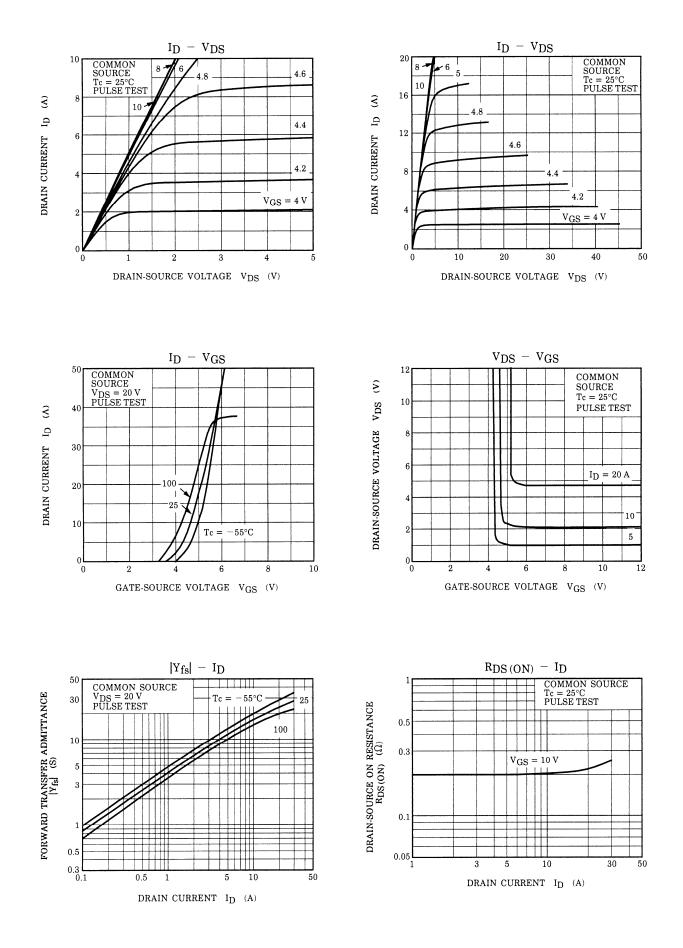
Marking



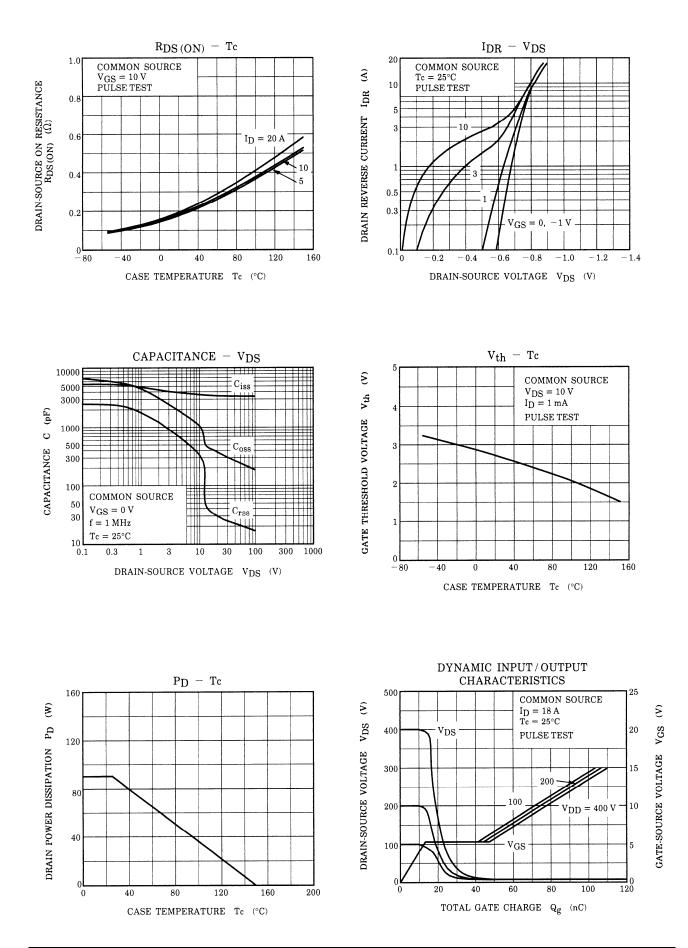
Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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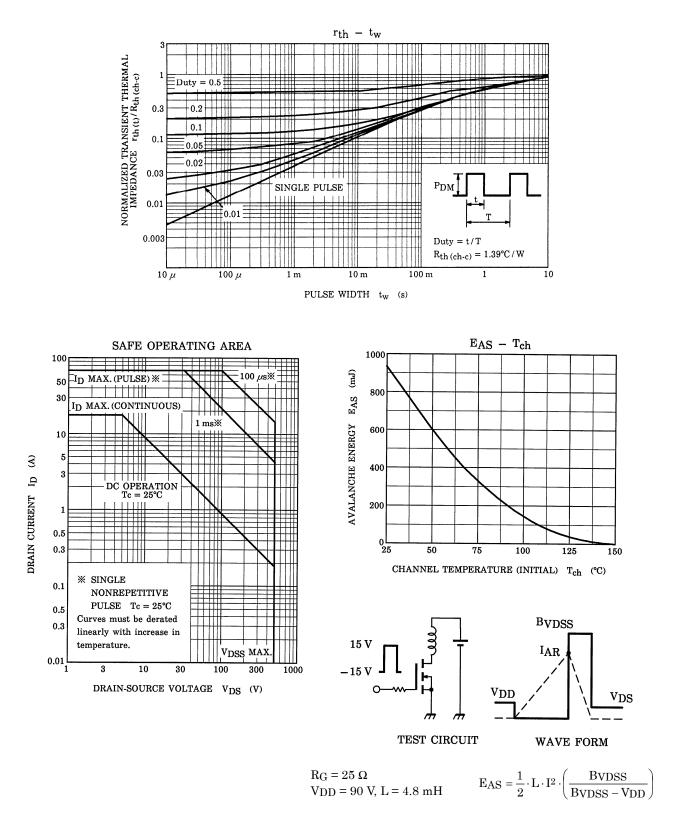
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