MOSFETs Silicon N-channel MOS (U-MOSⅧ-H)

TPH5R906NH

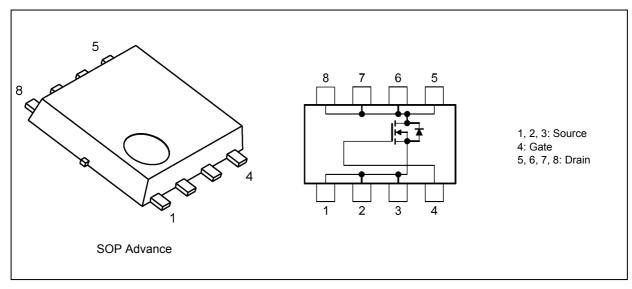
1. Applications

- Switching Voltage Regulators
- Motor Drivers
- DC-DC Converters

2. Features

- (1) Small footprint due to a small and thin package
- (2) High-speed switching
- (3) Small gate charge: $Q_{SW} = 18 \text{ nC}$ (typ.)
- (4) Low drain-source on-resistance: $R_{DS(ON)}$ = 4.8 m Ω (typ.) (V_{GS} = 10 V)
- (5) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 60 \ V)$
- (6) Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 0.3 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics				Rating	Unit
Drain-source voltage			V _{DSS}	60	V
Gate-source voltage			V _{GSS}	±20	
Drain current (DC)	(Silicon limit)	(Note 1), (Note 2)	I _D	71	A
Drain current (DC)		(Note 1)	Ι _D	28	
Drain current (pulsed)	(t = 1 ms)	(Note 1)	I _{DP}	84	
Power dissipation	(T _c = 25°C)		PD	57	W
Power dissipation	(t = 10 s)	(Note 3)	PD	2.8	W
Power dissipation	(t = 10 s)	(Note 4)	PD	1.6	W
Single-pulse avalanche energy		(Note 5)	E _{AS}	124	mJ
Avalanche current			I _{AR}	28	A
Channel temperature			T _{ch}	150	°C
Storage temperature			T _{stg}	-55 to 150]

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics				Max	Unit
Channel-to-case thermal resistance	(T _c = 25°C)		R _{th(ch-c)}	2.19	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R _{th(ch-a)}	44.6	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 4)	R _{th(ch-a)}	78.1	°C/W

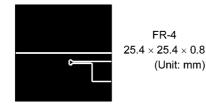
Note 1: Ensure that the channel temperature does not exceed 150°C.

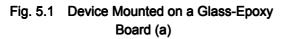
Note 2: Limited by silicon capability. Package limit is 60 A.

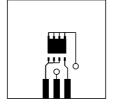
Note 3: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 4: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 5: V_DD = 24 V, T_ch = 25°C (initial), L = 0.22 mH, R_G = 1 Ω , I_AR = 28 A







 $\begin{array}{c} \text{FR-4}\\ \text{25.4}\times\text{25.4}\times\text{0.8}\\ \text{(Unit: mm)} \end{array}$

Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

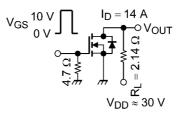
6.1. Static Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	_		±0.1	μA
Drain cut-off current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_		10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	60	_	—	V
Drain-source breakdown voltage (Note 6)	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	45	_	—	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 0.3 mA	2.0	_	4.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 6.5 V, I _D = 14 A	_	6.4	14	mΩ
		V _{GS} = 10 V, I _D = 14 A		4.8	5.9	

Note 6: If a reverse bias is applied between gate and source, this device enters V_{(BR)DSX} mode. Note that the drainsource breakdown voltage is lowered in this mode.

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 30 V, V _{GS} = 0 V, f = 1 MHz		2340	3100	pF
Reverse transfer capacitance	C _{rss}]		50	95	
Output capacitance	C _{oss}]		745	_	
Gate resistance	r _g	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		1.0	1.5	Ω
Switching time (rise time)	tr	See Figure 6.2.1.		9.9	_	ns
Switching time (turn-on time)	t _{on}]		22	_	
Switching time (fall time)	t _f]		9.0	_	
Switching time (turn-off time)	t _{off}]		27	_	



Duty \leq 1%, $t_W^{}=$ 10 μs

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

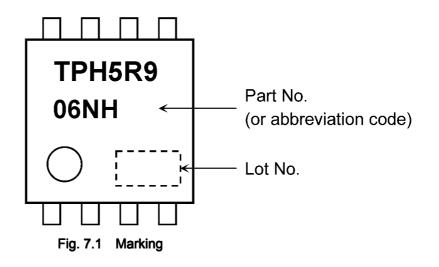
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 48$ V, V_{GS} = 10 V, I_D = 28 A	—	38	—	nC
Gate-source charge 1	Q _{gs1}			12	_	
Gate-drain charge	Q _{gd}		_	12	_	
Gate switch charge	Q _{SW}			18	_	

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

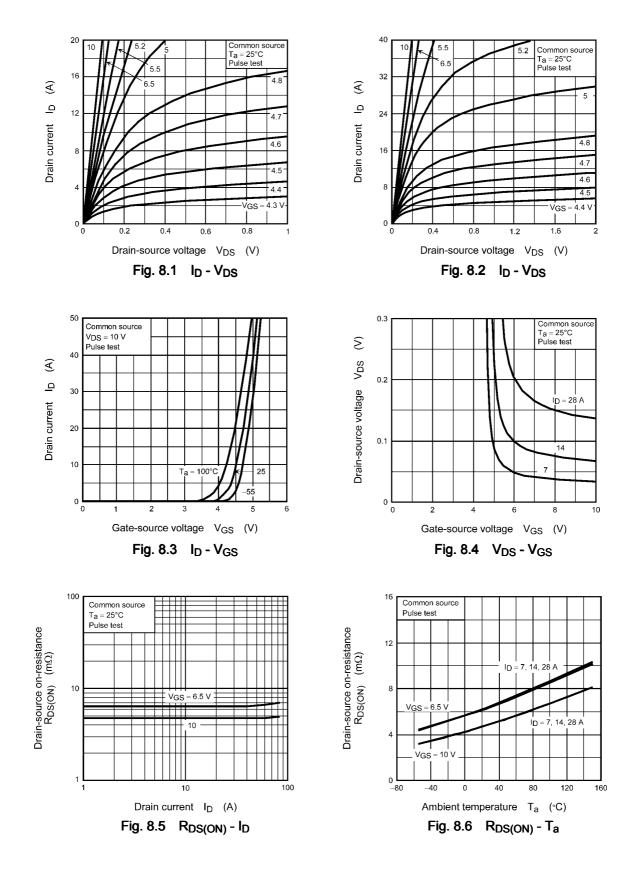
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed) (Not	e 7) I _{DRP}	—	_	_	84	А
Diode forward voltage	V _{DSF}	I _{DR} = 28 A, V _{GS} = 0 V	—		-1.2	V

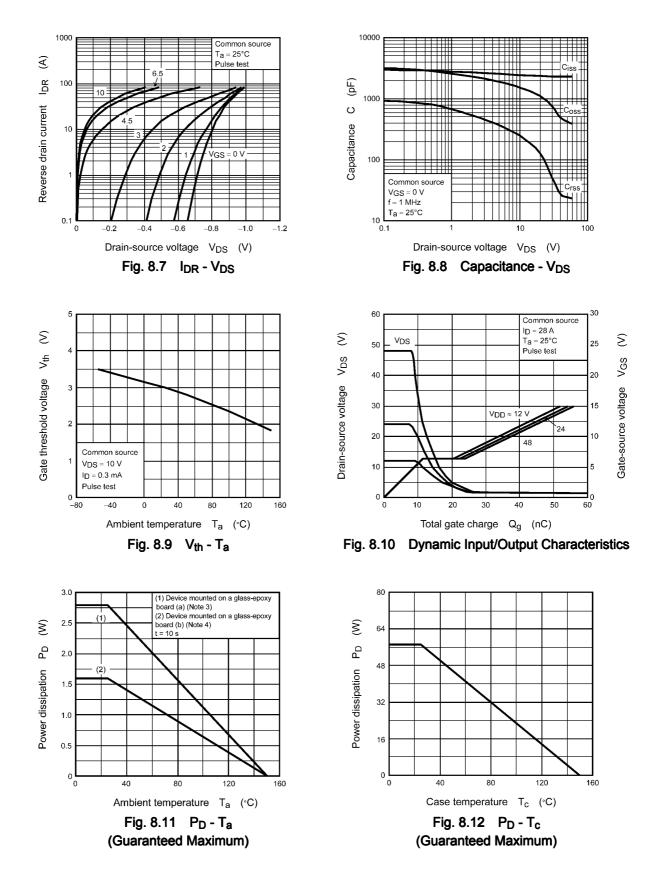
Note 7: Ensure that the channel temperature does not exceed 150°C.

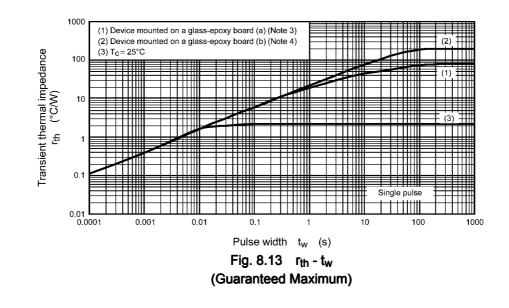
7. Marking

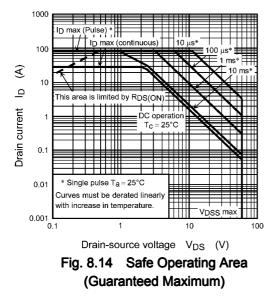


8. Characteristics Curves (Note)









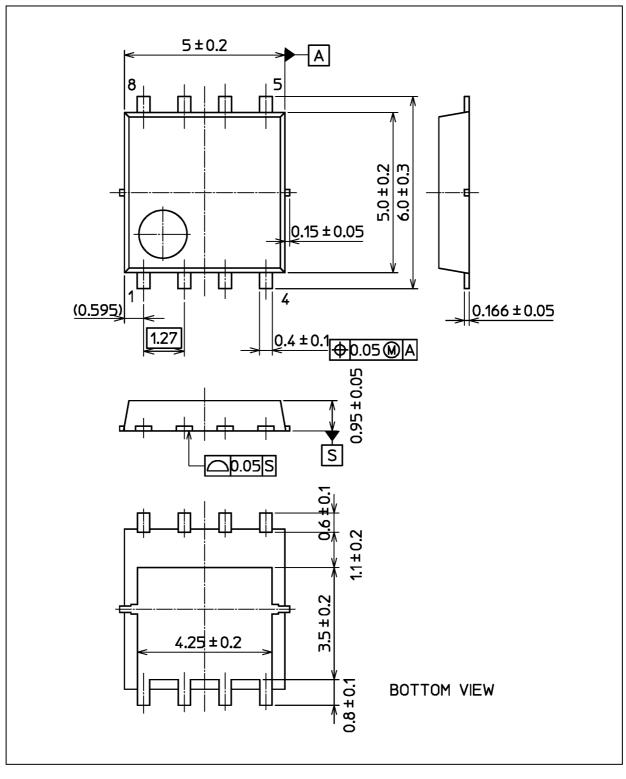
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



TPH5R906NH

Package Dimensions

Unit: mm



Weight: 0.069 g (typ.)

Package Name(s)

TOSHIBA: 2-5Q1S

Nickname: SOP Advance

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