Bus buffer/line driver; 3-state Rev. 1 — 1 October 2012

#### **General description** 1.

The 74LVC1G126-Q100 provides one non-inverting buffer/line driver with 3-state output. The 3-state output is controlled by the output enable input (OE). A LOW-level at pin OE causes the output to assume a high-impedance OFF-state.

The input can be driven from either 3.3 V or 5 V devices. This feature allows the use of this device in a mixed 3.3 V and 5 V environment.

This device is fully specified for partial power-down applications using IOFF. The IOFF circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

#### 2. **Features and benefits**

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
  - Specified from –40 °C to +85 °C and from –40 °C to +125 °C
- Wide supply voltage range from 1.65 V to 5.5 V
- High noise immunity
- Complies with JEDEC standard:
  - JESD8-7 (1.65 V to 1.95 V)
  - JESD8-5 (2.3 V to 2.7 V)
  - JESD8B/JESD36 (2.7 V to 3.6 V)
- $\pm 24$  mA output drive (V<sub>CC</sub> = 3.0 V)
- ESD protection:
  - MIL-STD-883, method 3015 exceeds 2000 V
  - HBM JESD22-A114F exceeds 2000 V
  - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- CMOS low power consumption
- Inputs accept voltages up to 5 V
- Latch-up performance exceeds 250 mA
- Direct interface with TTL levels
- Multiple package options



## 3. Ordering information

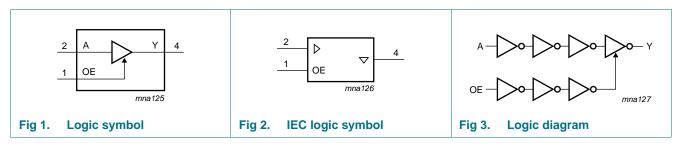
Table 1.         Ordering information							
Type number	Package						
	Temperature range	Name	Description	Version			
74LVC1G126GW-Q100	–40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			
74LVC1G126GV-Q100	–40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753			

### 4. Marking

Type number	Marking <sup>[1]</sup>
74LVC1G126GW-Q100	VN
74LVC1G126GV-Q100	V26

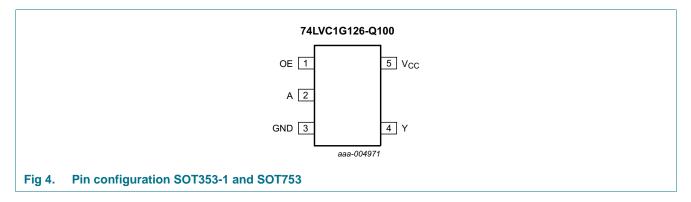
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

## 5. Functional diagram



## 6. Pinning information

### 6.1 Pinning



74LVC1G126\_Q100

### 6.2 Pin description

Table 3.	Pin description	
Symbol	Pin	Description
OE	1	output enable input
A	2	data input
GND	3	ground (0 V)
Y	4	data output
V <sub>CC</sub>	5	supply voltage

## 7. Functional description

#### Table 4. Function table<sup>[1]</sup>

Input OE A		Output
OE	Α	Y
н	L	L
Н	Н	Н
L	Х	Z

[1] H = HIGH voltage level;

L = LOW voltage level;

X = don't care;

Z = high-impedance OFF-state.

## 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

			•	10	,
Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+6.5	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V	-50	-	mA
VI	input voltage		<u>[1]</u> –0.5	+6.5	V
I <sub>OK</sub>	output clamping current	$V_{O} > V_{CC}$ or $V_{O} < 0 V$	-	±50	mA
Vo	output voltage	Active mode	<u>[1][2]</u> –0.5	V <sub>CC</sub> + 0.5	V
		Power-down mode	<u>[1][2]</u> –0.5	+6.5	V
Ι <sub>Ο</sub>	output current	$V_{O} = 0 V$ to $V_{CC}$	-	±50	mA
I <sub>CC</sub>	supply current		-	100	mA
I <sub>GND</sub>	ground current		-100	-	mA
P <sub>tot</sub>	total power dissipation	$T_{amb}$ = -40 °C to +125 °C	<u>[3]</u>	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When  $V_{CC} = 0 V$  (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For TSSOP5 and SC-74A packages: above 87.5 °C the value of Ptot derates linearly with 4.0 mW/K.

## 9. Recommended operating conditions

Table 6.	Recommended operating condi	tions				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CC</sub>	supply voltage		1.65	-	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	Active mode	0	-	V <sub>CC</sub>	V
		V <sub>CC</sub> = 0 V; Power-down mode	0	-	5.5	V
T <sub>amb</sub>	ambient temperature		-40	-	+125	°C
$\Delta t / \Delta V$	input transition rise and fall rate	$V_{CC}$ = 1.65 V to 2.7 V	-	-	20	ns/V
		$V_{CC}$ = 2.7 V to 5.5 V	-	-	10	ns/V

## **10. Static characteristics**

#### Table 7.Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ <mark>[1]</mark>	Max	Unit
T <sub>amb</sub> = -	-40 °C to +85 °C					
VIH	HIGH-level input voltage	V <sub>CC</sub> = 1.65 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.7	-	-	V
		$V_{CC}$ = 2.7 V to 3.6 V	2.0	-	-	V
		$V_{CC}$ = 4.5 V to 5.5 V	$0.7\times V_{CC}$	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 1.65 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		$V_{CC}$ = 2.3 V to 2.7 V	-	-	0.7	V
		$V_{CC}$ = 2.7 V to 3.6 V	-	-	0.8	V
		$V_{CC}$ = 4.5 V to 5.5 V	-	-	$0.3\times V_{CC}$	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$V_{CC}$ = 1.65 V to 5.5 V; $I_O$ = 100 $\mu A$	-	-	0.1	V
		V <sub>CC</sub> = 1.65 V; I <sub>O</sub> = 4 mA	-	-	0.45	V
		$V_{CC} = 2.3 \text{ V}; I_{O} = 8 \text{ mA}$	-	-	0.3	V
		$V_{CC} = 2.7 \text{ V}; I_{O} = 12 \text{ mA}$	-	-	0.4	V
		$V_{CC} = 3.0 \text{ V}; \text{ I}_{O} = 24 \text{ mA}$	-	-	0.55	V
		$V_{CC} = 4.5 \text{ V}; \text{ I}_{O} = 32 \text{ mA}$	-	-	0.55	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$V_{CC}$ = 1.65 V to 5.5 V; $I_O$ = –100 $\mu A$	$V_{CC}-0.1$	-	-	V
		$V_{CC} = 1.65 \text{ V}; I_{O} = -4 \text{ mA}$	1.2	-	-	V
		$V_{CC} = 2.3 \text{ V}; I_{O} = -8 \text{ mA}$	1.9	-	-	V
		$V_{CC} = 2.7 \text{ V}; I_{O} = -12 \text{ mA}$	2.2	-	-	V
		$V_{CC} = 3.0 \text{ V}; \text{ I}_{O} = -24 \text{ mA}$	2.3	-	-	V
		$V_{CC} = 4.5 \text{ V}; \text{ I}_{O} = -32 \text{ mA}$	3.8	-	-	V
I <sub>I</sub>	input leakage current	$V_{CC}$ = 0 V to 5.5 V; $V_{I}$ = 5.5 V or GND	-	±0.1	±5	μA
I <sub>OZ</sub>	OFF-state output current	$V_{CC} = 3.6 \text{ V}; V_{I} = V_{IH} \text{ or } V_{IL};$ $V_{O} = 5.5 \text{ V or GND}$	-	±0.1	±10	μΑ

Bus buffer/line driver; 3-state

Symbol	Parameter	Conditions	Min	Typ <mark>[1]</mark>	Max	Unit
I <sub>OFF</sub>	power-off leakage current	$V_{CC} = 0 \text{ V}; \text{ V}_{I} \text{ or } \text{ V}_{O} = 5.5 \text{ V}$	-	±0.1	±10	μA
I <sub>CC</sub>	supply current	$V_{I} = 5.5 V \text{ or GND};$ $V_{CC} = 1.65 V \text{ to } 5.5 V; I_{O} = 0 \text{ A}$	-	0.1	10	μΑ
∆l <sub>CC</sub>	additional supply current	per pin; $V_{CC} = 2.3 \text{ V}$ to 5.5 V; $V_I = V_{CC} - 0.6 \text{ V}$ ; $I_O = 0 \text{ A}$	-	5	500	μA
Cı	input capacitance		-	5	-	pF
T <sub>amb</sub> =	40 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 1.65 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.7	-	-	V
		$V_{CC} = 2.7 \text{ V to } 3.6 \text{ V}$	2.0	-	-	V
		$V_{CC} = 4.5 V \text{ to } 5.5 V$	$0.7\times V_{CC}$	-	-	V
VIL	LOW-level input voltage	V <sub>CC</sub> = 1.65 V to 1.95 V	-	-	$0.35\times V_{CC}$	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	-	0.7	V
		$V_{CC} = 2.7 \text{ V to } 3.6 \text{ V}$	-	-	0.8	V
		$V_{CC}$ = 4.5 V to 5.5 V	-	-	$0.3\times V_{CC}$	V
V <sub>OL</sub>	LOW-level output voltage	$V_I = V_{IH} \text{ or } V_{IL}$				
		$V_{CC}$ = 1.65 V to 5.5 V; $I_O$ = 100 $\mu A$	-	-	0.1	V
		V <sub>CC</sub> = 1.65 V; I <sub>O</sub> = 4 mA	-	-	0.70	V
		$V_{CC} = 2.3 \text{ V}; I_{O} = 8 \text{ mA}$	-	-	0.45	V
		$V_{CC} = 2.7 \text{ V}; I_{O} = 12 \text{ mA}$	-	-	0.60	V
		$V_{CC} = 3.0 \text{ V}; I_{O} = 24 \text{ mA}$	-	-	0.80	V
		$V_{CC} = 4.5 \text{ V}; I_{O} = 32 \text{ mA}$	-	-	0.80	V
V <sub>OH</sub>	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$V_{CC}$ = 1.65 V to 5.5 V; $I_O$ = $-100~\mu A$	$V_{CC}-0.1$	-	-	V
		$V_{CC}$ = 1.65 V; I <sub>O</sub> = -4 mA	0.95	-	-	V
		$V_{CC} = 2.3 \text{ V}; \text{ I}_{O} = -8 \text{ mA}$	1.7	-	-	V
		$V_{CC} = 2.7 \text{ V}; I_{O} = -12 \text{ mA}$	1.9	-	-	V
		$V_{CC} = 3.0 \text{ V}; \text{ I}_{O} = -24 \text{ mA}$	2.0	-	-	V
		$V_{CC}$ = 4.5 V; I <sub>O</sub> = -32 mA	3.4	-	-	V
l <sub>l</sub>	input leakage current	$V_{CC}$ = 0 V to 5.5 V; $V_{I}$ = 5.5 V or GND	-	-	±100	μA
l <sub>oz</sub>	OFF-state output current	$V_{CC}$ = 3.6 V; $V_{I}$ = $V_{IH}$ or $V_{IL}$ ; $V_{O}$ = 5.5 V or GND	-	-	±200	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{CC}$ = 0 V; V <sub>I</sub> or V <sub>O</sub> = 5.5 V	-	-	±200	μΑ
I <sub>CC</sub>	supply current	$V_I = 5.5 V \text{ or GND};$ $V_{CC} = 1.65 V \text{ to } 5.5 V; I_O = 0 A$	-	-	200	μA
Δl <sub>CC</sub>	additional supply current	per pin; $V_{CC} = 2.3 \text{ V}$ to 5.5 V; $V_I = V_{CC} - 0.6 \text{ V}$ ; $I_O = 0 \text{ A}$	-	-	5000	μΑ

#### Table 7. Static characteristics ...continued

[1] All typical values are measured at V\_{CC} = 3.3 V and T\_{amb} = 25 °C.

Bus buffer/line driver; 3-state

## **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V). For test circuit see Figure 7.

Symbol	Parameter	Conditions		-40	–40 °C to +85 °C			–40 °C to +125 °C	
				Min	Typ <mark>[1]</mark>	Max	Min	Max	
t <sub>pd</sub>	propagation delay	A to Y; see Figure 5	[2]						
		$V_{CC}$ = 1.65 V to 1.95 V		1.0	3	8.0	1.0	10.5	ns
		$V_{CC}$ = 2.3 V to 2.7 V		0.5	2.1	5.5	0.5	7	ns
		$V_{CC} = 2.7 V$		0.5	2.3	5.5	0.5	7	ns
		$V_{CC}$ = 3.0 V to 3.6 V		0.5	2.0	4.5	0.5	6	ns
		$V_{CC}$ = 4.5 V to 5.5 V		0.5	1.7	4.0	0.5	5.5	ns
t <sub>en</sub>	enable time	OE to Y; see Figure 6	[3]						
		$V_{CC}$ = 1.65 V to 1.95 V		1.0	3.2	9.4	1.0	12	ns
		$V_{CC}$ = 2.3 V to 2.7 V		0.5	2.2	6.6	0.5	8.5	ns
		$V_{CC} = 2.7 V$		0.5	2.4	6.6	0.5	8.5	ns
		$V_{CC}$ = 3.0 V to 3.6 V		0.5	2.1	5.3	0.5	7	ns
		$V_{CC}$ = 4.5 V to 5.5 V		0.5	1.6	5.0	0.5	6.5	ns
t <sub>dis</sub>	disable time	OE to Y; see Figure 6	[4]						
		$V_{CC}$ = 1.65 V to 1.95 V		1.0	4.3	9.2	1.0	12	ns
		$V_{CC}$ = 2.3 V to 2.7 V		0.5	2.7	5.5	0.5	7	ns
		$V_{CC} = 2.7 V$		0.5	3.4	5.5	0.5	7	ns
		$V_{CC}$ = 3.0 V to 3.6 V		0.5	3.0	5.5	0.5	7	ns
		$V_{CC}$ = 4.5 V to 5.5 V		0.5	2.2	4.2	0.5	5.5	ns
C <sub>PD</sub>	power dissipation	per buffer; $V_I$ = GND to $V_{CC}$	[5]						
	capacitance	output enabled		-	25	-	-	-	pF
		output disabled		-	6	-	-	-	pF

[1] Typical values are measured at  $T_{amb}$  = 25 °C and  $V_{CC}$  = 1.8 V, 2.5 V, 2.7 V, 3.3 V and 5.0 V respectively.

[2]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ 

[3]  $t_{en} \mbox{ is the same as } t_{PZH} \mbox{ and } t_{PZL}$ 

 $[4] \quad t_{dis} \text{ is the same as } t_{PLZ} \text{ and } t_{PHZ}$ 

[5]  $C_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).

 $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} \times N + \sum (C_{L} \times V_{CC}^{2} \times f_{o}) \text{ where:}$ 

 $f_i = input frequency in MHz;$ 

 $f_o = output frequency in MHz;$ 

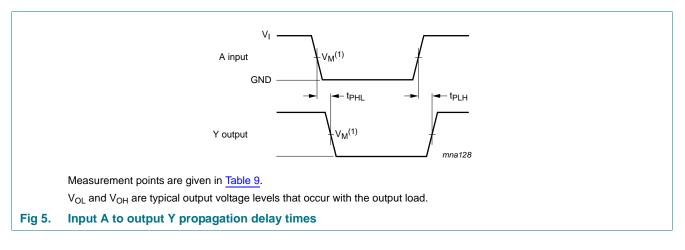
 $C_L$  = output load capacitance in pF;

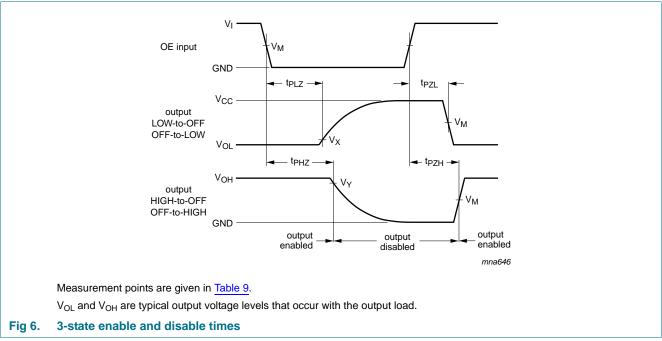
 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;  $\Sigma(C_L \times V_{CC}^2 \times f_o) = \text{sum of outputs.}$ 

Bus buffer/line driver; 3-state

## 12. Waveforms



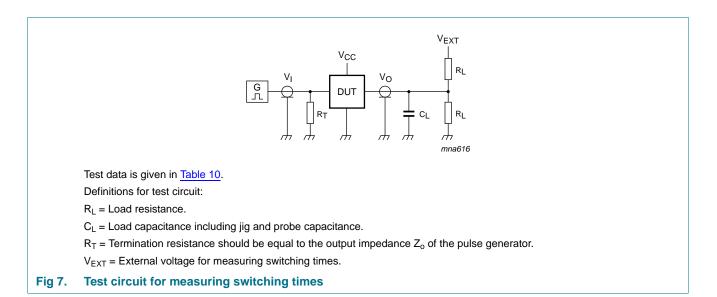


#### Table 9.Measurement points

Supply voltage	Input	Output				Output		
V <sub>CC</sub>	V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>	V <sub>Y</sub>				
1.65 V to 1.95 V	0.5V <sub>CC</sub>	$0.5V_{CC}$	V <sub>OL</sub> + 0.15 V	V <sub>OH</sub> – 0.15 V				
2.3 V to 2.7 V	$0.5V_{CC}$	$0.5V_{CC}$	V <sub>OL</sub> + 0.15 V	V <sub>OH</sub> – 0.15 V				
2.7 V	1.5 V	1.5 V	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> – 0.3 V				
3.0 V to 3.6 V	1.5 V	1.5 V	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> – 0.3 V				
4.5 V to 5.5 V	$0.5V_{CC}$	$0.5V_{CC}$	V <sub>OL</sub> + 0.3 V	V <sub>OH</sub> – 0.3 V				

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#### Bus buffer/line driver; 3-state

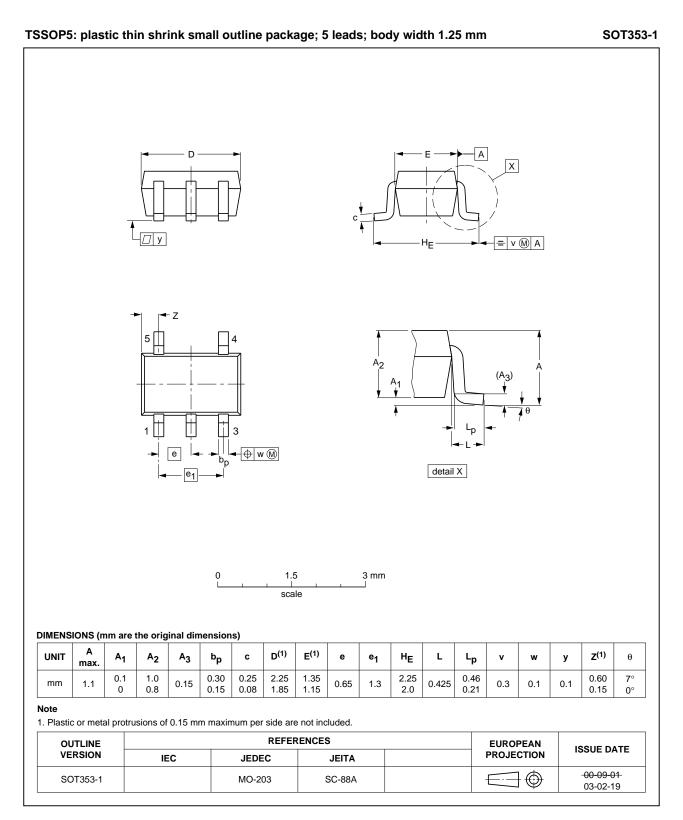


#### Table 10. Test data

Supply voltage	Input	nput		Load		V <sub>EXT</sub>		
V <sub>CC</sub>	VI	t <sub>r</sub> , t <sub>f</sub>	CL	RL	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>	
1.65 V to 1.95 V	V <sub>CC</sub>	$\leq$ 2.0 ns	30 pF	1 kΩ	open	GND	2V <sub>CC</sub>	
2.3 V to 2.7 V	V <sub>CC</sub>	$\leq$ 2.0 ns	30 pF	500 Ω	open	GND	2V <sub>CC</sub>	
2.7 V	2.7 V	$\leq$ 2.5 ns	50 pF	500 Ω	open	GND	6 V	
3.0 V to 3.6 V	2.7 V	$\leq$ 2.5 ns	50 pF	500 Ω	open	GND	6 V	
4.5 V to 5.5 V	V <sub>CC</sub>	$\leq$ 2.5 ns	50 pF	500 Ω	open	GND	$2V_{CC}$	

Bus buffer/line driver; 3-state

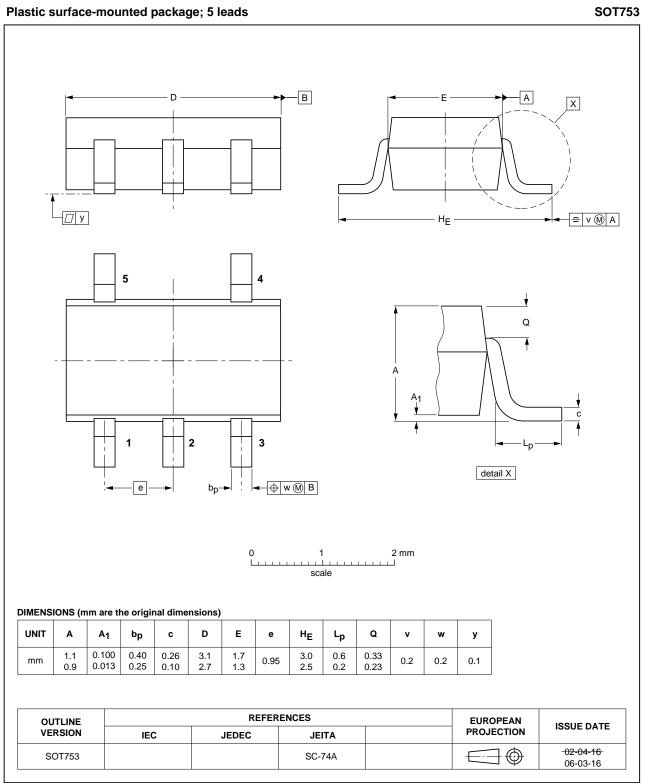
## 13. Package outline



#### Fig 8. Package outline SOT353-1 (TSSOP5)

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74LVC1G126\_Q100



Package outline SOT753 (SC-74A) Fig 9.

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Bus buffer/line driver; 3-state

## 14. Abbreviations

Table 11.	Abbreviations		
Acronym	Description		
CMOS	Complementary Metal Oxide Semiconductor		
DUT	Device Under Test		
ESD	ElectroStatic Discharge		
HBM	Human Body Model		
MM	Machine Model		
TTL	Transistor-Transistor Logic		
MIL	Military		

## **15. Revision history**

Table 12. Revision hist	e 12. Revision history							
Document ID	Release date	Data sheet status	Change notice	Supersedes				
74LVC1G126_Q100 v.1	20121001	Product data sheet	-	-				

## **16. Legal information**

#### 16.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <a href="http://www.nxp.com">http://www.nxp.com</a>.

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