Product data sheet

1. Product profile

1.1 General description

The BB173LX is a variable capacitance diode, fabricated in planar technology, and encapsulated in the SOD882D (DFN1006D-2) ultra small leadless SMD plastic package.

1.2 Features and benefits

- Excellent linearity
- Ultra small leadless SMD package
- $C_{d(28V)} = 2.6 \text{ pF}$; $C_{d(1V)}$ to $C_{d(28V)}$ ratio = 15
- Low series resistance

1.3 Applications

■ Voltage Controlled Oscillators (VCO)

2. Pinning information

Table 1. Pinning

| Pin | Description | Simplified outline | Symbol |
|-----|-------------|----------------------|--------|
| 1 | cathode | [1] | _IL |
| 2 | anode | Transparent top view | sym008 |

^[1] The marking bar indicates the cathode.

3. Ordering information

Table 2. Ordering information

| Type number Package | | | |
|---------------------|------------|---|---------|
| | Name | Description | Version |
| BB173LX | DFN1006D-2 | leadless ultra small plastic package; 2 terminals; body 1 \times 0.6 \times 0.4 | SOD882D |



VHF variable capacitance diode

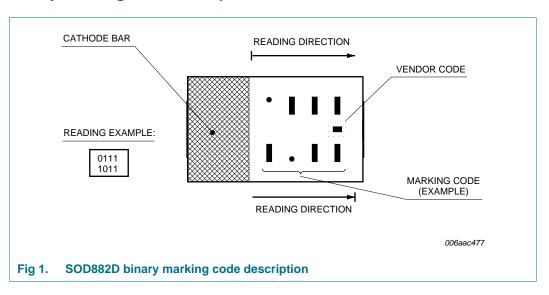
4. Marking

Table 3. Marking codes

| Type number | Marking code [1] |
|-------------|------------------|
| BB173LX | 1000 |
| | 1001 |

^[1] For SOD882D binary marking code description, see Figure 1.

4.1 Binary marking code description



5. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions Min | Max | Unit |
|------------------|----------------------|----------------|------|------|
| V_{R} | reverse voltage | - | 32 | V |
| I _F | forward current | - | 20 | mΑ |
| T _{stg} | storage temperature | -55 | +150 | °C |
| Tj | junction temperature | - 55 | +125 | °C |

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6. Characteristics

 Table 5.
 Characteristics

 $T_i = 25$ °C unless otherwise specified.

| Symbol | Parameter | Conditions | | Min | Typ | Max | Unit |
|--|--|---|-----|--------|------|-------|------|
| Syllibol | Parameter | Conditions | | IVIIII | Тур | IVIAX | Unit |
| I_R | reverse current | $V_R = 30 \text{ V}$ | [1] | - | - | 10 | nA |
| | | $V_R = 30 \text{ V}; T_j = 85 ^{\circ}\text{C}$ | [1] | - | - | 200 | nA |
| r _s | diode series resistance | $f = 100 \text{ MHz}; C_d = 30 \text{ pF}$ | [2] | - | 0.7 | - | Ω |
| C_d | diode capacitance | f = 1 MHz | [3] | | | | |
| | | V _R = 1 V | | 34.65 | - | 42.35 | pF |
| | | V _R = 28 V | | 2.36 | 2.6 | 2.75 | pF |
| $C_{d(1V)}/C_{d(2V)}$ | diode capacitance ratio (1 V to 2 V) | f = 1 MHz | | - | 1.3 | - | |
| $C_{d(1V)}/C_{d(28V)}$ | diode capacitance ratio (1 V to 28 V) | f = 1 MHz | | 13.5 | 15 | - | |
| C _{d(25V)} /C _{d(28V)} | diode capacitance ratio (25 V to 28 V) | f = 1 MHz | | - | 1.08 | - | |

^[1] See Figure 4.

^[3] See Figure 2 and Figure 5.

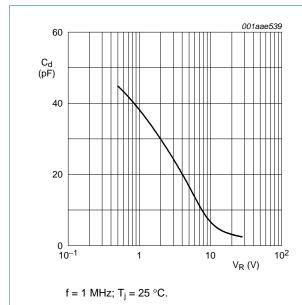


Fig 2. Diode capacitance as a function of reverse voltage; typical values

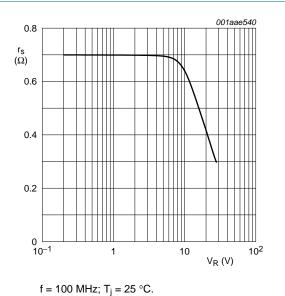


Fig 3. Diode series resistance as a function of reverse voltage; typical values

^[2] See Figure 3.

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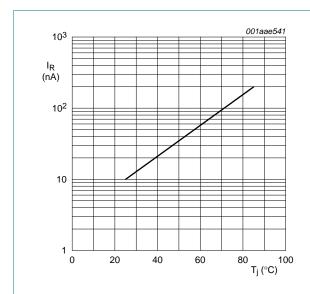


Fig 4. Reverse current as a function of junction temperature; maximum values

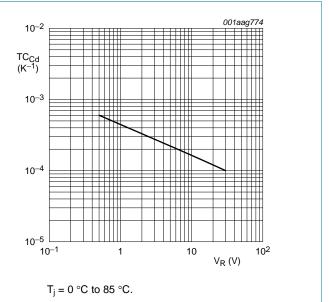


Fig 5. Diode capacitance temperature coefficient as a function of reverse voltage; typical values

7. Package outline

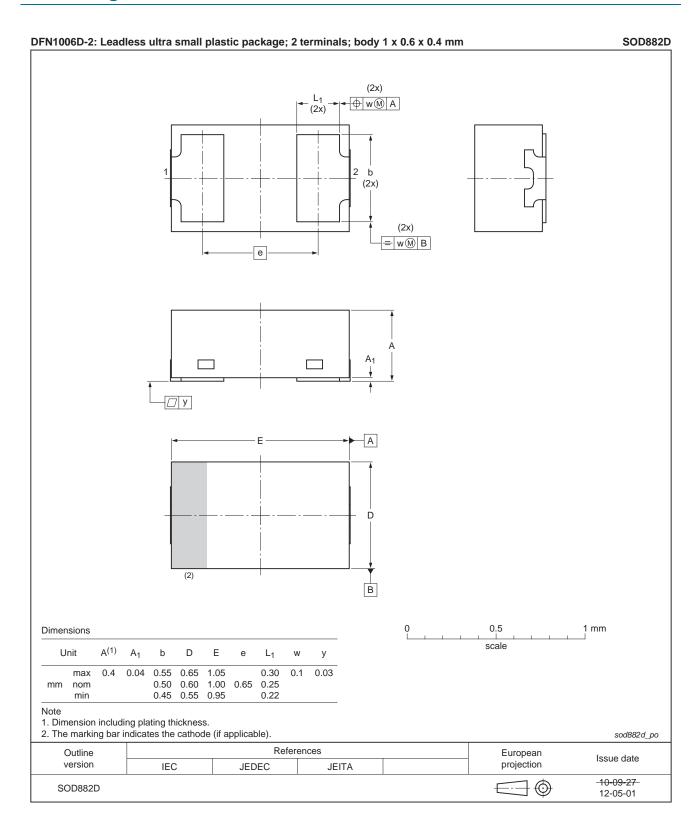


Fig 6. Package outline SOD882D (DFN1006D-2)

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8. Abbreviations

Table 6. Abbreviations

| Acronym | Description |
|---------|------------------------|
| SMD | Surface Mounted Device |
| VHF | Very High Frequency |

9. Revision history

Table 7. Revision history

| Document ID | Release date | Data sheet status | Change notice | Supersedes |
|-------------|--------------|--------------------|---------------|------------|
| BB173LX v.1 | 20130325 | Product data sheet | - | - |

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10. Legal information

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|--------------------------------|-------------------|---|
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- [2] The term 'short data sheet' is explained in section "Definitions"
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