



# BUK7215-55A

## N-channel TrenchMOS standard level FET

Rev. 02 — 27 January 2011

Product data sheet

## 1. Product profile

### 1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

### 1.2 Features and benefits

- AEC Q101 compliant
- Low conduction losses due to low on-state resistance
- Suitable for standard level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

### 1.3 Applications

- 12 V and 24 V loads
- Automotive and general purpose power switching
- Motors, lamps and solenoids

### 1.4 Quick reference data

Table 1. Quick reference data

| Symbol                        | Parameter                        | Conditions  | Min                   | Typ | Max | Unit |
|-------------------------------|----------------------------------|---|-----------------------|-----|-----|------|
| $V_{DS}$                      | drain-source voltage             | $T_j \geq 25\text{ °C}$ ; $T_j \leq 175\text{ °C}$  | -                     | -   | 55  | V    |
| $I_D$                         | drain current                    | $V_{GS} = 10\text{ V}$ ; $T_{mb} = 25\text{ °C}$ ; see <a href="#">Figure 3</a> ; see <a href="#">Figure 1</a>                      | <a href="#">[1]</a> - | -   | 62  | A    |
| $P_{tot}$                     | total power dissipation          | $T_{mb} = 25\text{ °C}$ ; see <a href="#">Figure 2</a>  | -                     | -   | 115 | W    |
| <b>Static characteristics</b> |                                  |   |                       |     |     |      |
| $R_{DS(on)}$                  | drain-source on-state resistance | $V_{GS} = 10\text{ V}$ ; $I_D = 25\text{ A}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 11</a> ; see <a href="#">Figure 12</a> | -                     | 13  | 15  | mΩ   |



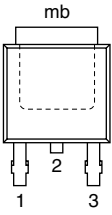
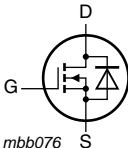
Table 1. Quick reference data ...continued

| Symbol                         | Parameter                                    | Conditions   | Min | Typ | Max | Unit |
|--------------------------------|--|--|-----|-----|-----|------|
| <b>Avalanche Ruggedness</b>    |  |  |     |     |     |      |
| $E_{DS(AL)S}$                  | non-repetitive drain-source avalanche energy | $I_D = 62\text{ A}$ ; $V_{sup} \leq 55\text{ V}$ ;<br>$R_{GS} = 50\ \Omega$ ; $V_{GS} = 10\text{ V}$ ;<br>$T_{j(init)} = 25\text{ }^\circ\text{C}$ ; unclamped | -   | -   | 211 | mJ   |
| <b>Dynamic characteristics</b> |  |  |     |     |     |      |
| $Q_{GD}$                       | gate-drain charge                            | $V_{GS} = 10\text{ V}$ ; $I_D = 25\text{ A}$ ;<br>$V_{DS} = 44\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$ ;<br>see <a href="#">Figure 13</a>                 | -   | 19  | -   | nC   |

[1] Current is limited by power dissipation chip rating.

## 2. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description                       | Simplified outline   | Graphic symbol   |
|-----|--------|-----------------------------------|--|--|
| 1   | G      | gate                              |  |  |
| 2   | D      | drain                             |  |  |
| 3   | S      | source                            |  |  |
| mb  | D      | mounting base; connected to drain |  |  |

SOT428 (DPAK)

## 3. Ordering information

Table 3. Ordering information

| Type number | Package |   |         |
|-------------|---------|---|---------|
|             | Name    | Description   | Version |
| BUK7215-55A | DPAK    | plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped) | SOT428  |

## 4. Limiting values

**Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter               | Conditions  | Min                   | Max | Unit |
|------------------|-------------------------|---|-----------------------|-----|------|
| V <sub>DS</sub>  | drain-source voltage    | T <sub>j</sub> ≥ 25 °C; T <sub>j</sub> ≤ 175 °C                                       | -                     | 55  | V    |
| V <sub>DGR</sub> | drain-gate voltage      | R <sub>GS</sub> = 20 kΩ   | -                     | 55  | V    |
| V <sub>GS</sub>  | gate-source voltage     |   | -20                   | 20  | V    |
| I <sub>D</sub>   | drain current           | T <sub>mb</sub> = 100 °C; V <sub>GS</sub> = 10 V; see <a href="#">Figure 1</a>        | <a href="#">[1]</a> - | 44  | A    |
|                  |                         | T <sub>mb</sub> = 25 °C; V <sub>GS</sub> = 10 V; see <a href="#">Figure 3</a> ;       | <a href="#">[1]</a> - | 62  | A    |
|                  |                         | see <a href="#">Figure 1</a>  | <a href="#">[2]</a> - | 55  | A    |
| I <sub>DM</sub>  | peak drain current      | T <sub>mb</sub> = 25 °C; pulsed; t <sub>p</sub> ≤ 10 μs; see <a href="#">Figure 3</a> | -                     | 248 | A    |
| P <sub>tot</sub> | total power dissipation | T <sub>mb</sub> = 25 °C; see <a href="#">Figure 2</a>                                 | -                     | 115 | W    |
| T <sub>stg</sub> | storage temperature     |   | -55                   | 175 | °C   |
| T <sub>j</sub>   | junction temperature    |   | -55                   | 175 | °C   |

### Source-drain diode

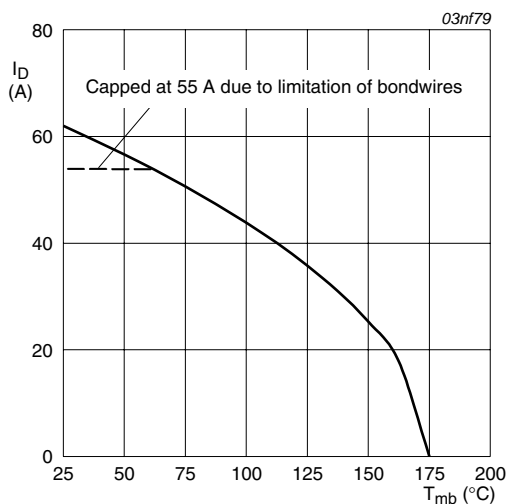
|                 |                     |   |     |   |     |   |
|-----------------|---------------------|---|-----|---|-----|---|
| I <sub>S</sub>  | source current      | T <sub>mb</sub> = 25 °C                                 | [1] | - | 62  | A |
|                 |                     |   | [2] | - | 55  | A |
| I <sub>SM</sub> | peak source current | pulsed; t <sub>p</sub> ≤ 10 μs; T <sub>mb</sub> = 25 °C | -   |   | 248 | A |

### Avalanche Ruggedness

|               |  |  |   |     |    |
|---------------|--|--|---|-----|----|
| $E_{DS(AL)S}$ | non-repetitive drain-source avalanche energy | $I_D = 62\text{ A}$ ; $V_{sup} \leq 55\text{ V}$ ; $R_{GS} = 50\text{ }\Omega$ ; $V_{GS} = 10\text{ V}$ ; $T_{j(init)} = 25\text{ }^{\circ}\text{C}$ ; unclamped | - | 211 | mJ |
|---------------|--|--|---|-----|----|

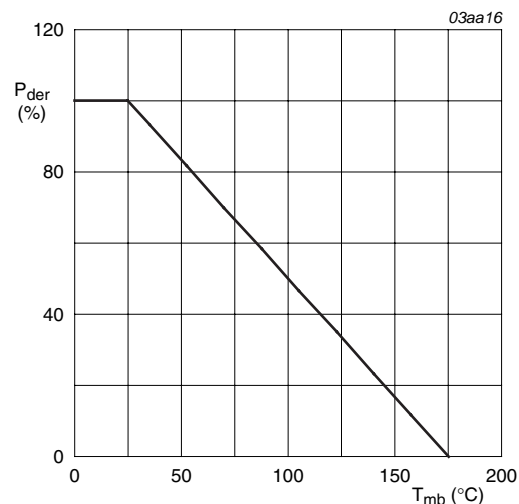
[1] Current is limited by power dissipation chip rating.

[2] Continuous current is limited by bond wires.



$$I_{der} = \frac{I_D}{I_{D(25^{\circ}\text{C})}} \times 100\%$$

**Fig 1. Continuous drain current as a function of mounting base temperature**



$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}\text{C})}} \times 100\%$$

**Fig 2. Normalized total power dissipation as a function of mounting base temperature**

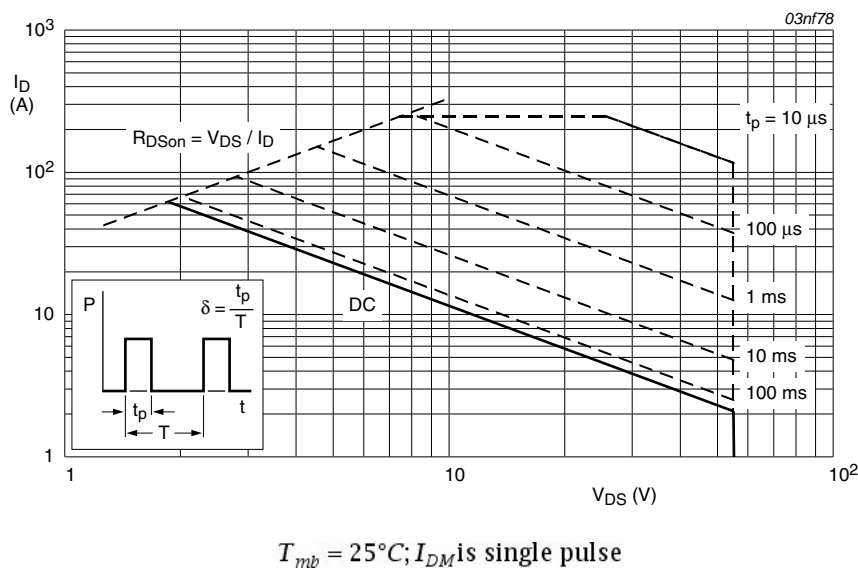


Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

5. Thermal characteristics

Table 5. Thermal characteristics

| Symbol         | Parameter   | Conditions                                    | Min | Typ  | Max | Unit |
|----------------|---|---|-----|------|-----|------|
| $R_{th(j-mb)}$ | thermal resistance from junction to mounting base | see <a href="#">Figure 4</a>                  | -   | -    | 1.3 | K/W  |
| $R_{th(j-a)}$  | thermal resistance from junction to ambient       | minimum footprint ; FR4 printed circuit board | -   | 71.4 | -   | K/W  |

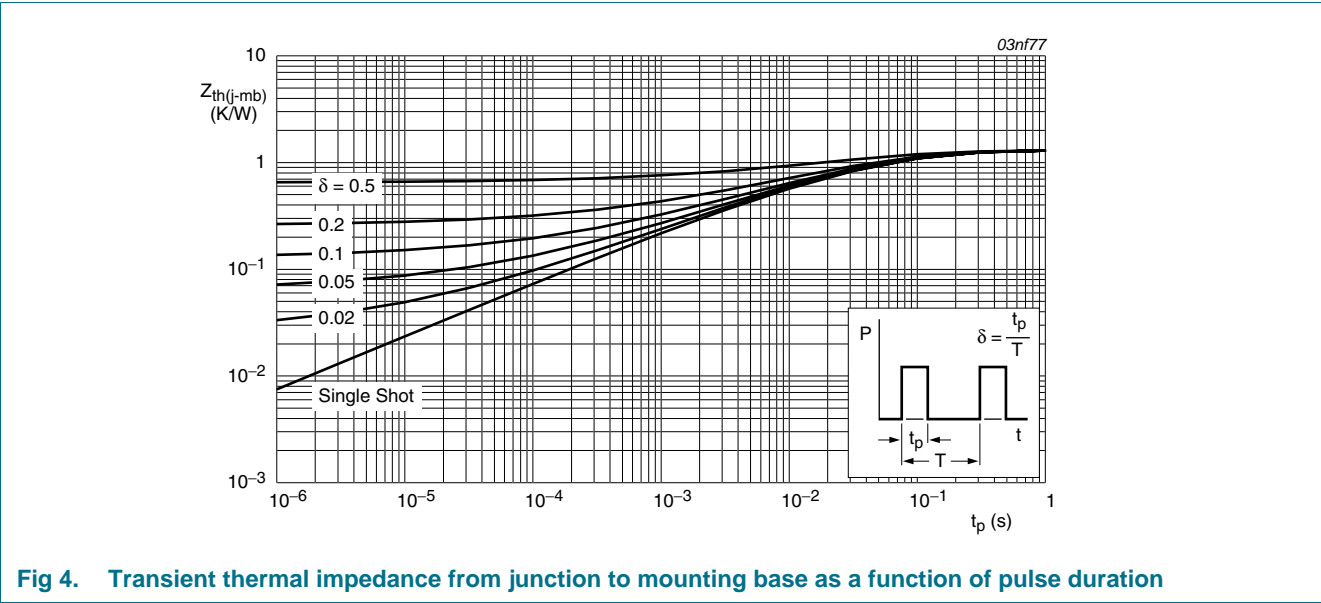


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration

## 6. Characteristics

Table 6. Characteristics

| Symbol                  | Parameter                        | Conditions  | Min | Typ  | Max  | Unit |
|-------------------------|----------------------------------|---|-----|------|------|------|
| Static characteristics  |                                  |   |     |      |      |      |
| V <sub>(BR)DSS</sub>    | drain-source breakdown voltage   | I <sub>D</sub> = 0.25 mA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C   | 55  | -    | -    | V    |
|                         |                                  | I <sub>D</sub> = 0.25 mA; V <sub>GS</sub> = 0 V; T <sub>j</sub> = -55 °C  | 50  | -    | -    | V    |
| V <sub>GS(th)</sub>     | gate-source threshold voltage    | I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 25 °C; see <a href="#">Figure 10</a>                      | 2   | 3    | 4    | V    |
|                         |                                  | I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = 175 °C; see <a href="#">Figure 10</a>                     | 1   | -    | -    | V    |
|                         |                                  | I <sub>D</sub> = 1 mA; V <sub>DS</sub> = V <sub>GS</sub> ; T <sub>j</sub> = -55 °C; see <a href="#">Figure 10</a>                     | -   | -    | 4.4  | V    |
| I <sub>DSS</sub>        | drain leakage current            | V <sub>DS</sub> = 55 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 175 °C  | -   | -    | 500  | μA   |
|                         |                                  | V <sub>DS</sub> = 55 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C   | -   | 0.05 | 10   | μA   |
| I <sub>GSS</sub>        | gate leakage current             | V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C   | -   | 2    | 100  | nA   |
|                         |                                  | V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C  | -   | 2    | 100  | nA   |
| R <sub>DSon</sub>       | drain-source on-state resistance | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 175 °C; see <a href="#">Figure 11</a> ; see <a href="#">Figure 12</a> | -   | -    | 30   | mΩ   |
|                         |                                  | V <sub>GS</sub> = 10 V; I <sub>D</sub> = 25 A; T <sub>j</sub> = 25 °C; see <a href="#">Figure 11</a> ; see <a href="#">Figure 12</a>  | -   | 13   | 15   | mΩ   |
| Dynamic characteristics |                                  |   |     |      |      |      |
| Q <sub>G(tot)</sub>     | total gate charge                | I <sub>D</sub> = 25 A; V <sub>DS</sub> = 44 V; V <sub>GS</sub> = 10 V; T <sub>j</sub> = 25 °C; see <a href="#">Figure 13</a>          | -   | 50   | -    | nC   |
| Q <sub>GS</sub>         | gate-source charge               |   | -   | 9    | -    | nC   |
| Q <sub>GD</sub>         | gate-drain charge                |   | -   | 19   | -    | nC   |
| C <sub>iss</sub>        | input capacitance                | V <sub>GS</sub> = 0 V; V <sub>DS</sub> = 25 V; f = 1 MHz; T <sub>j</sub> = 25 °C; see <a href="#">Figure 14</a>                       | -   | 1580 | 2107 | pF   |
| C <sub>oss</sub>        | output capacitance               |   | -   | 370  | 446  | pF   |
| C <sub>rss</sub>        | reverse transfer capacitance     |   | -   | 220  | 300  | pF   |
| t <sub>d(on)</sub>      | turn-on delay time               | V <sub>DS</sub> = 30 V; R <sub>L</sub> = 1.2 Ω; V <sub>GS</sub> = 10 V; R <sub>G(ext)</sub> = 10 Ω; T <sub>j</sub> = 25 °C            | -   | 26   | -    | ns   |
| t <sub>r</sub>          | rise time                        |   | -   | 99   | -    | ns   |
| t <sub>d(off)</sub>     | turn-off delay time              |   | -   | 73   | -    | ns   |
| t <sub>f</sub>          | fall time                        |   | -   | 65   | -    | ns   |
| L <sub>D</sub>          | internal drain inductance        | measured from drain to centre of die ; T <sub>j</sub> = 25 °C   | -   | 2.5  | -    | nH   |
| L <sub>S</sub>          | internal source inductance       | measured from source to source bond pad ; T <sub>j</sub> = 25 °C  | -   | 7.5  | -    | nH   |
| Source-drain diode      |                                  |   |     |      |      |      |
| V <sub>SD</sub>         | source-drain voltage             | I <sub>S</sub> = 25 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C; see <a href="#">Figure 15</a>                                   | -   | 0.85 | 1.2  | V    |
| t <sub>rr</sub>         | reverse recovery time            | I <sub>S</sub> = 25 A; dI <sub>S</sub> /dt = -100 A/μs; V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 30 V; T <sub>j</sub> = 25 °C       | -   | 48   | -    | ns   |
| Q <sub>r</sub>          | recovered charge                 |   | -   | 106  | -    | nC   |

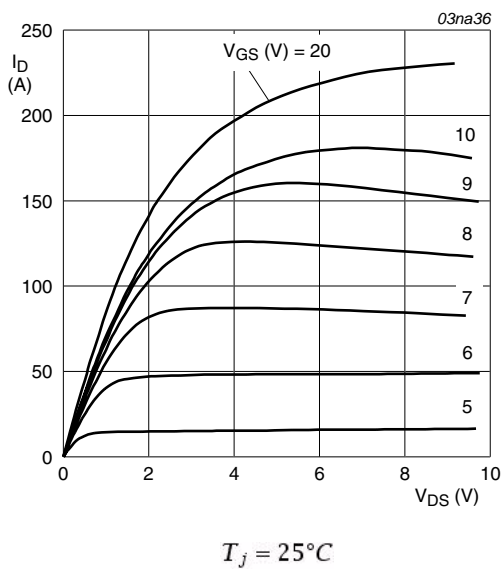


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values

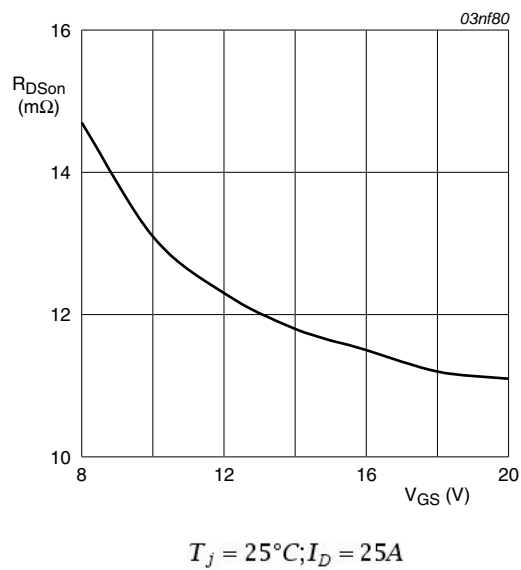


Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values

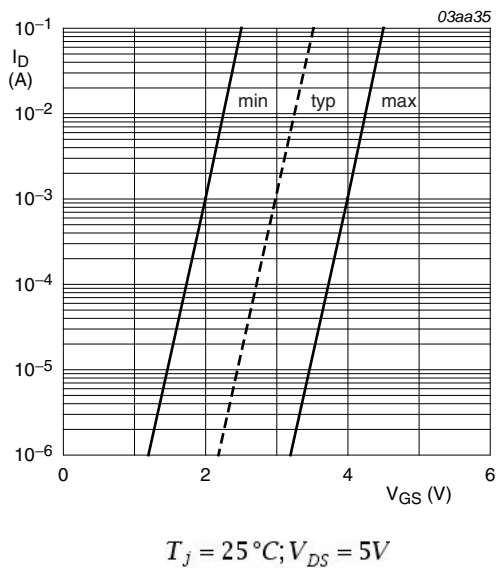


Fig 7. Sub-threshold drain current as a function of gate-source voltage

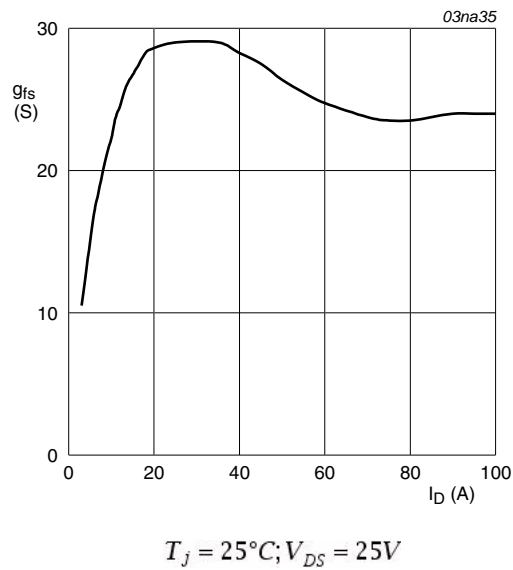


Fig 8. Forward transconductance as a function of drain current; typical values

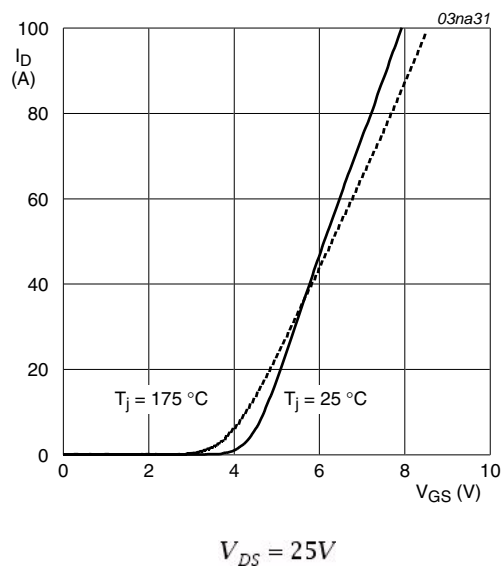


Fig 9. Transfer characteristics: drain current as a function of gate-source voltage; typical values

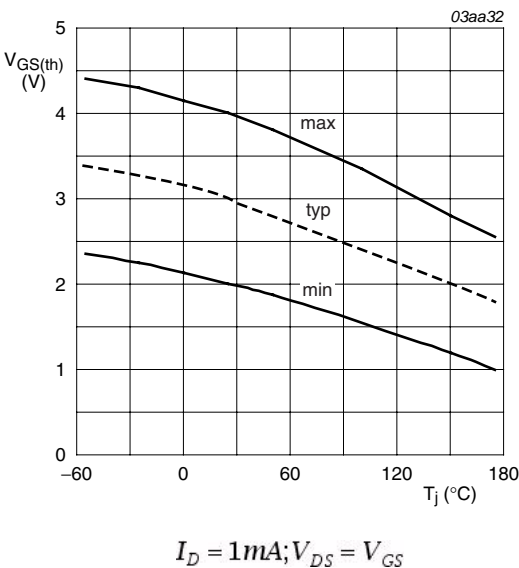


Fig 10. Gate-source threshold voltage as a function of junction temperature

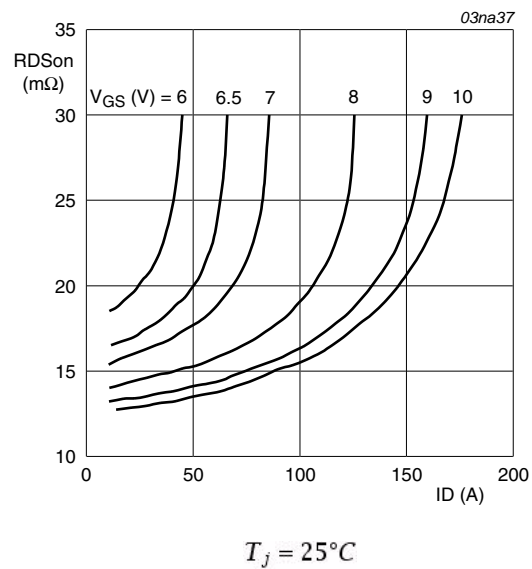


Fig 11. Drain-source on-state resistance as a function of drain current; typical values

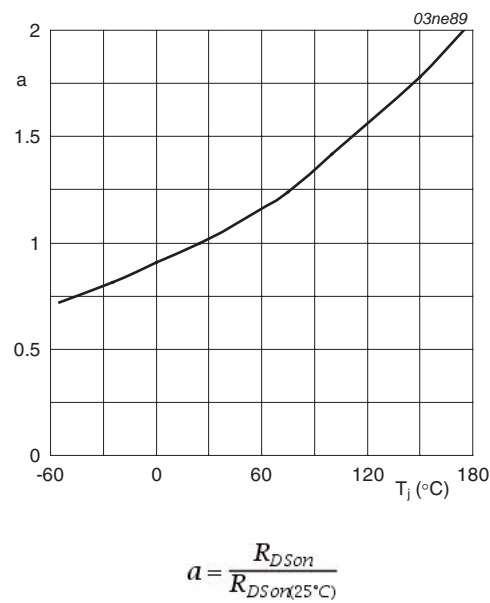


Fig 12. Normalized drain-source on-state resistance factor as a function of junction temperature



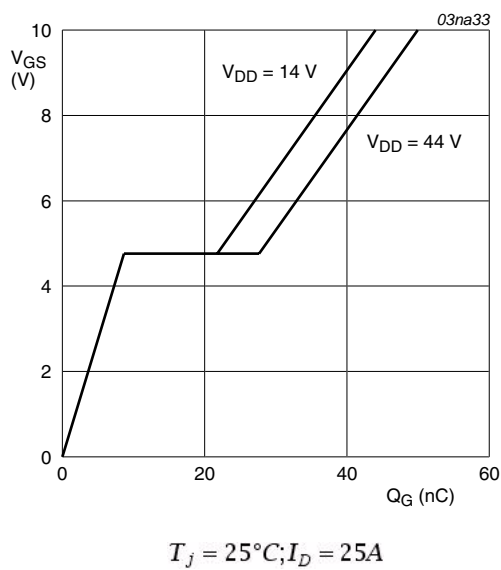


Fig 13. Gate-source voltage as a function of turn-on gate charge; typical values

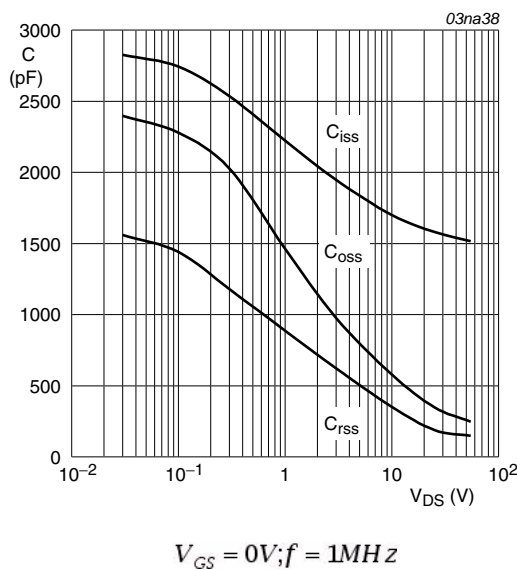


Fig 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

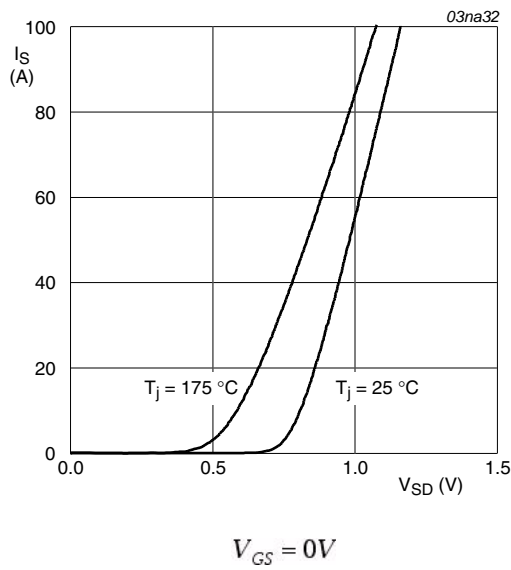


Fig 15. Reverse diode current as a function of reverse diode voltage; typical values

7. Package outline

Plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)

SOT428

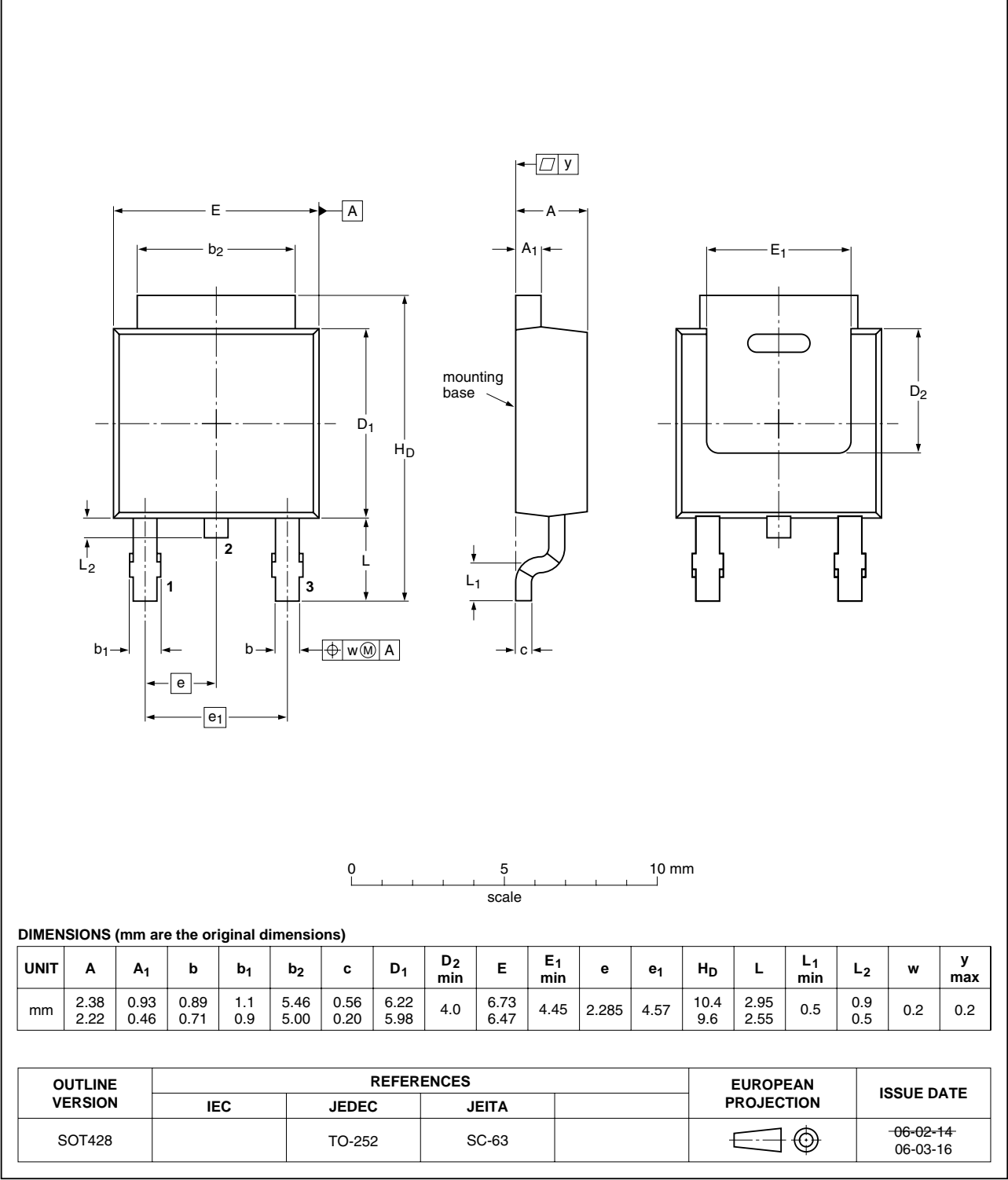


Fig 16. Package outline SOT428 (DPAK)

## 8. Revision history

Table 7. Revision history

| Document ID     | Release date   | Data sheet status  | Change notice | Supersedes      |
|-----------------|--|--------------------|---------------|-----------------|
| BUK7215-55A v.2 | 20110127   | Product data sheet | -             | BUK7215_55A v.1 |
| Modifications:  | <ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li></ul> |                    |               |                 |
| BUK7215_55A v.1 | 20010816   | Product data       | -             | -               |

## 9. Legal information

### 9.1 Data sheet status

| Document status <sup>[1][2]</sup> | Product status <sup>[3]</sup> | Definition  |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet      | Development                   | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet    | Qualification                 | This document contains data from the preliminary specification.                       |
| Product [short] data sheet        | Production                    | This document contains the product specification.                                     |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

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