

# 2 A low V<sub>F</sub> dual MEGA Schottky barrier rectifier Rev. 1 — 24 August 2010 Pr

Product data sheet

#### 1. **Product profile**

#### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier in common cathode configuration with an integrated guard ring for stress protection, encapsulated in a SOT1061 leadless small Surface-Mounted Device (SMD) plastic package with medium power capability.

#### 1.2 Features and benefits

- Average forward current: I<sub>F(AV)</sub> ≤ 2 A
- Reverse voltage: V<sub>R</sub> ≤ 30 V
- Low forward voltage
- Exposed heat sink (cathode pad) for excellent thermal and electrical conductivity
- Leadless small SMD plastic package with medium power capability
- AEC-Q101 qualified

#### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications
- Battery chargers for mobile equipment

#### 1.4 Quick reference data

#### Table 1. Quick reference data

 $T_i = 25 \ ^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per diode						
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; f = 20 kHz				
		$T_{amb} \le 75 \ ^{\circ}C$	<u>[1]</u> _	-	2	А
		$T_{sp} \le 135 \ ^{\circ}C$	-	-	2	А
V <sub>R</sub>	reverse voltage		-	-	30	V
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 2 A	-	410	440	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 30 V	-	485	2000	μA

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al<sub>2</sub>O<sub>3</sub>, standard footprint.



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#### 2. Pinning information

Table 2.	Pinning		
Pin	Description	Simplified outline Graphic symbo	bl
1	anode diode 1		
2	anode diode 2	3	
3	common cathode		2
		Transparent top view 006aaa	a438

### 3. Ordering information

Table 3. Orde	ring inforn	nation	
Type number Package			
	Name	Description	Version
PMEG3020CPA	HUSON3	plastic thermal enhanced ultra thin small outline package; no leads; three terminals; body $2 \times 2 \times 0.65$ mm	SOT1061

#### 4. Marking

Table 4.	Marking codes	
Type num	iber	Marking code
PMEG302	20CPA	AM

#### 5. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

			,		
Symbol	Parameter	Conditions	Min	Max	Unit
Per diode					
V <sub>R</sub>	reverse voltage	$T_j \le 25 \ ^\circ C$	-	30	V
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; f = 20 kHz			
		$T_{amb} \le 75 \ ^{\circ}C$	<u>[1]</u> _	2	А
		$T_{sp} \le 135 \ ^{\circ}C$	-	2	А
I <sub>FRM</sub>	repetitive peak forward current	$t_p \le$ 1 ms; $\delta \le$ 0.25	-	7	A
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms	[2]	9	A

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#### Table 5. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Per device, or	ne diode loaded				
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[3][4]</u> _	500	mW
			[3][5]	960	mW
			<u>[1][3]</u>	1800	mW
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

[1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[2]  $T_j = 25 \ ^{\circ}C$  prior to surge.

[3] Reflow soldering is the only recommended soldering method.

[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[5] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

#### 6. Thermal characteristics

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per devic	e, one diode loaded					
R <sub>th(j-a)</sub>	thermal resistance from	in free air	[1][2]			
	junction to ambient		[3] _	-	250	K/W
			[4] _	-	130	K/W
			[5] _	-	70	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		<u>[6]</u> _	-	12	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.

[2] Reflow soldering is the only recommended soldering method.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

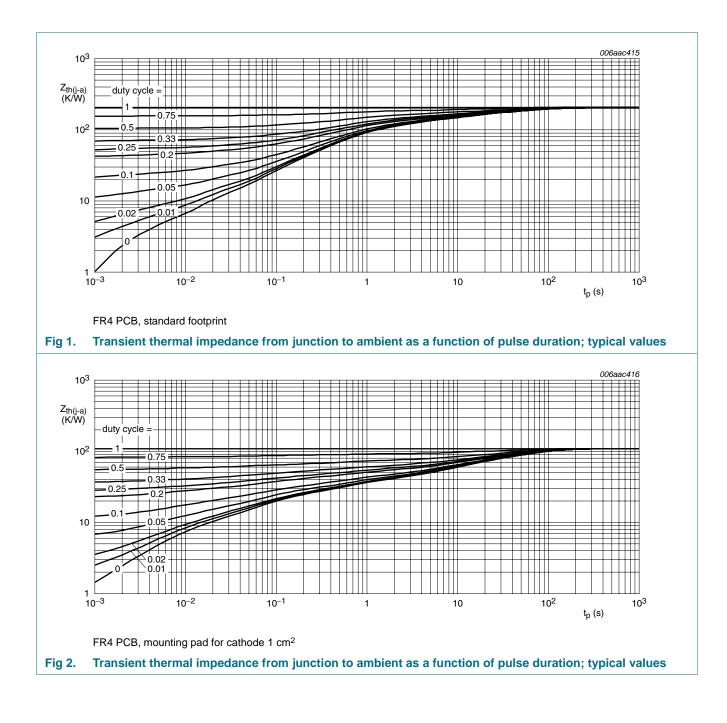
[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

[5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.

[6] Soldering point of cathode tab.

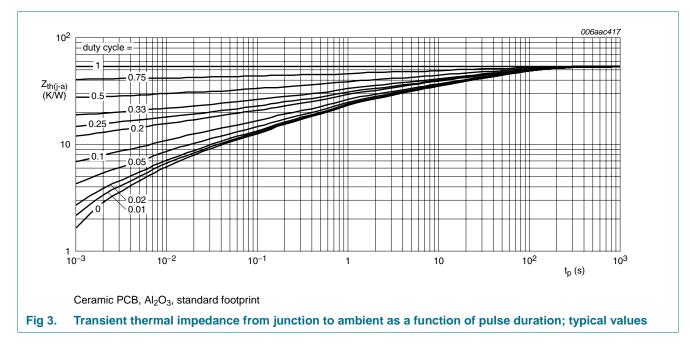
### PMEG3020CPA

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#### 2 A low V<sub>F</sub> dual MEGA Schottky barrier rectifier



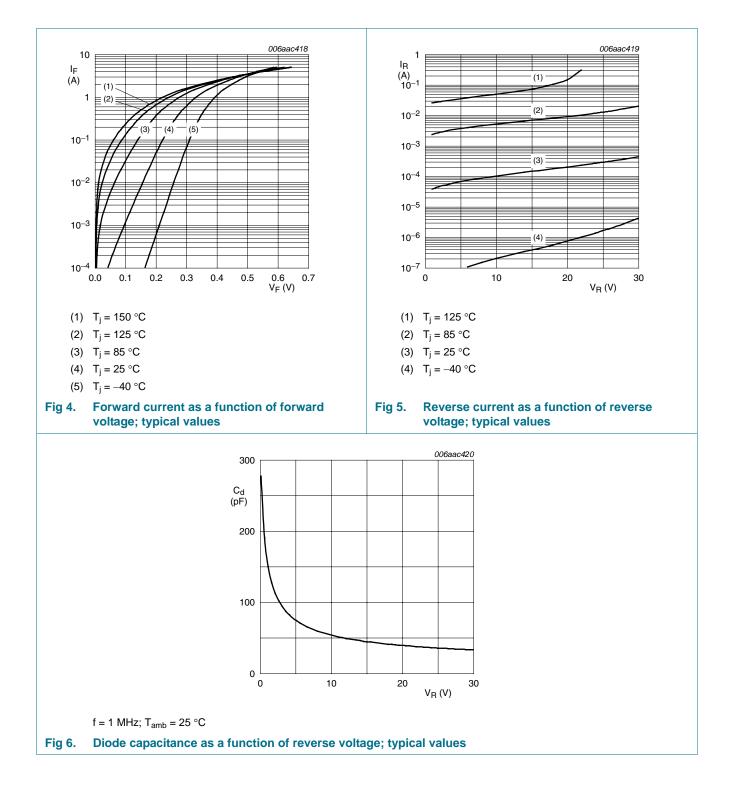
#### 7. Characteristics

Table 7.Characteristics

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
Per diode						
V <sub>F</sub> forward voltage	forward voltage	I <sub>F</sub> = 100 mA	-	220	-	mV
		I <sub>F</sub> = 1 A	-	335	370	mV
	I <sub>F</sub> = 2 A	-	410	440	mV	
I <sub>R</sub>	R reverse current	V <sub>R</sub> = 10 V	-	120	-	μA
		V <sub>R</sub> = 30 V	-	485	2000	μA
C <sub>d</sub>	diode capacitance	f = 1 MHz				
		$V_R = 1 V$	-	170	-	pF
		V <sub>R</sub> = 10 V	-	60	-	pF
t <sub>rr</sub>	reverse recovery time	е	<u>[1]</u> _	50	-	ns

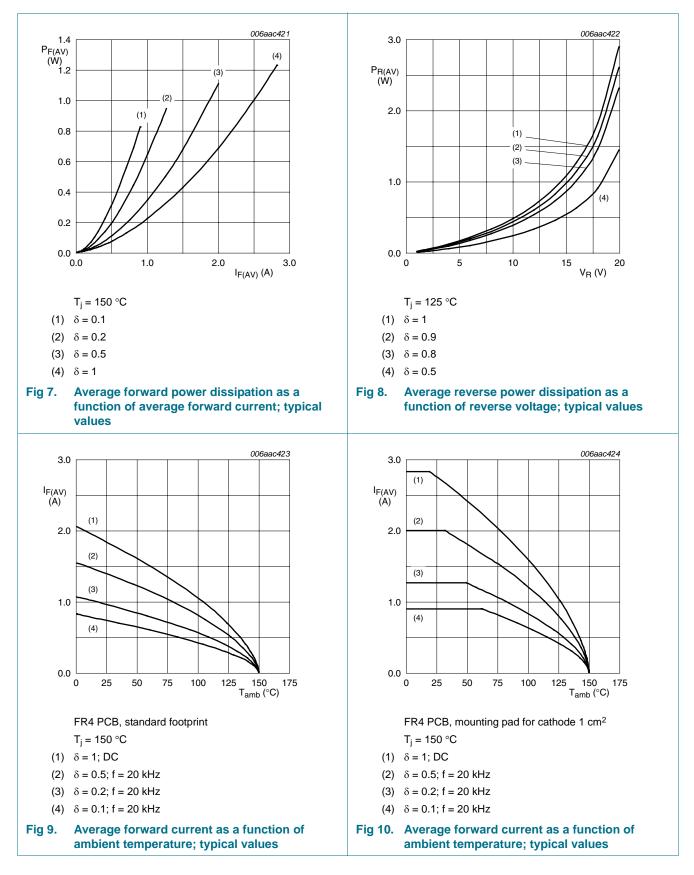
[1] When switched from I<sub>F</sub> = 10 mA to I<sub>R</sub> = 10 mA; R<sub>L</sub> = 100  $\Omega$ ; measured at I<sub>R</sub> = 1 mA.

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### PMEG3020CPA

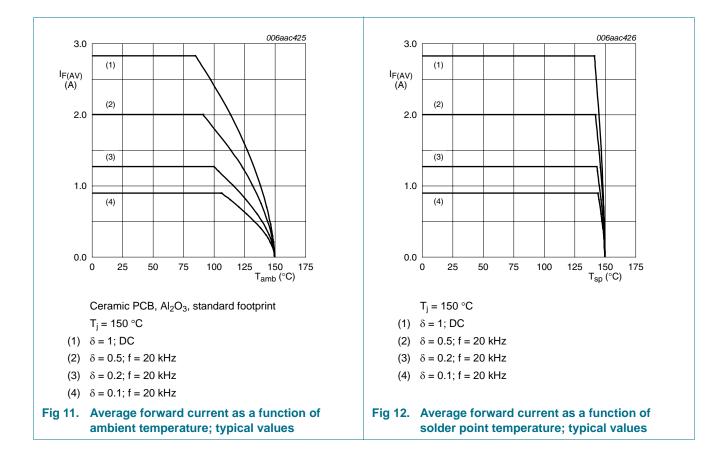
#### 2 A low V<sub>F</sub> dual MEGA Schottky barrier rectifier



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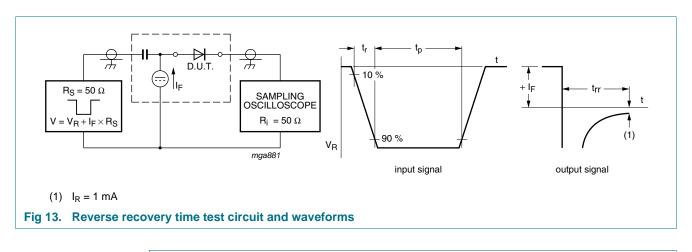
### PMEG3020CPA

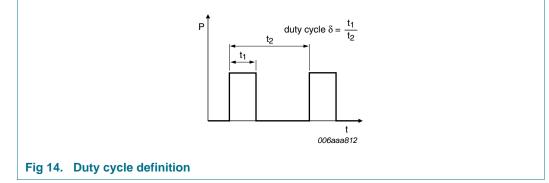
#### 2 A low V<sub>F</sub> dual MEGA Schottky barrier rectifier



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#### 8. Test information





The current ratings for the typical waveforms as shown in Figure 9, 10, 11 and 12 are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,

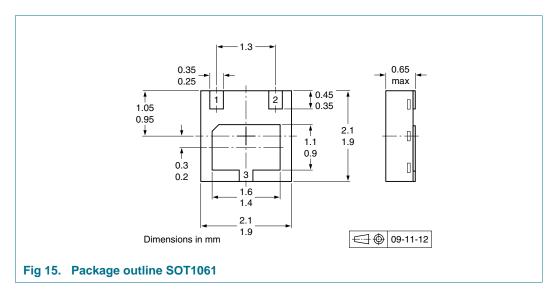
 $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with I<sub>RMS</sub> defined as RMS current.

#### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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#### 9. Package outline



#### **10. Packing information**

#### Table 8. Packing methods

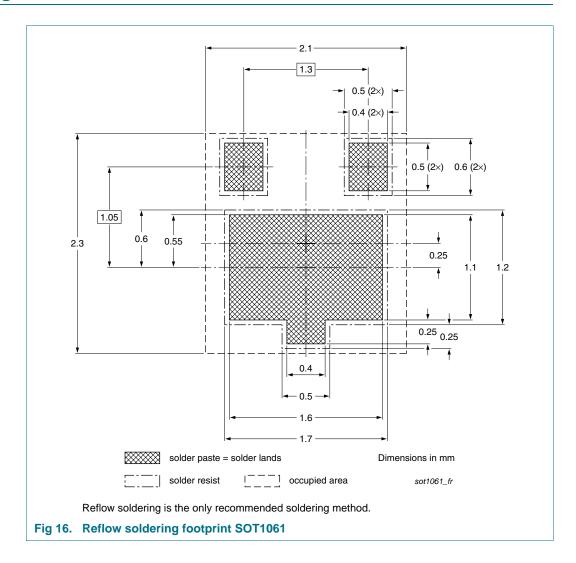
The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity
			3000
PMEG3020CPA	SOT1061	4 mm pitch, 8 mm tape and reel	-115

[1] For further information and the availability of packing methods, see Section 14.

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#### **11. Soldering**



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### **12. Revision history**

Table 9. Revision hist	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3020CPA v.1	20100824	Product data sheet	-	-

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#### **13. Legal information**

#### 13.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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