Note: xxxFN (JEDEC SOP) is not available in

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Japan.

TC4584BFN

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4584BFN

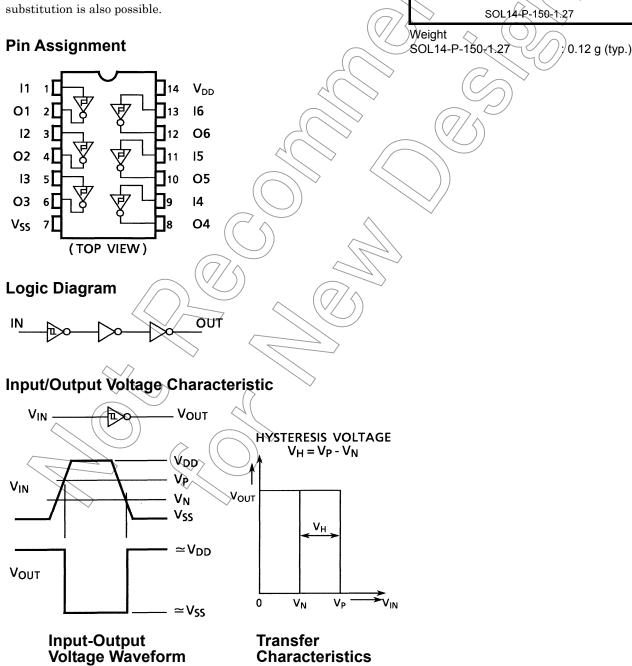
TC4584B Hex Schmitt Trigger

The TC4584B is the 6-circuit inverter having the Schmitt trigger function at the input terminal.

That is, since the circuit threshold level voltages at the leading and trailing edges of input waveform are different (VP, VN), the TC4584B can be used in the broad range application including line receiver, waveform shaping circuit, astable multivibrator, monostable multivibrator, etc.

In addition to ordinary inverter.

Since the pins are compatible with the TC4069UB, the substitution is also possible.



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V _{DD}	$V_{SS}-0.5V_{SS}+20$	V
Input voltage	V _{IN}	V _{SS} - 0.5~V _{DD} + 0.5	V
Output voltage	V _{OUT}	V _{SS} – 0.5~V _{DD} + 0.5	< v
DC input current	l _{IN}	±10	mA
Power dissipation	PD	180	(mV)
Operating temperature range	T _{opr}	-40~85	,°C
Storage temperature range	T _{stg}	-65~150	<u> </u>

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V _{DD}	$\langle \langle \rangle \rangle$	3	_	18	V
Input voltage		_	0	_	V _{DD}	V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics ($V_{SS} = 0 V$)

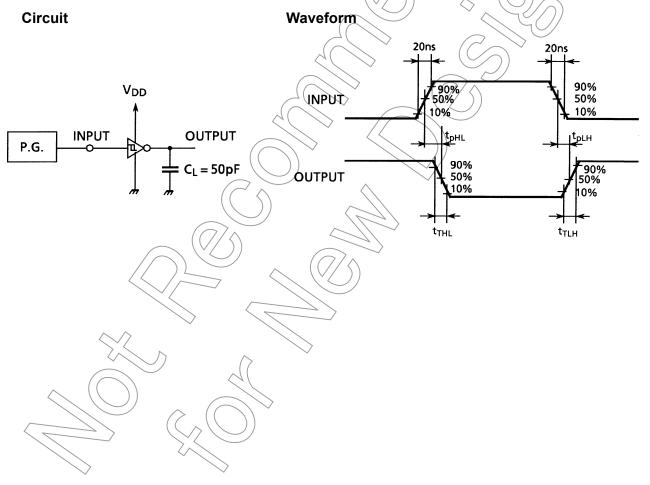
		Sym-	Test Condition		–40°C			25°C		85°C		
Charact	eristics	bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output voltage	V _{OH}	I _{OUT} < 1 μΑ	5	4.95	_	4.95	5.00	_	4.95	_		
			10	9.95	—	9.95	10.00	_	9.95	—	V	
		$V_{IN} = V_{SS}, V_{DD}$	15	14.95	—	14.95	15.00	\geq	14.95	—		
				5	_	0.05	_	0.00	0.05)~	0.05	
Low-level of voltage	output	V _{OL}	I _{OUT} < 1 μΑ	10	—	0.05	—	0.00	0.05	2_	0.05	V
· · · · · · · · · · · · · · · · · · ·			$V_{IN} = V_{SS}, V_{DD}$	15	—	0.05	\checkmark	0.00	0,05	—	0.05	
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	Ľ	-0.42	_	mA
			V _{OH} = 2.5 V	5	-2.50	—	-2.10 (-4.0	> —	-1.70	_	
Output hig	h current	IOH	V _{OH} = 9.5 V	10	-1.50	—	-1.30	-2.2	—	-1.10	_	
			V _{OH} = 13.5 V	15	-4.00	- <	-3.40	9.0	—	-2.80	$\left< - \right>$	
			$V_{IN} = V_{SS}$				$\langle \rangle$		ſ			
		I _{OL}	V _{OL} = 0.4 V	5	0.61	(-(//	0.51	1.5	-((0.42		mA
	ourront		$V_{OL} = 0.5 V$	10	1.50		1.30	3.8	\mathcal{K}	(1.10))	
Output low	current		V _{OL} = 1.5 V	15	4.00	\rightarrow	3.40	15.0	\rightarrow	2.80	—	
			$V_{IN} = V_{DD}$		20	\searrow		((~		
		VP	V _{OUT} = 0.5 V	5	2.05	3.75	2.15	3.0	3.75	2.15	3.85	V
Positive trig threshold v			V _{OUT} = 1.0 V	10 (4.80	7.60	4.90	6,4	7.60	4.90	7.70	
			V _{OUT} = 1.5 V	15	7.80	11.60	7.90	9.9	11.60	7.90	11.70	
		V _N	V _{OUT} = 4.5 V	5	1.25	2.95	1.25	2.3	2.85	1.15	2.85	
Negative tr threshold v			V _{OUT} = 9.0 V (10	2.40	5.20	2.40	/3.8	5.10	2.30	5.10	V
			V _{OUT} = 13.5 V	15	3.40	7.20	3.40	5.2	7.10	3.30	7.10	
			$(\bigcirc \bigcirc)$	5	0.10	1.25	0.25	0.65	1.25	0.25	1.40	
Hysteresis	voltage	V_{H}		10	1.80 <	3.50	1.90	2.60	3.50	1.90	3.60	V
			$\langle 0/5 \rangle$	15	3.70	5.60	3.80	4.70	5.60	3.80	5.70	
Input	"H" level	Ин	VIH = 18 V	18	(7)	0.1	_	10 ⁻⁵	0.1	_	1.0	μA
current	"L" level	(hr	NH=OV	18	X	0.1		-10 ⁻⁵	-0.1	—	-1.0	μA
				5		1	—	0.001	1	_	7.5	
Quiescent current	supply	IDD	$V_{IN} = V_{SS}, V_{DD}$	10		2	—	0.002	2	—	15.0	μA
	\sim	2.	(Note)	15	> —	4	—	0.004	4	—	30.0	

Note: All valid input combinations.

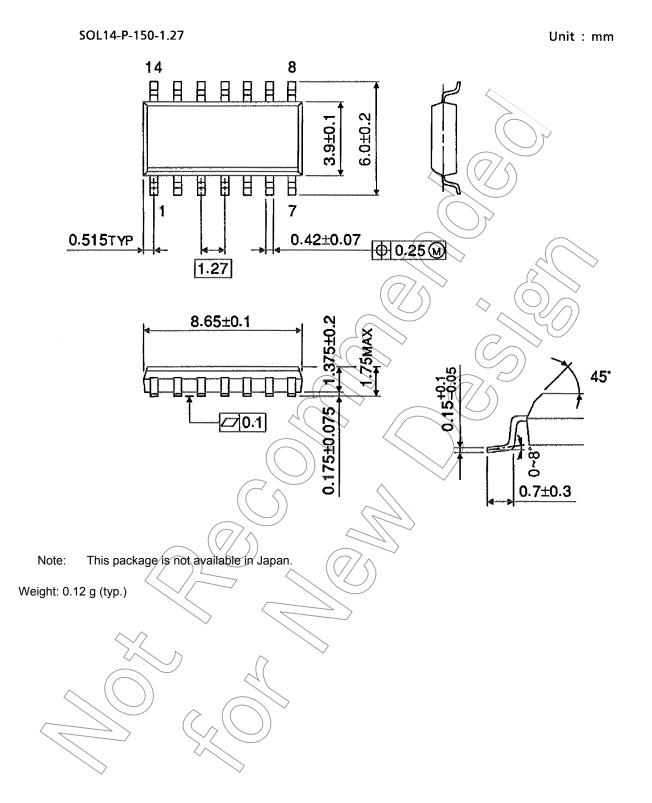
Dynamic Electrical Characteristics ($Ta = 25^{\circ}C$, $V_{SS} = 0 V$, $C_{L} = 50 pF$)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Characteristics	Gymbol		V _{DD} (V)	IVIIII	тур.	IVIAX	Onit
Output transition time			5	_	80	200	
(low to high)	t _{TLH}	—	10	_	50	100	ns
(low to high)			15	\mathbf{X}	40	80	
Output transition time	t⊤н∟		5		80	200	
Output transition time (high to low)		—	10	$\langle \succ \rangle$	50	100	ns
		~	15		40	80	
	t _{pLH} t _{pHL}		5	Y	170	340	
Propagation delay time		—	10		80	160	ns
			15	_	60	120	
Input capacitance	C _{IN}		$\langle \rangle$		5	7.5	pF

Circuit and Waveform for Measurement of Dynamic Characteristics



Package Dimensions (Note)



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