MOSFETs Silicon N-channel MOS (U-MOSVII-H)

TPN3300ANH

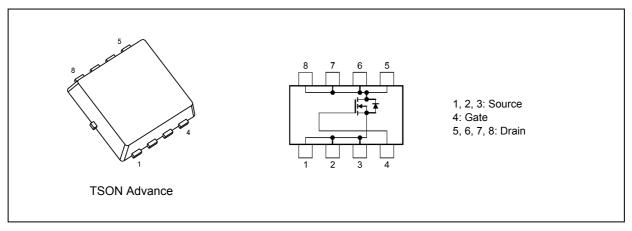
1. Applications

- DC-DC Converters
- Switching Voltage Regulators
- Motor Drivers

2. Features

- (1) Small, thin package
- (2) High-speed switching
- (3) Small gate charge: $Q_{SW} = 4.5 \text{ nC}$ (typ.)
- (4) Low drain-source on-resistance: $R_{DS(ON)} = 28 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 10 \text{ V})$
- (5) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 100 \ V)$
- (6) Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 0.1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (T_a = 25 °C unless otherwise specified)

Characteris	tics		Symbol	Rating	Unit
Drain-source voltage			V _{DSS}	100	V
Gate-source voltage	,		V _{GSS}	±20]
Drain current (DC)	(Silicon limit)	(Note 1), (Note 2)	Ι _D	21	A
Drain current (DC)	(T _c = 25 °C)	(Note 1)	I _D	9.4]
Drain current (pulsed)	(t = 1 ms)	(Note 1)	I _{DP}	38	1
Power dissipation	(T _c = 25 °C)		PD	27	W
Power dissipation	(t = 10 s)	(Note 3)	PD	1.9	W
Power dissipation	(t = 10 s)	(Note 4)	PD	0.7	W
Single-pulse avalanche energy		(Note 5)	E _{AS}	23	mJ
Avalanche current			I _{AR}	9.4	A
Channel temperature			T _{ch}	150	°C
Storage temperature			T _{stg}	-55 to 150]

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics			Symbol	Max	Unit
Channel-to-case thermal resistance	(T _c = 25 °C)		R _{th(ch-c)}	4.62	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R _{th(ch-a)}	65.7	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 4)	R _{th(ch-a)}	178	

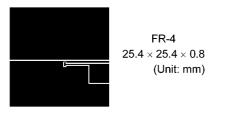
Note 1: Ensure that the channel temperature does not exceed 150 °C.

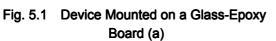
Note 2: Limited by silicon chip capability.

Note 3: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 4: Device mounted on a glass-epoxy board (b), Figure 5.2

Note 5: V_DD = 60 V, T_ch = 25 °C (initial), L = 0.29 mH, I_{AR} = 9.4 A





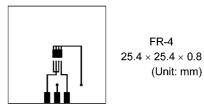


Fig. 5.2 Device Mounted on a Glass-Epoxy Board (b)

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

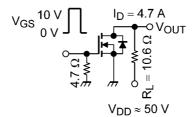
6. Electrical Characteristics

6.1. Static Characteristics (T_a = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	_	_	±0.1	μA
Drain cut-off current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	_	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Drain-source breakdown voltage	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	65	_	_	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 0.1 mA	2.0	_	4.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 10 V, I _D = 4.7 A		28	33	mΩ

6.2. Dynamic Characteristics ($T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition		Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		680	880	pF
Reverse transfer capacitance	C _{rss}		_	8.8	50	
Output capacitance	C _{oss}		_	130	_	
Gate resistance	rg	—		0.6	1.1	Ω
Switching time (rise time)	t _r	See Fig. 6.2.1	_	4.4	—	ns
Switching time (turn-on time)	t _{on}		_	12	_	
Switching time (fall time)	t _f]	_	3.8	_	
Switching time (turn-off time)	t _{off}		_	15	_	



Duty \leq 1%, t_w = 10 μ s

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25$ °C unless otherwise specified)

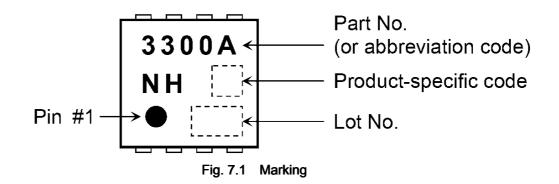
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD}\approx 50$ V, V_{GS} = 10 V, I_{D} = 9.4 A	—	11	—	nC
Gate-source charge 1	Q _{gs1}		_	3.6	_]
Gate-drain charge	Q _{gd}			3.0	_	
Gate switch charge	Q _{SW}		_	4.5	_	

6.4. Source-Drain Characteristics ($T_a = 25 \ ^{\circ}C$ unless otherwise specified)

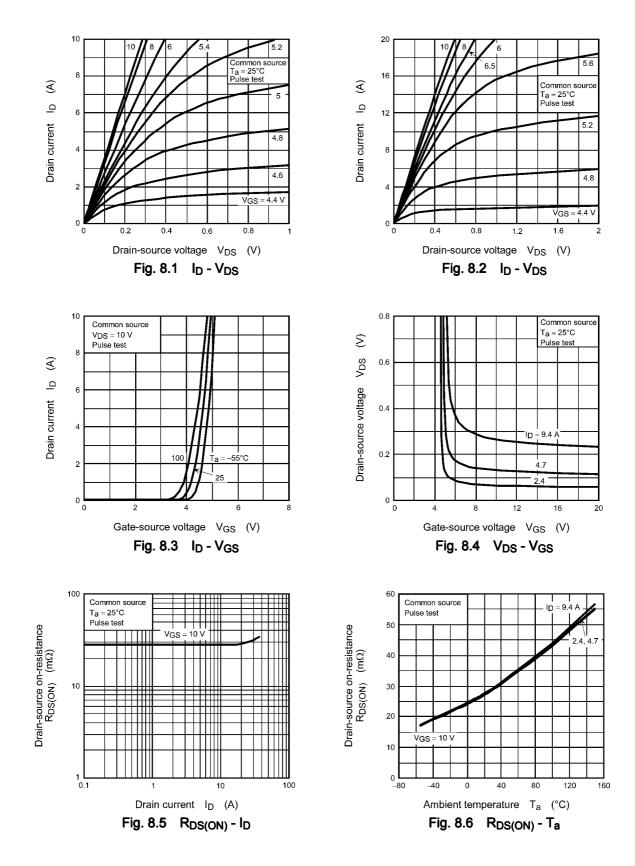
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed)	(Note 6)	I _{DRP}	—	_	_	38	Α
Diode forward voltage		V_{DSF}	I _{DR} = 9.4 A, V _{GS} = 0 V	_	_	-1.2	V

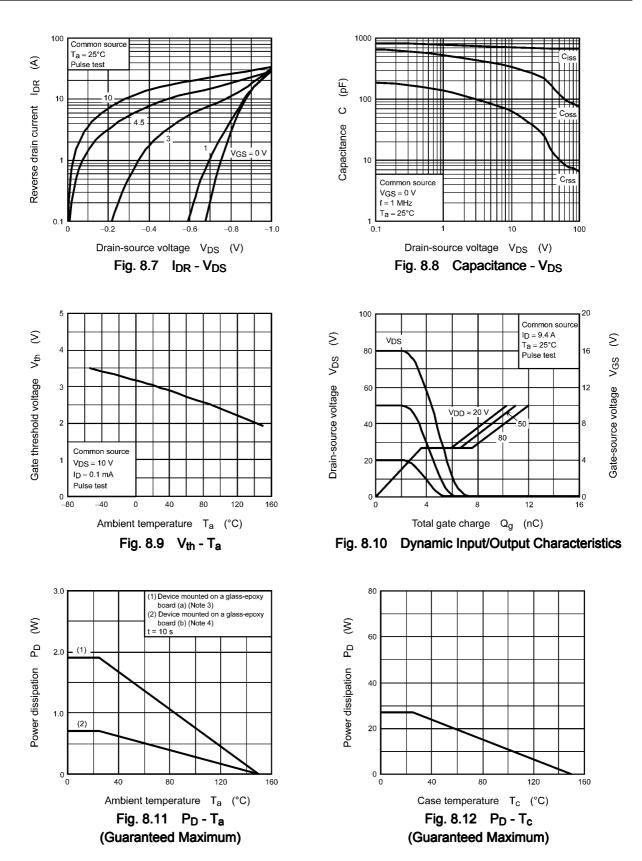
Note 6: Ensure that the channel temperature does not exceed 150 °C.

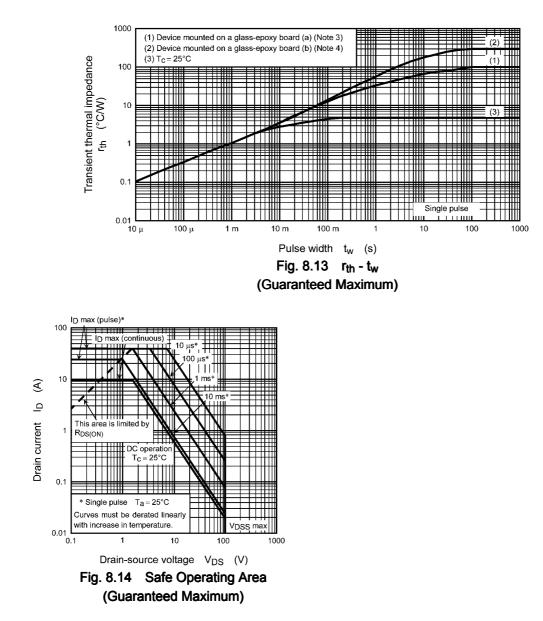
7. Marking



8. Characteristics Curves (Note)







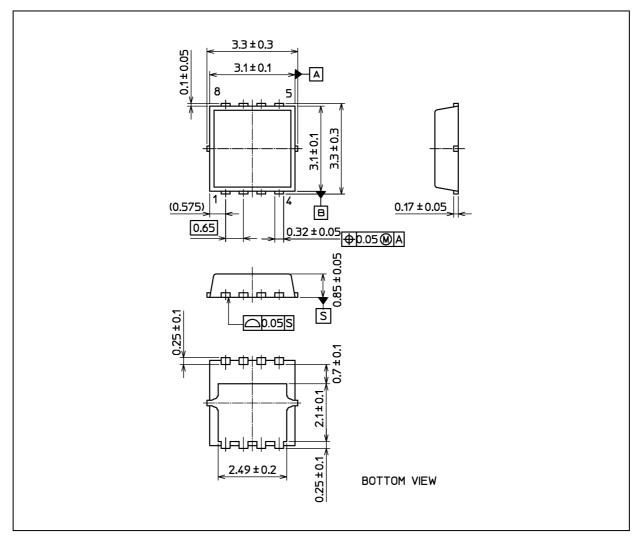
Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



TPN3300ANH

Package Dimensions

Unit: mm



Weight: 0.02 g (typ.)

Package Name(s)
TOSHIBA: 2-3X1S
Nickname: TSON Advance

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