

DATA SHEET

CURRENT SENSOR - LOW TCR

4 Termination PS Series 5%, 1%, 0.5%

0306/0612

RoHS compliant & Halogen free



YAGEO Phícomp



SCOPE

This specification describes PS series 4-terminal current sensor - low TCR chip resistors with lead-free terminations made by metal alloy process.

<u>APPLICATIONS</u>

- Battery pack
- Inverter/Converter (DC-DC/AC-DC/DC-AC)
- Consumer electronics
- Laptops

FEATURES

- This product with lead-free terminations meet RoHS requirements
- High component and equipment reliability
- Ultra low resistance and narrow tolerance suitable for current detection

ORDERING INFORMATION - GLOBAL PART NUMBER

Global part numbers are identified by the series, size, tolerance, packing type, temperature coefficient, taping reel and resistance value.

GLOBAL PART NUMBER

PS XXXX X X X XX XXX L
(1) (2) (3) (4) (5) (6) (7)

(I) SIZE

0306 / 0612

(2) TOLERANCE

D= $\pm 0.5\%$ (I0m Ω & 20m Ω)

 $F = \pm 1\%$

 $| = \pm 5\%$

(3) PACKAGING TYPE

K= Embossed taping reel

R = Paper taping reel

(4) TEMPERATURE COEFFICIENT OF RESISTANCE

 $M = \pm 75 \text{ppm/}^{\circ}\text{C}$

 $F = \pm 100 \text{ppm/}^{\circ}\text{C}$

 $L = \pm 150 \text{ppm/}^{\circ}\text{C}$

 $G = \pm 200 ppm/^{\circ}C$

 $P = \pm 300 ppm/^{\circ}C$

(5) TAPING REEL

07 / 7W / 7T= 7 inch dia. Reel and specific rated power.

Detailed power rating are shown in the Table 2.

(6) RESISTANCE VALUE

 $0.5 m\Omega$ to $100 m\Omega$

There are 3~5 digits indicated the resistance value. Letter R is decimal point.

Detailed coding rules of resistance are shown in the table of "Resistance rule of global part number".

(7) DEFAULT CODE

Letter L is the system default code for ordering only. (Note)

number		
Resistance code rule	Exampl	
	0R001 = ImΩ	
0RXXX	$ORI = I00m\Omega$	
(0UX)	$0U5 = 0.5 \text{m}\Omega$	

Resistance rule of global part

ORDERING EXAMPLE

The ordering code of a PS0306 IW chip resistor, value 0.003 Ω with $\pm 1\%$ tolerance, supplied in 7-inch tape reel is: PS0306FRL0R003L

NOTE

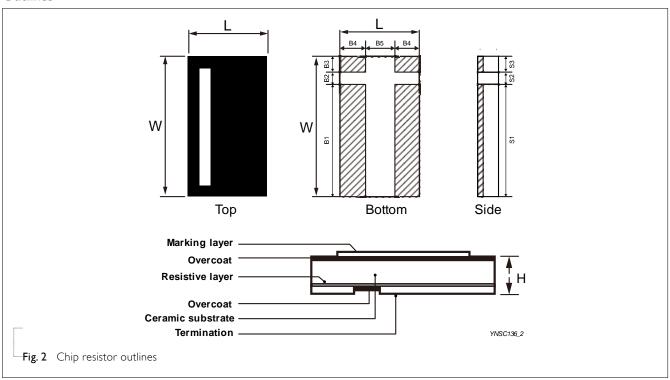
 All our RSMD products are RoHS compliant. "LFP" of the internal 2D reel label mentions "Lead-Free Process"



PS0306/0612



Outlines



DIMENSION

-Table I

TYPE	L (mm)	W (mm)	BI/SI (mm)	B2/S2 (mm)	B3/S3 (mm)	B4 (mm)	B5 (mm)	H (mm)
PS0306	0.80±0.15	1.60±0.20	1.10±0.20	0.25±0.10	0.25±0.10	0.20±0.10	0.40±0.20	0.50±0.20
PS0612	1.60+0.15/-0.20	3.20±0.20	2.20±0.20	0.50±0.20	0.50±0.20	0.45±0.20	0.70±0.20	$(0.5 \sim \text{Im}\Omega) \ 0.70 \pm 0.20$ $(2 \sim \text{I0m}\Omega) \ 0.60 \pm 0.20$ $(12 \sim \text{I00m}\Omega) \ 0.50 \pm 0.20$

Note:

- $\label{eq:loss_plane} \textbf{I. For relevant physical dimensions, please refer to construction outlines.}$
- 2. Please contact with sales offices, distributors and representatives in your region before ordering.

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ELECTRICAL CHARACTERISTICS

Table 2

SERIES	SIZE	POWER RATING	TOLERANCE	resistance range	TEMPERATURE COEEFICIENT OF RESISTANCE
		I/4W		$2m\Omega \le R < 5m\Omega$	±150ppm/°C
	0306	1/3W	\pm 0.5%(10, 20m Ω)	$5m\Omega \le R \le 100m\Omega$	±75ppm/°C
		1/2W		511132 2 1 2 1 0 0 1 1 32	±100ppm/°C
				$0.5 m\Omega$	±300ppm/°C
PS			±1%,	10	±100ppm/°C
	0412	0612 W	±5%	lmΩ	±150ppm/°C
	0612			$2m\Omega \le R \le 9m\Omega$	±100ppm/°C
				$14m\Omega \le R \le 100m\Omega$	±100ppm/°C
				$10\text{m}\Omega \le R \le 13\text{m}\Omega$	±200ppm/°C

Note: Please contact with sales offices, distributors and representatives in your region before ordering.

FUNCTIONAL DESCRIPTION

OPERATING TEMPERATURE RANGE

PS0612 $0.5 \text{m}\Omega \leq R \leq 10 \text{m}\Omega$ -55°C to +155°C $12m\Omega \le R \le 100m\Omega$ -55°C to +125°C

PS0306 -55 $^{\circ}$ C to +125 $^{\circ}$ C

POWER RATING

Standard rated power at 70°C

RATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

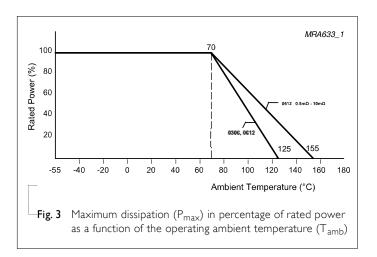
$$\vee = \sqrt{(P*R)}$$

Where

V = Continuous rated DC or AC (rms) working voltage (V)

P = Rated power (W)

 $R = Resistance value (\Omega)$



PACKING STYLE AND PACKAGING QUANTITY

Table 3 Packing style and packaging quantity

PACKING STYLE	REEL DIMENSION	PS0306	PS0612
Paper taping reel (R)	7" (178 mm)	5,000	
Embossed taping reel (K)	7" (178 mm)		4,000

PAPER TAPE

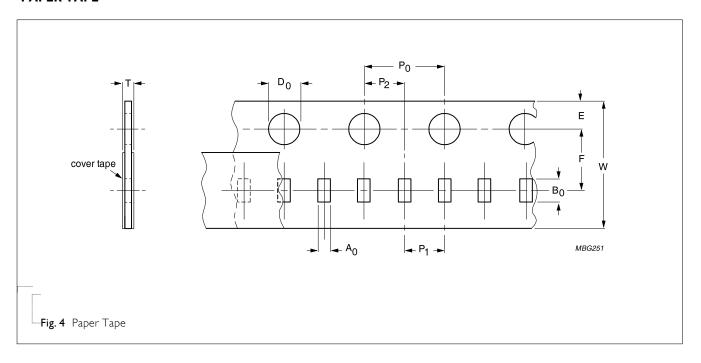


Table 4 Dimensions of paper tape for relevant chip resistors size

SIZE	SYMBOL									Unit: mm
	A ₀	B ₀	W	E	F	P ₀	Pı	P ₂	$ØD_0$	Т
PS0306	1.10±0.15	1.90±0.15	8.00±0.30	1.75±0.10	3.50±0.10	4.00±0.10	4.00±0.10	2.00±0.10	1.5±0.10	0.80±0.10

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EMBOSSED TAPE

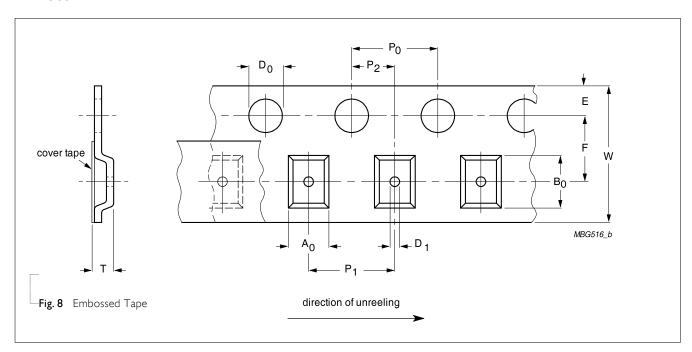


Table 5 Dimensions of embossed tape for relevant chip resistors size

SIZE	SYMBOL									Unit: mm
	A_0	B ₀	W	E	F	P ₀	Pı	P_2	$ØD_0$	Т
PS0612	1.91±0.05	3.65±0.05	8.00+0.30/-0.10	1.75±0.10	3.50±0.05	4.00±0.10	4.00±0.10	2.00±0.05	1.5±0.10	0.88±0.05

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REEL SPECIFICATION

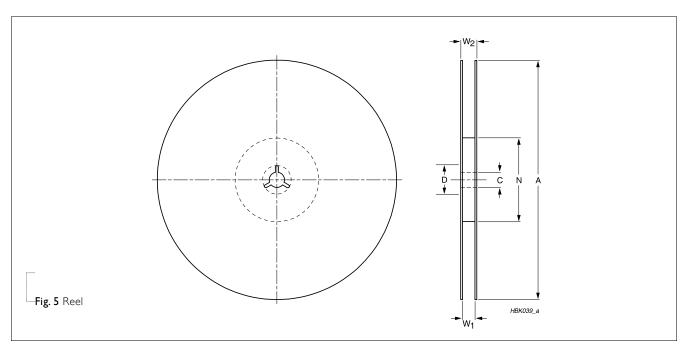


Table 6 Dimensions of reel specification for relevant chip resistors size

	QUANTITY _	REEL SIZE	SYMBO	DL		Unit: mm
SIZE	PER REEL	8 mm TAPE WIDE	Α	N	W_{l}	W _{2 MAX.}
PS0306	5000	7"(Ø 178 mm)	178.0±5	60.0±2	9.0±0.2	12.0±0.2
PS0612	4000	7"(Ø 178 mm)	178.0± 5	60.0±2	9.0±0.2	12.0±0.2

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SOLDERING PROFILES

For recommended soldering profiles, please refer to data sheet "Chip resistors mounting".

<u>FOOTPRINT</u>

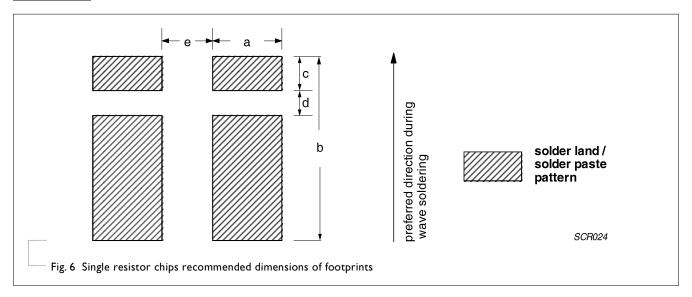


 Table 7
 Footprint dimensions

SIZE	DIMENSION	Unit: mm				
FOOTPRINT	a	b	С	d	е	t(um)
PS0306	0.40	1.75	0.35	0.20	0.20	105
PS0612	1.00	3.50	0.80	0.38	0.75	105



SERIES

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TESTS AND REQUIREMENTS

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Table 8 Test condition, procedure and requirements

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Life/	MIL-STD-202-method 108	1,000 hours at 70±2 °C applied RCWV	±(1%+0.0005 Ω)
Operational Life/ Endurance	IEC 60115-1 4.25.1	1.5 hours on, 0.5 hour off, still air required	
High Temperature Exposure/ Endurance at Upper Category Temperature	IEC 60068-2-2	1,000 hours at 125 °C &155 °C ,unpowered	±(1%+0.0005 Ω)
Moisture Resistance	MIL-STD-202-method 106	Each temperature / humidity cycle is defined at 8 hours (method 106F), 3 cycles / 24 hours for 10d with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered	±(0.5%+0.0005 Ω)
		Parts mounted on test-boards, without condensation on parts	
		Measurement at 24±2 hours after test conclusion	
Thermal Shock	MIL-STD-202-method 107	-55/+125 °C	±(1%+0.0005 Ω)
		Note: Number of cycles required is 300. Devices mounted	
		Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	
Short Time	IEC60115-1 4.13	5 times of rated power for 5 seconds at room	±(1%+0.0005 Ω)
Overload		temperature	No visible damage
Board Flex/	IEC 60068-2-21	Chips mounted on a 90mm glass epoxy resin	±(1%+0.0005 Ω)
Bending		PCB(FR4) 2 mm bending Bending time: 60±5 seconds	No visible damage

SERIES

TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Solderability			
- Wetting	J-STD-002 test B	Electrical Test not required	Well tinned (≥95% covered)
		Magnification 50X	No visible damage
		SMD conditions:	
		I st step: method B, aging 4 hours at 155 °C dry heat	
		2^{nd} step: leadfree solder bath at 245 $\pm 3^{\circ}\text{C}$	
		Dipping time: 3±0.5 seconds	
- Resistance to	IEC 60068-2-58	Condition B, no pre-heat of samples	±(0.5%+0.0005 Ω)
Soldering Heat		Leadfree solder, 260 °C, 10 seconds immersion time	No visible damage
		Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	

Product specification

Chip Resistor Surface Mount

PS

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REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version I	July 16, 2019	=	- Extend resistor value
Version 0	Mar. 06, 2017	-	- New datasheet for current sensor - low TCR 4 terminal PS series

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