EP2645ETTTS-44.237M TR

RoHS | Pb | ECLIPTEK® CORPORATION

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EP26  45  ET  T  TS  -44.237M  TR
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Series
RoHS Compliant (Pb-free) 3.3V 4 Pad 5mm x 7mm
Ceramic SMD LVCMOS Programmable Oscillator

Frequency Tolerance/Stability
±50ppm Maximum

Operating Temperature Range
-40°C to +85°C

Duty Cycle
50 ±5(%)

Pin 1 Connection
Tri-State (Disabled Output: High Impedance)

Nominal Frequency
44.237MHz
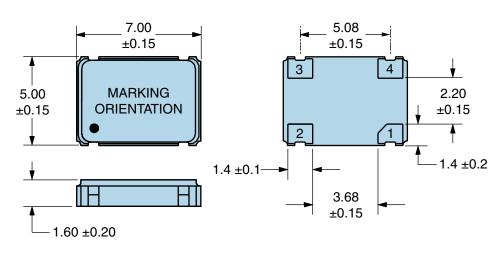
Packaging Options
Tape & Reel

ELECTRICAL SPECIFICATIONS

Nominal Frequency	44.237MHz
Frequency Tolerance/Stability	±50ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range,Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration)
Aging at 25°C	±5ppm/year Maximum
Operating Temperature Range	-40°C to +85°C
Supply Voltage	3.3Vdc ±0.3Vdc
Input Current	28mA Maximum (Unloaded)
Output Voltage Logic High (Voh)	Vdd-0.4Vdc Minimum (IOH= -8mA)
Output Voltage Logic Low (Vol)	0.4Vdc Maximum (IOL= +8mA)
Rise/Fall Time	4nSec Maximum (Measured at 20% to 80% of waveform)
Duty Cycle	50 ±5(%) (Measured at 50% of waveform)
Load Drive Capability	30pF Maximum
Output Logic Type	CMOS
Pin 1 Connection	Tri-State (Disabled Output: High Impedance)
Tri-State Input Voltage (Vih and Vil)	70% of Vdd Minimum to enable output, 20% of Vdd Maximum to disable output, No Connect to enable output.
Standby Current	20µA Maximum (Pin 1 = Ground)
Disable Current	16mA Maximum (Pin 1 = Ground)
Absolute Clock Jitter	±125pSec Maximum, ±75pSec Typical
One Sigma Clock Period Jitter	±40pSec Maximum
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to +125°C

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

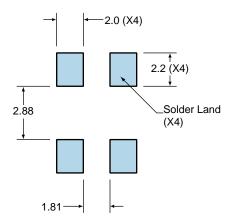|	
ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Flammability	UL94-V0
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A

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MECHANICAL DIMENSIONS (all dimensions in millimeters)

PIN	CONNECTION
1	Tri-State (High Impedance)
2	Ground/Case Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	**ECLIPTEK**
2	**44.237M**
3	**PXXYZZ** *P=Configuration Designator* *XX=Ecliptek Manufacturing Code* *Y=Last Digit of the Year* *ZZ=Week of the Year*

Suggested Solder Pad Layout

All Dimensions in Millimeters

Solder Land (X4)

All Tolerances are ±0.1

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OUTPUT WAVEFORM & TIMING DIAGRAM

TRI-STATE INPUT

V_{IH}

V_{IL}

CLOCK OUTPUT

V_{OH}
80% of Waveform
50% of Waveform
20% of Waveform
V_{OL}

OUTPUT DISABLE (HIGH IMPEDANCE STATE)

t_{PLZ}

t_{PZL}

Fall Time

Rise Time

T_W

T

Duty Cycle (%) = T_W/T x 100

Test Circuit for CMOS Output

Oscilloscope — Frequency Counter

Probe (Note 2)

Output

Supply Voltage (V_{DD})

Current Meter

Power Supply

Voltage Meter

0.01µF (Note 1)

0.1µF (Note 1)

No Connect or Tri-State

Ground

C_L (Note 3)

Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and V_{DD} pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.
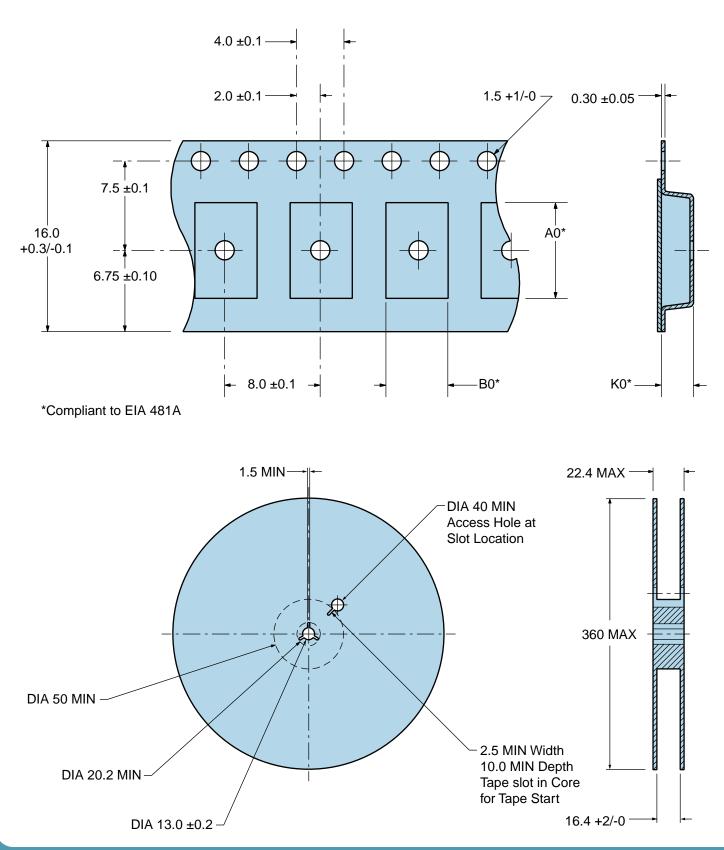
Note 3: Capacitance value C_L includes sum of all probe and fixture capacitance.

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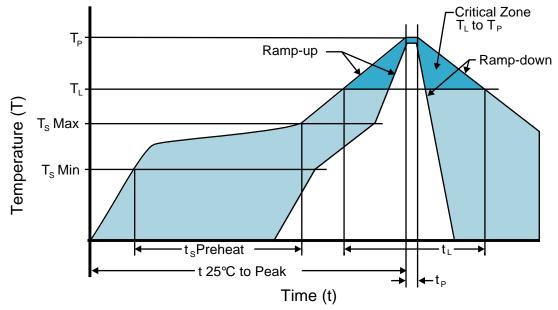
Tape & Reel Dimensions

Quantity Per Reel: 1,000 Units

4.0 ±0.1

2.0 ±0.1

1.5 +1/-0

0.30 ±0.05

7.5 ±0.1

16.0 +0.3/-0.1

6.75 ±0.10

A0*

8.0 ±0.1

B0*

K0*

*Compliant to EIA 481A

1.5 MIN

DIA 40 MIN Access Hole at Slot Location

DIA 50 MIN

DIA 20.2 MIN

DIA 13.0 ±0.2

2.5 MIN Width 10.0 MIN Depth Tape slot in Core for Tape Start

22.4 MAX

360 MAX

16.4 +2/-0

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Recommended Solder Reflow Methods

High Temperature Infrared/Convection

T$_S$ MAX to T$_L$ (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (T$_S$ MIN)	150°C
- Temperature Typical (T$_S$ TYP)	175°C
- Temperature Maximum (T$_S$ MAX)	200°C
- Time (t$_S$ MIN)	60 - 180 Seconds
Ramp-up Rate (T$_L$ to T$_P$)	3°C/second Maximum
Time Maintained Above:	
- Temperature (T$_L$)	217°C
- Time (t$_L$)	60 - 150 Seconds
Peak Temperature (T$_P$)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T$_P$ Target)	250°C +0/-5°C
Time within 5°C of actual peak (t$_p$)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

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Recommended Solder Reflow Methods

Low Temperature Infrared/Convection 240°C

T_S MAX to T_L (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T_S MIN)	N/A
- Temperature Typical (T_S TYP)	150°C
- Temperature Maximum (T_S MAX)	N/A
- Time (t_S MIN)	60 - 120 Seconds
Ramp-up Rate (T_L to T_P)	5°C/second Maximum
Time Maintained Above:	
- Temperature (T_L)	150°C
- Time (t_L)	200 Seconds Maximum
Peak Temperature (T_P)	240°C Maximum
Target Peak Temperature (T_P Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (t_p)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)