BT151-500R SCR, 12 A, 15mA, 500 V, SOT78 Rev. 05 – 2 March 2009

Product data sheet

Product profile 1.

1.1 General description

Planar passivated SCR (Silicon Controlled Rectifier) in a SOT78 plastic package.

1.2 Features and benefits

High reliability

- High thermal cycling performance
- High surge current capability

1.3 Applications

- Ignition circuits
- Motor control

1.4 Quick reference data

Protection Circuits Static switching

Table 1. **Quick reference**

	Quick reference					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	-	500	V
I _{T(AV)}	average on-state current	half sine wave; T _{mb} ≤ 109 °C; see <u>Figure 3</u>	-	-	7.5	А
I _{T(RMS)}	RMS on-state current	half sine wave; T _{mb} ≤ 109 °C; see <u>Figure 1</u> ; see <u>Figure 2</u>	-	-	12	A
Static ch	aracteristics					
I _{GT}	gate trigger current	$V_D = 12 V; T_j = 25 °C;$ $I_T = 100 mA; see Figure 8$	-	2	15	mA



2. Pinning information

Pinning	information		
Symbol	Description	Simplified outline	Graphic symbol
К	cathode		N 1
А	anode	mb	А Н К
G	gate	<u> </u>	G sym037
mb	anode	L	
	Symbol K A G	KcathodeAanodeGgate	SymbolDescriptionSimplified outlineKcathodeAanodeGgatembanode

3. Ordering information

Table 3. Ordering information

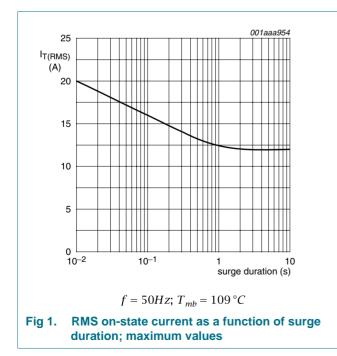
Type number	Package		
	Name	Description	Version
BT151-500R	TO-220AB; SC-46	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

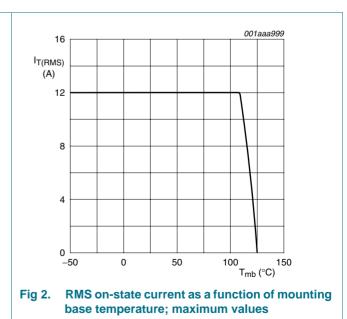
4. Limiting values

Table 4. Limiting values

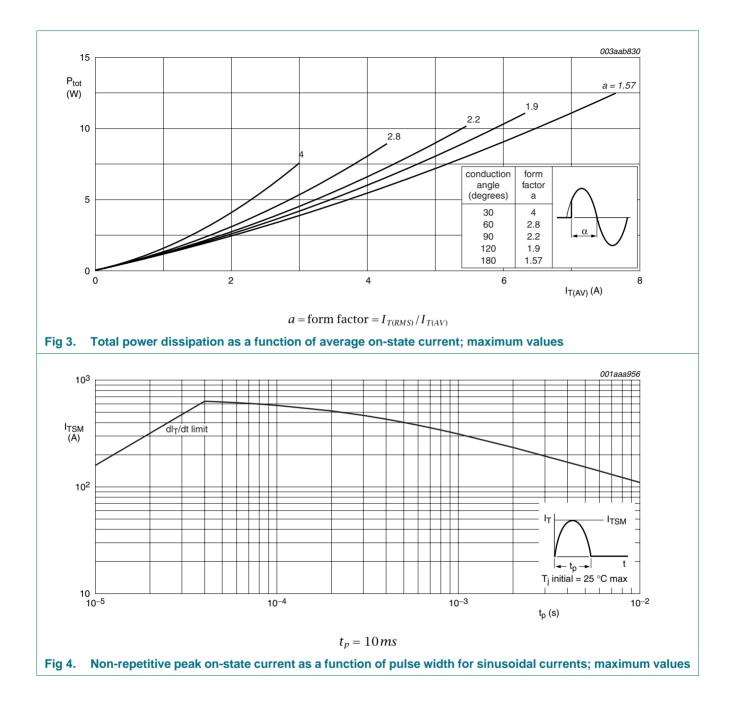
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	500	V
V _{RRM}	repetitive peak reverse voltage		-	500	V
$I_{T(AV)}$	average on-state current	half sine wave; $T_{mb} \le 109 \text{ °C}$; see Figure 3	-	7.5	А
I _{T(RMS)}	RMS on-state current	half sine wave; T _{mb} ≤ 109 °C; see <u>Figure 1</u> ; see <u>Figure 2</u>	-	12	А
dl _T /dt	rate of rise of on-state current	I_T = 20 A; I_G = 50 mA; dI_G/dt = 50 mA/µs	-	50	A/µs
I _{GM}	peak gate current		-	2	А
P _{GM}	peak gate power		-	5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C
I _{TSM}	non-repetitive peak	half sine wave; t _p = 8.3 ms; T _{j(init)} = 25 °C	-	132	А
	on-state current	half sine wave; $t_p = 10 \text{ ms}$; $T_{j(init)} = 25 \text{ °C}$; see Figure 4; see Figure 5	-	120	А
l ² t	I2t for fusing	t _p = 10 ms; sine-wave pulse	-	72	A ² s
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
V _{RGM}	peak reverse gate voltage		-	5	V



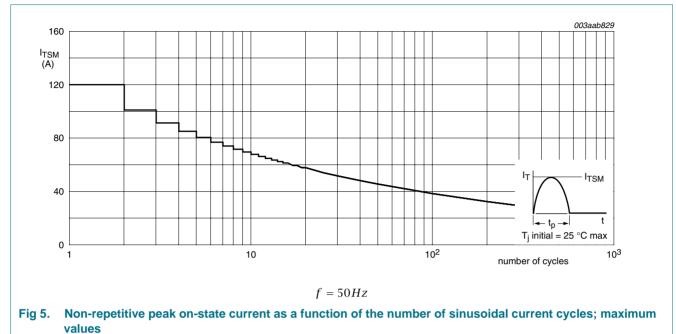


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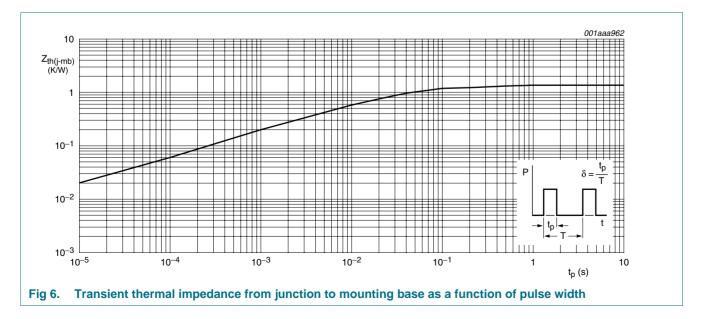
BT151-500R

SCR, 12 A, 15mA, 500 V, SOT78



5. Thermal characteristics

Table 5.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	see <u>Figure 6</u>	-	-	1.3	K/W
R _{th(j-a)}	thermal resistance from junction to ambient free air		-	60	-	K/W

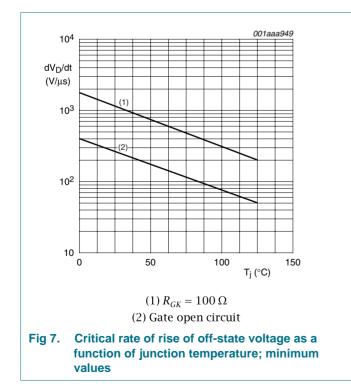


BT151-500R_5

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6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static ch	aracteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; \text{ T}_j = 25 \text{ °C}; \text{ I}_T = 100 \text{ mA}; \text{ see}$ Figure 8	-	2	15	mA
IL	latching current	$V_D = 12 \text{ V}; \text{ T}_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 9}}{1000 \text{ Figure 9}}$	-	10	40	mA
I _H	holding current	$V_D = 12 \text{ V}; \text{ T}_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure } 10}{10}$	-	7	20	mA
V _T	on-state voltage	I _T = 23 A; T _j = 25 °C; see <u>Figure 11</u>	-	1.4	1.75	V
V _{GT} gate trigger voltage		I_T = 100 mA; V_D = 12 V; T_j = 25 °C; see Figure 12	-	0.6	1.5	V
		$I_T = 100 \text{ mA}; V_D = 500 \text{ V}; T_j = 125 \text{ °C}$	0.25	0.4	-	V
I _D	off-state current	V _D = 500 V; T _j = 125 °C	-	0.1	0.5	mA
I _R	reverse current	V _R = 500 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic	characteristics					
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 335 V; T _j = 125 °C; exponential waveform; gate open circuit	50	130	-	V/µs
		V_{DM} = 335 V; T _j = 125 °C; R _{GK} = 100 Ω; exponential waveform; see Figure 7	200	1000	-	V/µs
t _{gt}	gate-controlled turn-on time	$\begin{split} I_{TM} &= 40 \text{ A}; V_D = 500 \text{V}; I_G = 100 \text{mA}; \\ dI_G/dt &= 5 A/\mu\text{s}; T_j = 25 ^\circ\text{C} \end{split}$	-	2	-	μs
t _q	commutated turn-off time		-	70	-	μs



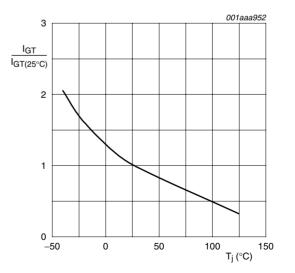
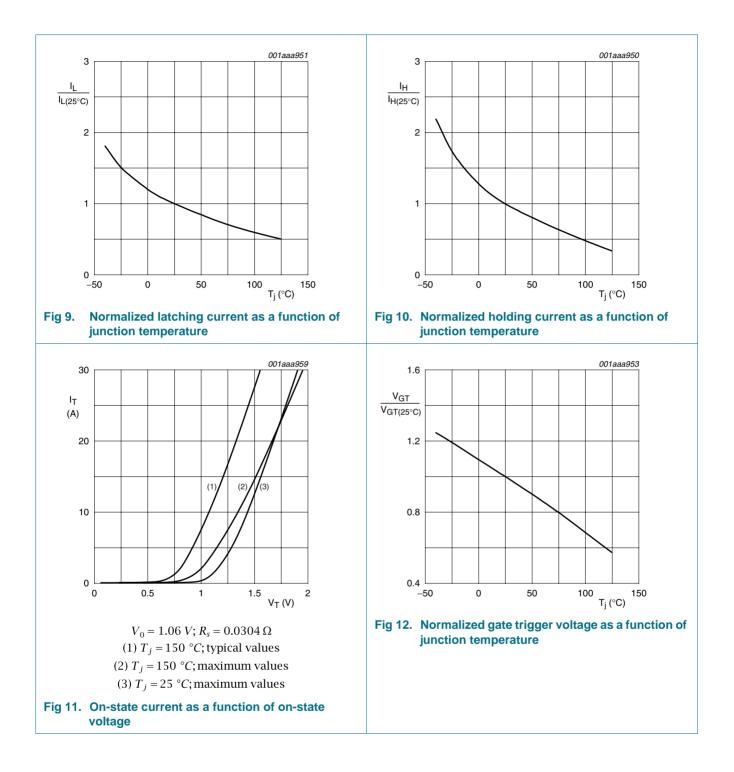


Fig 8. Normalized gate trigger current as a function of junction temperature

BT151-500R

SCR, 12 A, 15mA, 500 V, SOT78



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7. Package outline

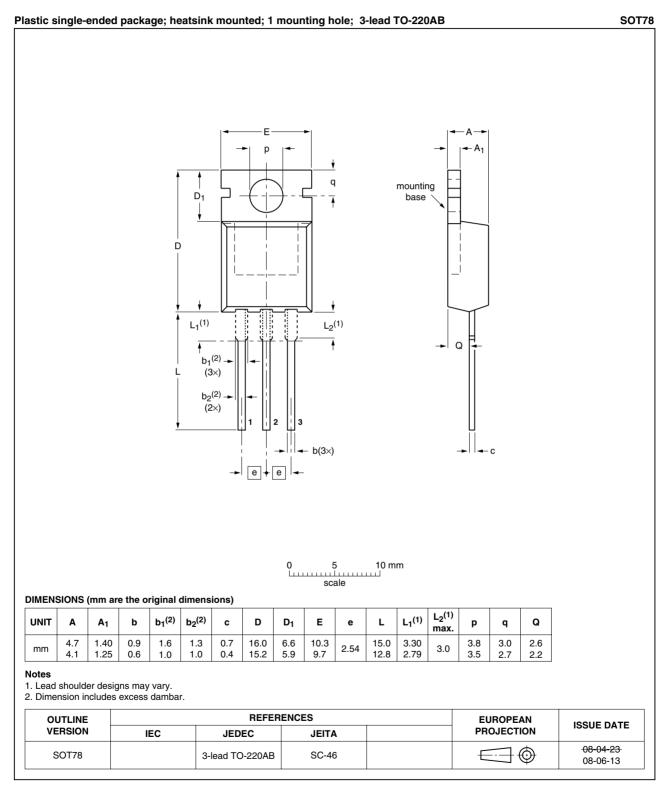


Fig 13. Package outline SOT78 (TO-220AB)

8. Revision history

Table 7. Revision histo	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BT151-500R_5	20090302	Product data sheet	-	BT151_SER_L_R_4
Modifications:	5	utline updated. er BT151-500R separated	from data sheet BT151_S	SER_L_R_4.
BT151_SER_L_R_4	20061023	Product data sheet	-	BT151_SERIES_3
BT151_SERIES_3 (9397 750 13159)	20040607	Product specification	-	BT151_SERIES_2
BT151_SERIES_2	19990601	Product specification	-	BT151_SERIES_1
BT151_SERIES_1	19970901	Product specification	-	-

9. Legal information

9.1 Data sheet status

Document status [1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions"

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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11. Contents

1	Product profile1
1.1	General description1
1.2	Features and benefits1
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Limiting values3
5	Thermal characteristics5
6	Characteristics6
7	Package outline8
8	Revision history9
9	Legal information10
9.1	Data sheet status10
9.2	Definitions10
9.3	Disclaimers
9.4	Trademarks10
10	Contact information10

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