

HAT3018R, HAT3018RJ

Silicon N/P Channel Power MOS FET
High Speed Power Switching

REJ03G0127-0200

Rev.2.00

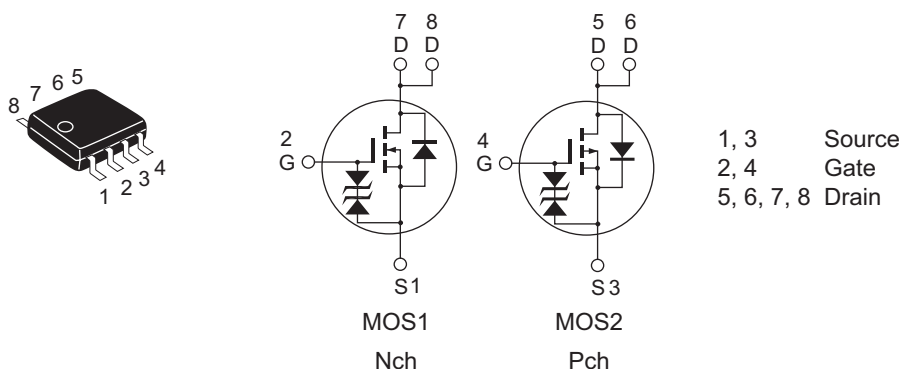
Nov 04, 2008

Features

- Low on-resistance
 - Capable of 4.5 V gate drive
 - High density mounting
 - “J” is for Automotive application
- High temperature D-S leakage guarantee
Avalanche rating

Outline

RENESAS Package code: PRSP0008DD-D
(Package name: SOP-8 <FP-8DAV>)



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings				Unit
		HAT3018R		HAT3018RJ		
		Nch	Pch	Nch	Pch	
Drain to source voltage	V _{DSS}	60	−60	60	−60	V
Gate to source voltage	V _{GSS}	±20	±20	±20	±20	V
Drain current	I _D	6	−5	6	−5	A
Drain peak current	I _D (pulse) ^{Note1}	48	−40	48	−40	A
Avalanche current	I _{AP} ^{Note4}	—	—	6	−5	A
Avalanche energy	E _{AR} ^{Note4}	—	—	3.08	2.14	mJ
Channel dissipation	P _{ch} ^{Note2}	2	2	2	2	W
Channel dissipation	P _{ch} ^{Note3}	3	3	3	3	W
Channel temperature	T _{ch}	150	150	150	150	°C
Storage temperature	T _{stg}	−55 to +150	−55 to +150	−55 to +150	−55 to +150	°C

Notes: 1. PW ≤ 10μs, duty cycle ≤ 1%

2. 1 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW ≤ 10 s

3. 2 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW ≤ 10 s

4. Value at T_{ch} = 25°C, R_g ≥ 50 Ω

Electrical Characteristics

• N Channel

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	60	—	—	V	I _D = 10 mA, V _{GS} = 0
Gate to Source breakdown voltage	V _{(BR)GSS}	±20	—	—	V	I _G = ±100 μA, V _{DS} = 0
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	V _{DS} = 60 V, V _{GS} = 0
Zero gate voltage drain current	HAT3018R	I _{DSS}	—	—	μA	V _{DS} = 48 V, V _{GS} = 0 Ta = 125°C
	HAT3018RJ	I _{DSS}	—	10	μA	
Gate to source leak current	I _{GSS}	—	—	±10	μA	V _{GS} = ±16 V, V _{DS} = 0
Gate to source cutoff voltage	V _{GS(off)}	1.5	—	2.5	V	V _{DS} = 10 V, I _D = 1 mA
Forward transfer admittance	y _{fs}	6	9.5	—	S	I _D = 3 A ^{Note 5} , V _{DS} = 10 V
Static drain to source on state resistance	R _{DS(on)}	—	28	35	mΩ	I _D = 3 A ^{Note 5} , V _{GS} = 10 V
	R _{DS(on)}	—	40	50	mΩ	I _D = 3 A ^{Note 5} , V _{GS} = 4.5 V
Input capacitance	C _{iss}	—	1000	—	pF	V _{DS} = 10 V, V _{GS} = 0 f = 1 MHz
Output capacitance	C _{oss}	—	145	—	pF	
Reverse transfer capacitance	C _{rss}	—	85	—	pF	
Total gate charge	Q _g	—	15	—	nC	V _{DD} = 25 V
Gate to source charge	Q _{gs}	—	2	—	nC	V _{GS} = 10 V
Gate to drain charge	Q _{gd}	—	3	—	nC	I _D = 6 A
Turn-on delay time	t _{d(on)}	—	12	—	ns	V _{GS} = 10 V, I _D = 3 A V _{DD} ≅ 30 V
Rise time	t _r	—	10	—	ns	
Turn-off delay time	t _{d(off)}	—	60	—	ns	R _L = 10 Ω R _G = 4.7 Ω
Fall time	t _f	—	11	—	ns	
Body-drain diode forward voltage	V _{DF}	—	0.82	1.07	V	I _F = 6 A, V _{GS} = 0 ^{Note 5}
Body-drain diode reverse recovery time	t _{rr}	—	40	—	ns	I _F = 6 A, V _{GS} = 0 diF/dt = 100 A/μs

Notes: 5. Pulse test

• P Channel

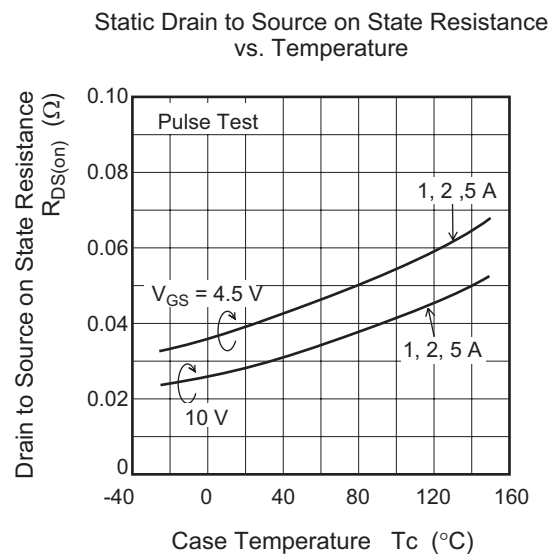
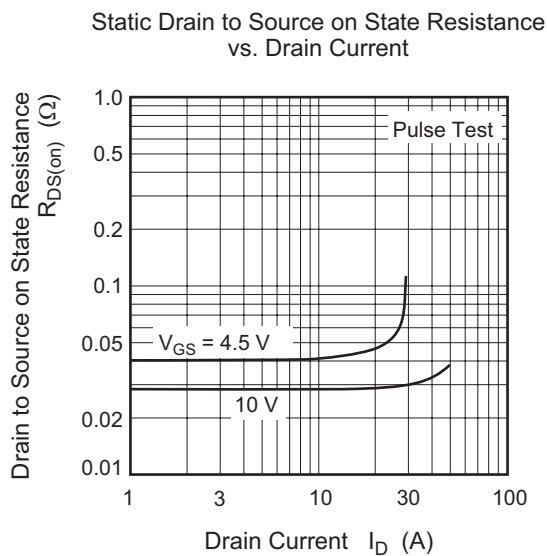
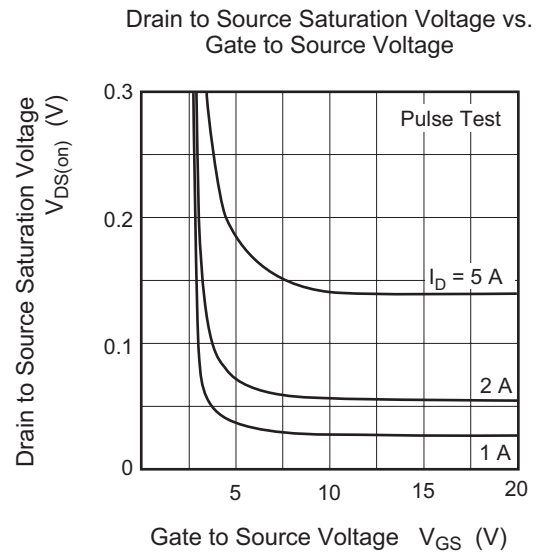
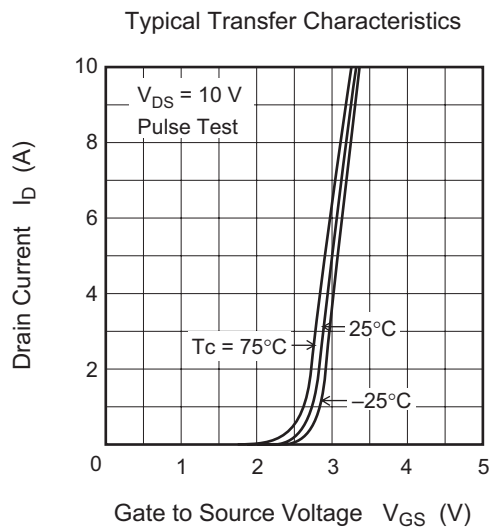
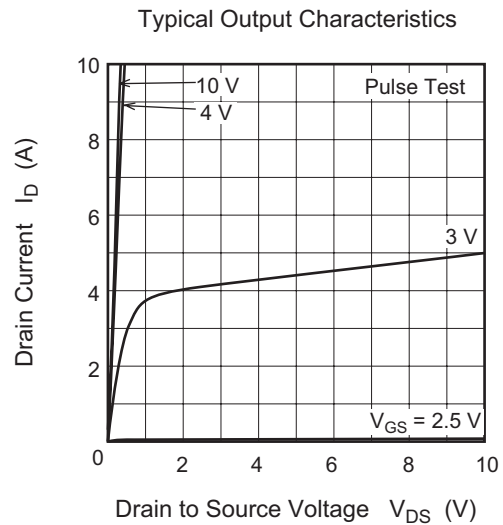
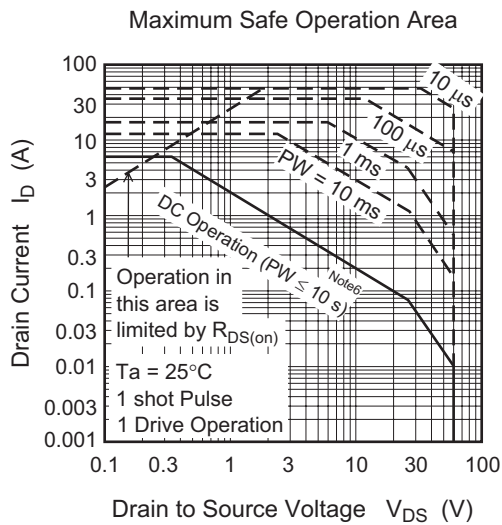
(Ta = 25°C)

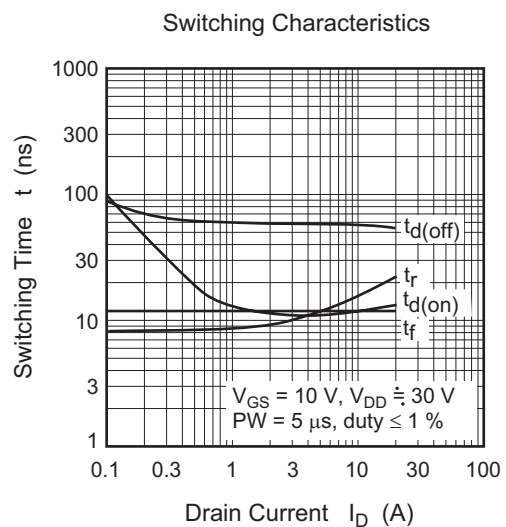
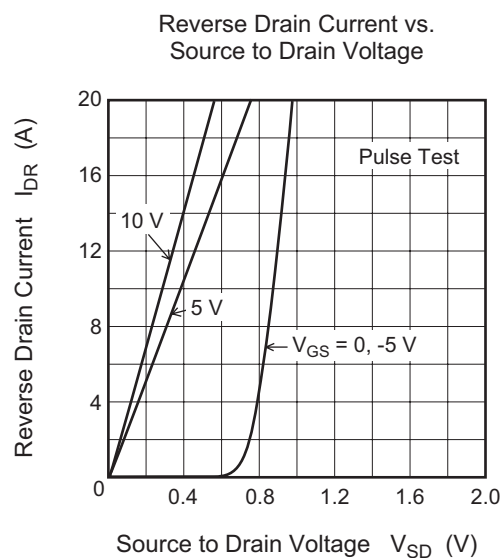
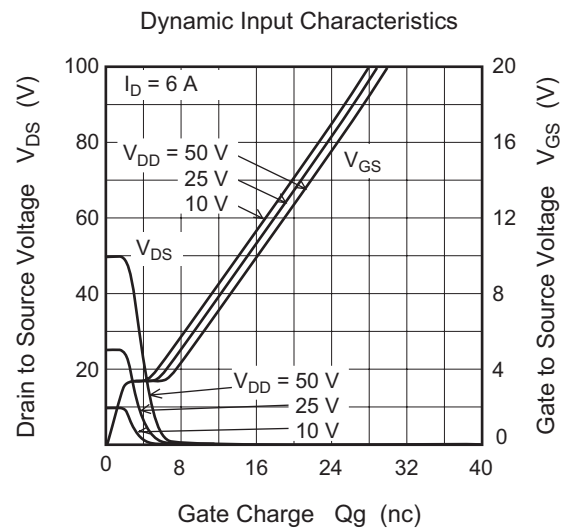
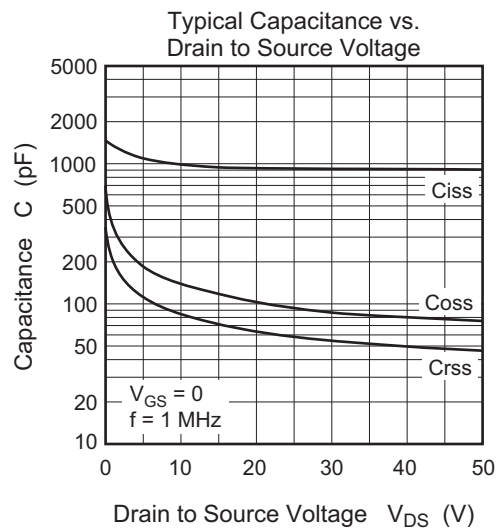
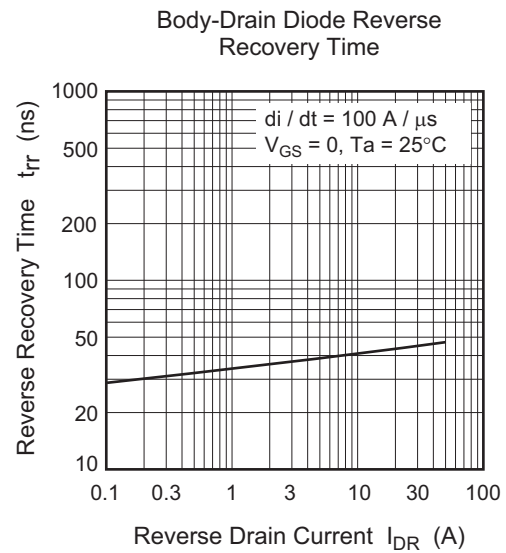
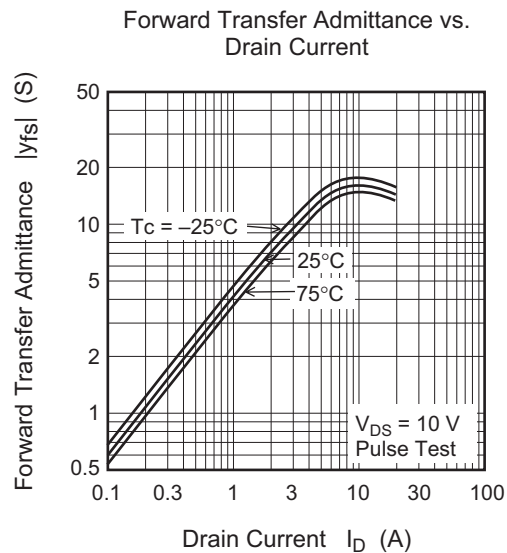
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	—	—	V	$I_D = -10 \text{ mA}$, $V_{GS} = 0$
Gate to Source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 100 \text{ }\mu\text{A}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	-1	μA	$V_{DS} = -60 \text{ V}$, $V_{GS} = 0$
Zero gate voltage drain current	HAT3018R	I_{DSS}	—	—	μA	$V_{DS} = -48 \text{ V}$, $V_{GS} = 0$
	HAT3018RJ	I_{DSS}	—	-10	μA	$T_a = 125^\circ\text{C}$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	—	-2.5	V	$V_{DS} = -10 \text{ V}$, $I_D = -1 \text{ mA}$
Forward transfer admittance	$ y_{fs} $	3	5	—	S	$I_D = -2.5 \text{ A}$ ^{Note 5} , $V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	60	76	m Ω	$I_D = -2.5 \text{ A}$ ^{Note 5} , $V_{GS} = -10 \text{ V}$
	$R_{DS(on)}$	—	90	130	m Ω	$I_D = -2.5 \text{ A}$ ^{Note 5} , $V_{GS} = -4.5 \text{ V}$
Input capacitance	C_{iss}	—	1350	—	pF	$V_{DS} = -10 \text{ V}$, $V_{GS} = 0$
Output capacitance	C_{oss}	—	135	—	pF	$f = 1 \text{ MHz}$
Reverse transfer capacitance	C_{rss}	—	85	—	pF	
Total gate charge	Q_g	—	21	—	nC	$V_{DD} = -25 \text{ V}$
Gate to source charge	Q_{gs}	—	3	—	nC	$V_{GS} = -10 \text{ V}$
Gate to drain charge	Q_{gd}	—	4	—	nC	$I_D = -5 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	20	—	ns	$V_{GS} = -10 \text{ V}$, $I_D = -2.5 \text{ A}$
Rise time	t_r	—	15	—	ns	$V_{DD} \cong -30 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	55	—	ns	$R_L = 12 \text{ }\Omega$
Fall time	t_f	—	10	—	ns	$R_G = 4.7 \text{ }\Omega$
Body-drain diode forward voltage	V_{DF}	—	-0.85	-1.10	V	$I_F = -5 \text{ A}$, $V_{GS} = 0$ ^{Note 5}
Body-drain diode reverse recovery time	t_{rr}	—	25	—	ns	$I_F = -5 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

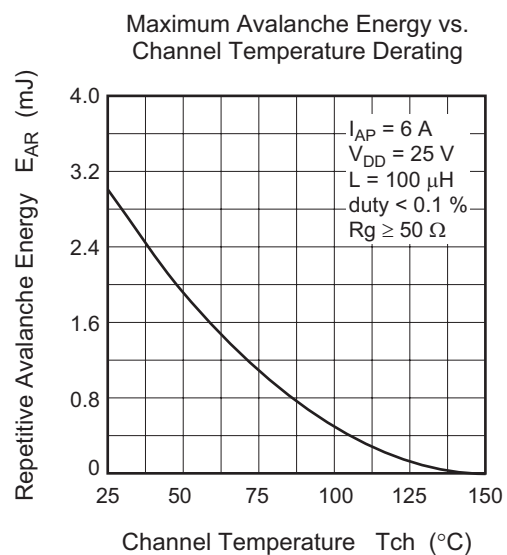
Notes: 5. Pulse test

Main Characteristics

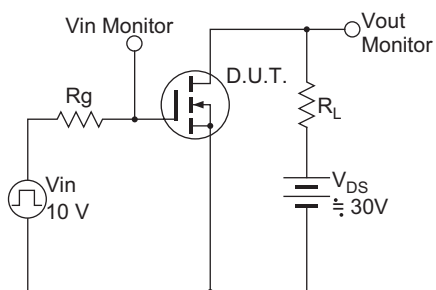
• N Channel



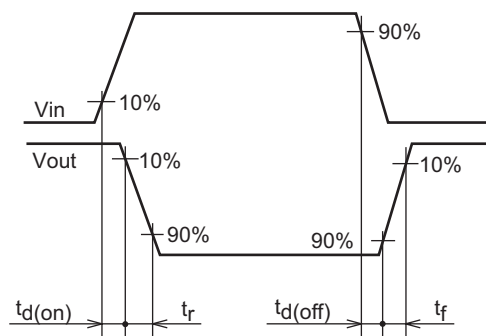




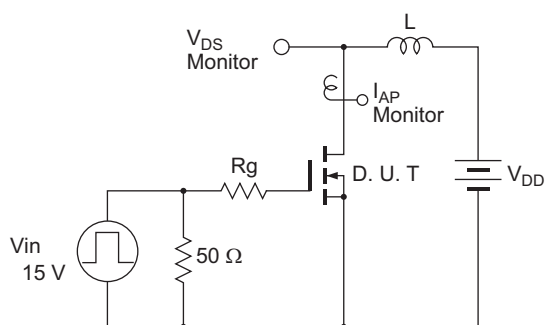
Switching Time Test Circuit



Switching Time Waveform

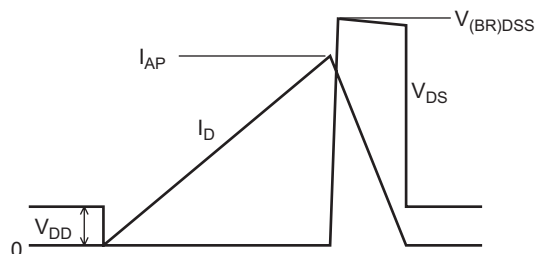


Avalanche Test Circuit



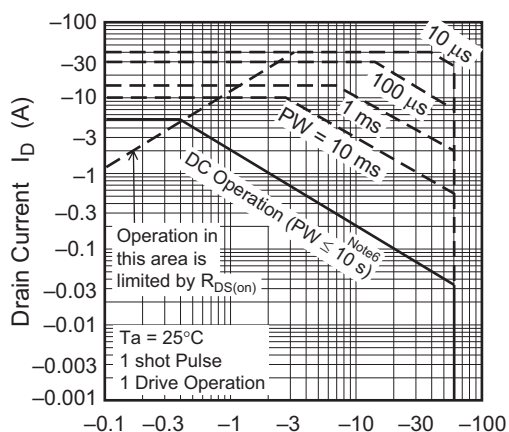
Avalanche Waveform

$$E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$



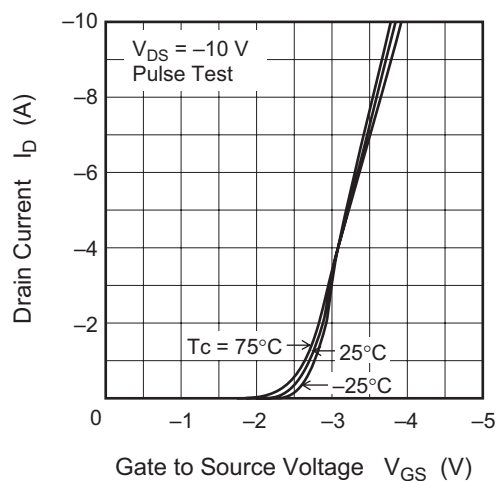
• P Channel

Maximum Safe Operation Area

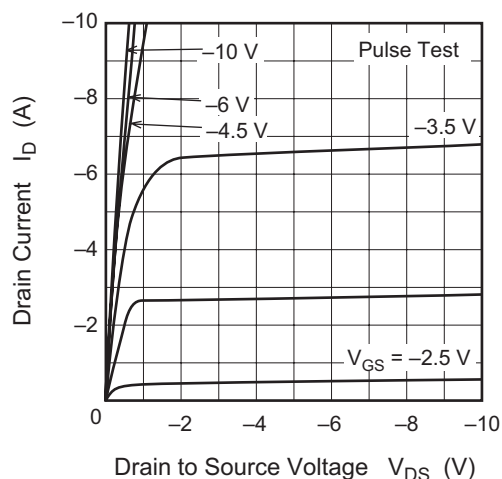
Drain to Source Voltage V_{DS} (V)

Note 6: When using the glass epoxy board (FR4 40 × 40 × 1.6 mm)

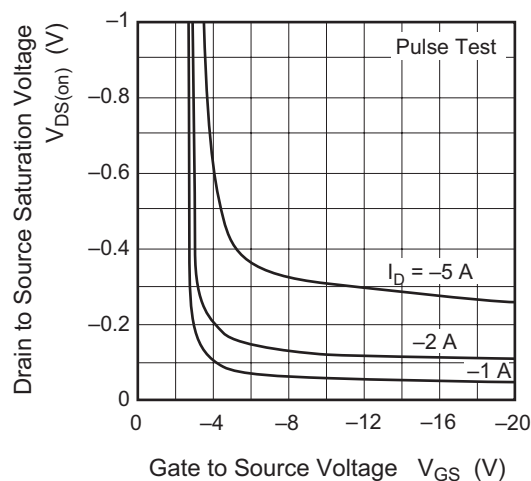
Typical Transfer Characteristics

Gate to Source Voltage V_{GS} (V)

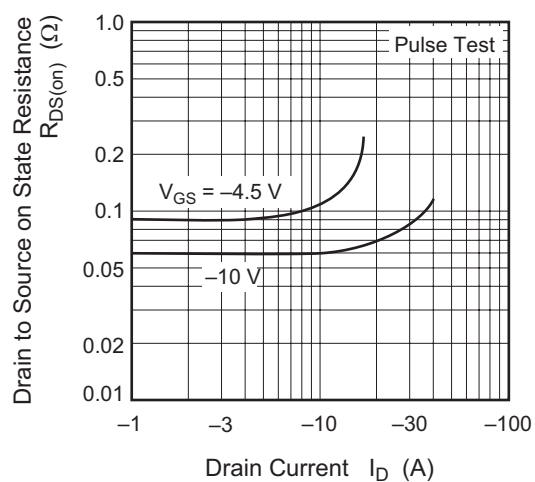
Typical Output Characteristics

Drain to Source Voltage V_{DS} (V)

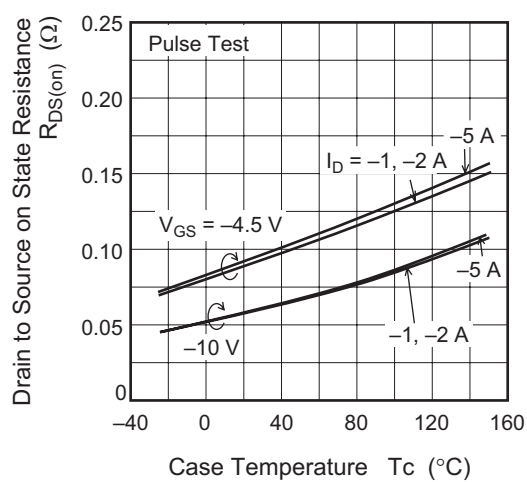
Drain to Source Saturation Voltage vs. Gate to Source Voltage

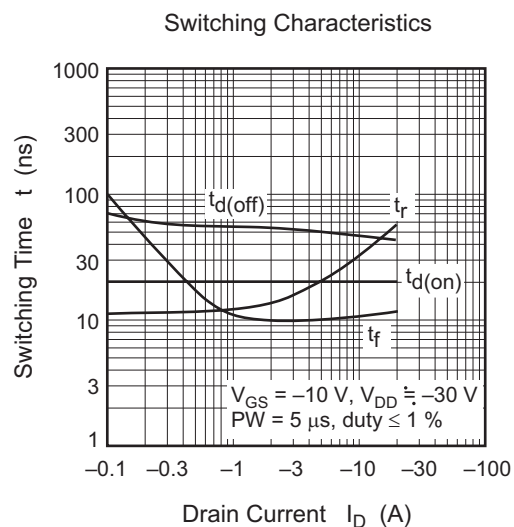
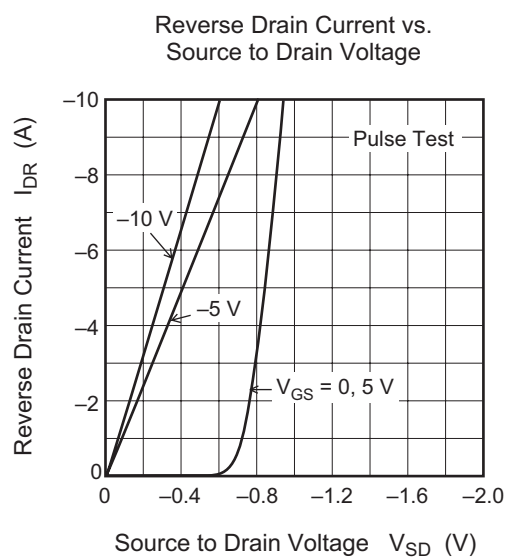
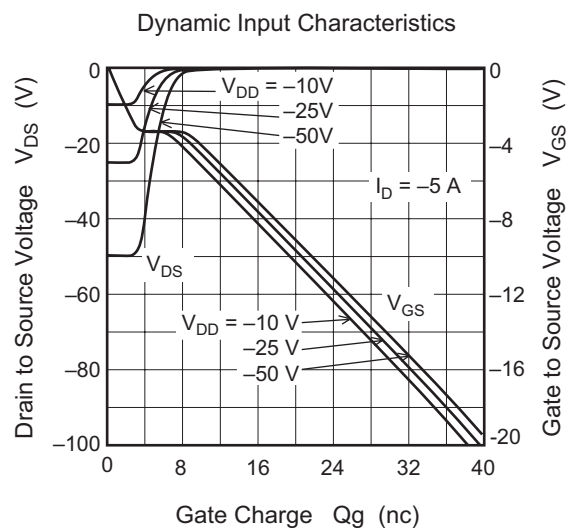
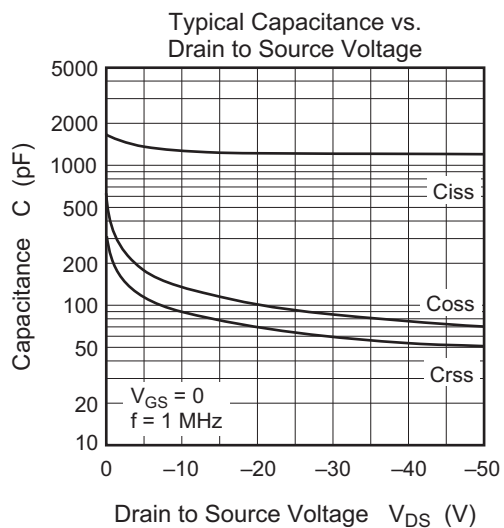
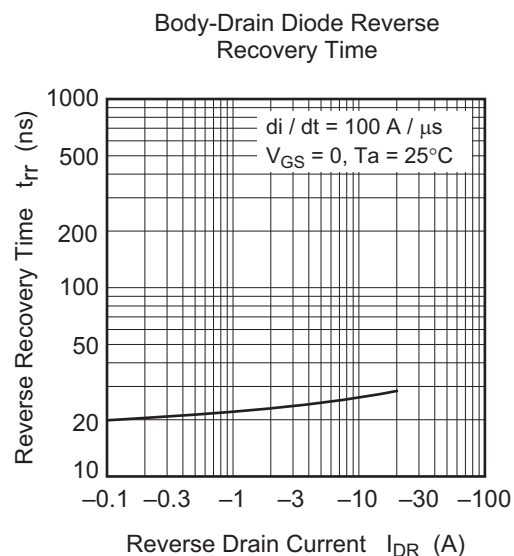
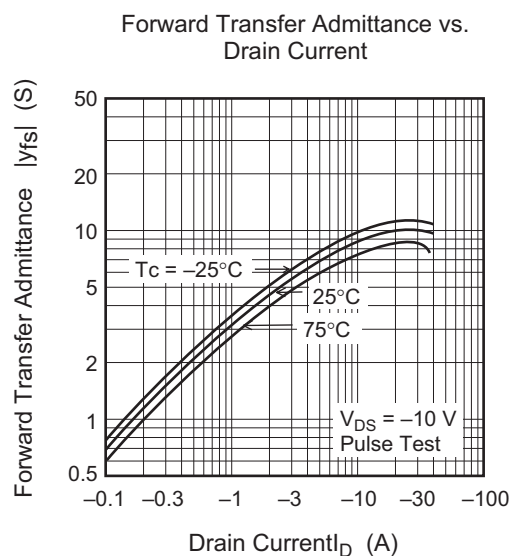
Gate to Source Voltage V_{GS} (V)

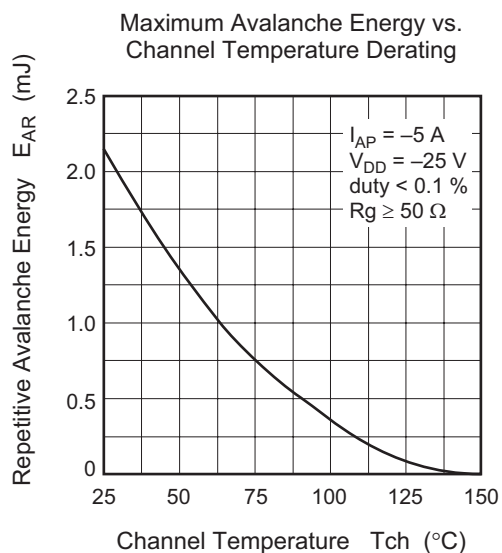
Static Drain to Source on State Resistance vs. Drain Current

Drain Current I_D (A)

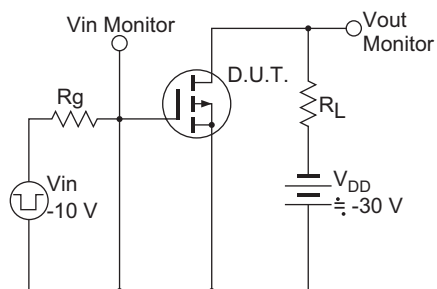
Static Drain to Source on State Resistance vs. Temperature

Case Temperature T_c ($^\circ\text{C}$)

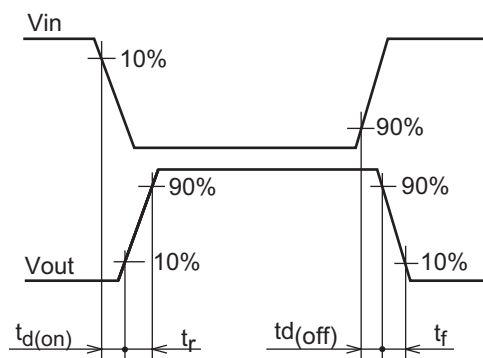




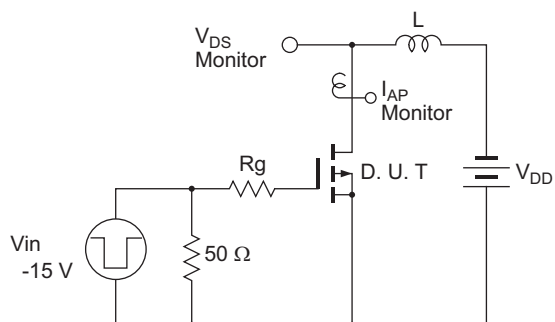
Switching Time Test Circuit



Switching Time Waveform

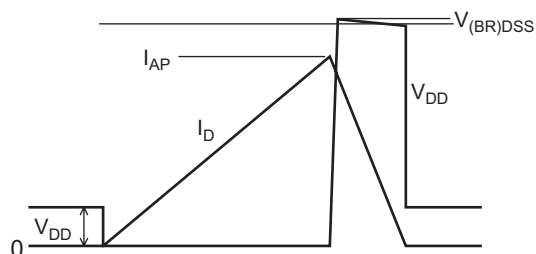


Avalanche Test Circuit

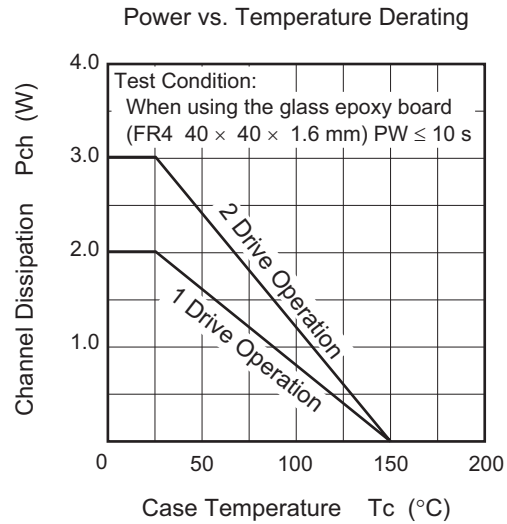


Avalanche Waveform

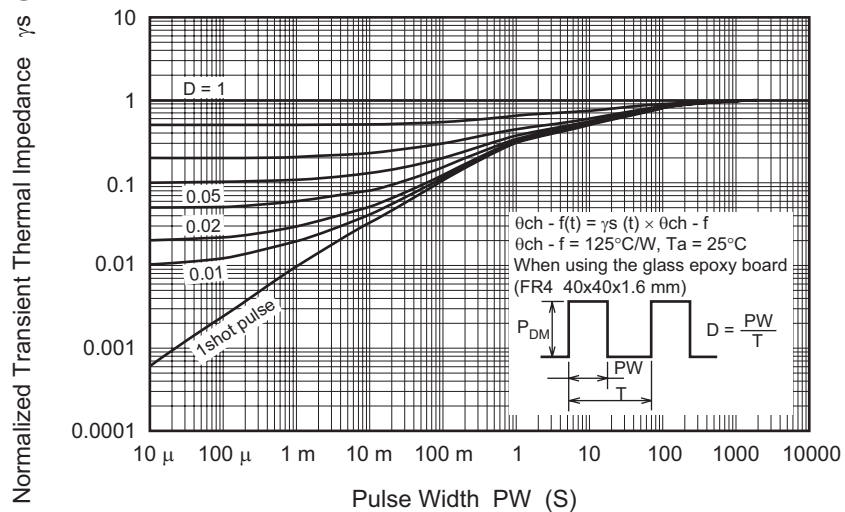
$$E_{AR} = \frac{1}{2} L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$



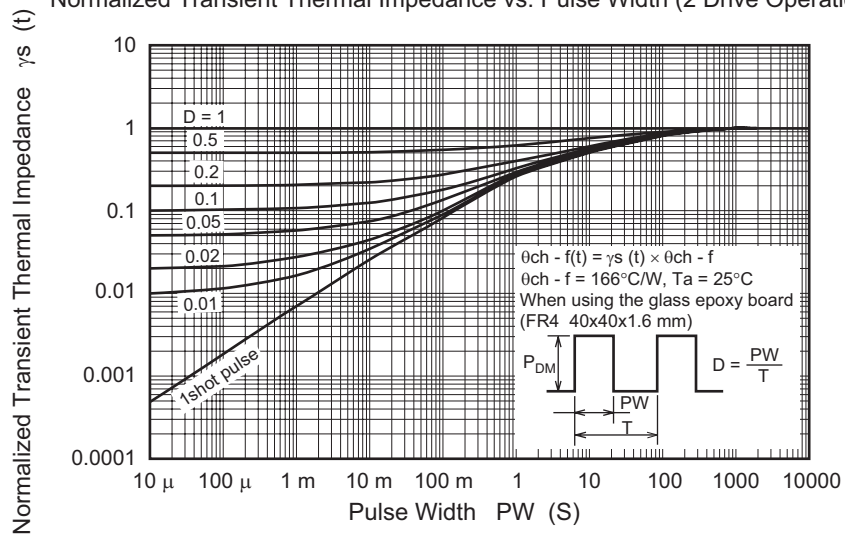
• In common



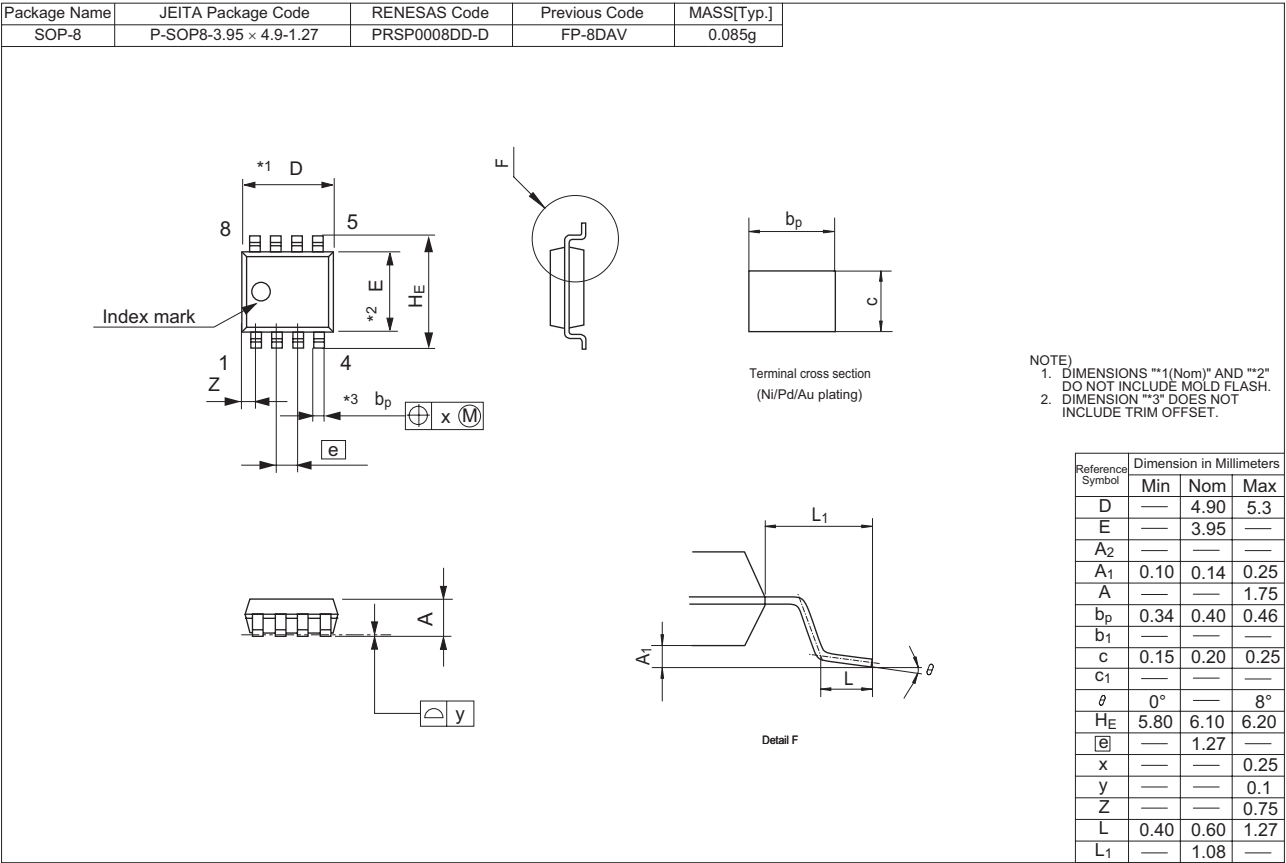
Normalized Transient Thermal Impedance vs. Pulse Width (1 Drive Operation)



Normalized Transient Thermal Impedance vs. Pulse Width (2 Drive Operation)



Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
HAT3018R-EL-E	2500 pcs.	Taping
HAT3018RJ-EL-E	2500 pcs.	Taping

Notes:

1. This document is provided for reference purposes only so that Renesas customers may select the appropriate Renesas products for their use. Renesas neither makes warranties or representations with respect to the accuracy or completeness of the information contained in this document nor grants any license to any intellectual property rights or any other rights of Renesas or any third party with respect to the information in this document.
2. Renesas shall have no liability for damages or infringement of any intellectual property or other rights arising out of the use of any information in this document, including, but not limited to, product data, diagrams, charts, programs, algorithms, and application circuit examples.
3. You should not use the products or the technology described in this document for the purpose of military applications such as the development of weapons of mass destruction or for the purpose of any other military use. When exporting the products or technology described herein, you should follow the applicable export control laws and regulations, and procedures required by such laws and regulations.
4. All information included in this document such as product data, diagrams, charts, programs, algorithms, and application circuit examples, is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas products listed in this document, please confirm the latest product information with a Renesas sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas such as that disclosed through our website. (<http://www.renesas.com>)
5. Renesas has used reasonable care in compiling the information included in this document, but Renesas assumes no liability whatsoever for any damages incurred as a result of errors or omissions in the information included in this document.
6. When using or otherwise relying on the information in this document, you should evaluate the information in light of the total system before deciding about the applicability of such information to the intended application. Renesas makes no representations, warranties or guarantees regarding the suitability of its products for any particular application and specifically disclaims any liability arising out of the application and use of the information in this document or Renesas products.
7. With the exception of products specified by Renesas as suitable for automobile applications, Renesas products are not designed, manufactured or tested for applications or otherwise in systems the failure or malfunction of which may cause a direct threat to human life or create a risk of human injury or which require especially high quality and reliability such as safety systems, or equipment or systems for transportation and traffic, healthcare, combustion control, aerospace and aeronautics, nuclear power, or undersea communication transmission. If you are considering the use of our products for such purposes, please contact a Renesas sales office beforehand. Renesas shall have no liability for damages arising out of the uses set forth above.
8. Notwithstanding the preceding paragraph, you should not use Renesas products for the purposes listed below:
 - (1) artificial life support devices or systems
 - (2) surgical implantations
 - (3) healthcare intervention (e.g., excision, administration of medication, etc.)
 - (4) any other purposes that pose a direct threat to human lifeRenesas shall have no liability for damages arising out of the uses set forth in the above and purchasers who elect to use Renesas products in any of the foregoing applications shall indemnify and hold harmless Renesas Technology Corp., its affiliated companies and their officers, directors, and employees against any and all damages arising out of such applications.
9. You should use the products described herein within the range specified by Renesas, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas shall have no liability for malfunctions or damages arising out of the use of Renesas products beyond such specified ranges.
10. Although Renesas endeavors to improve the quality and reliability of its products, IC products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard against the possibility of physical injury, and injury or damage caused by fire in the event of a failure of a Renesas product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other applicable measures. Among others, since the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
11. In case Renesas products listed in this document are detached from the products to which the Renesas products are attached or affixed, the risk of accident such as swallowing by infants and small children is very high. You should implement safety measures so that Renesas products may not be easily detached from your products. Renesas shall have no liability for damages arising out of such detachment.
12. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written approval from Renesas.
13. Please contact a Renesas sales office if you have any questions regarding the information contained in this document, Renesas semiconductor products, or if you have any other inquiries.



RENESAS SALES OFFICES

<http://www.renesas.com>

Refer to "<http://www.renesas.com/en/network>" for the latest and detailed information.

Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510