

# RKR0503AKH

# Silicon Schottky Barrier Diode for Rectifying

REJ03G1740-0100 Rev.1.00 Nov 17, 2008

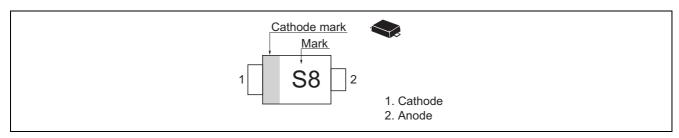
### **Features**

- Low forward voltage drop and suitable for high efficiency rectifying.
- Thin Ultra small Resin Package (TURP) is suitable for compact and high-density surface mount design.

# **Ordering Information**

Part No.	Laser Mark	Package Name	Package Code	
RKR0503AKH	S8	TURP	PUSF0002ZC-A	

# **Pin Arrangement**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	$V_{RRM}$	30	V
Reverse voltage	V <sub>R</sub>	30	V
Average rectified current	Io *1 *2	0.5	А
Non-Repetitive peak forward surge current	I <sub>FSM</sub> * <sup>3</sup>	3	Α
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. See from Fig.6 with Glass epoxy board.

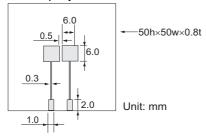
- 2. Ta = 62°C, With Glass epoxy board (board size: 50 mm  $\times$  50 mm, Land size 6 mm  $\times$  6 mm) Short form wave ( $\theta$ 180°C),  $V_R$  = 10 V.
- 3. 10 ms sine wave 1 pulse.

### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V <sub>F</sub>			0.37	V	I <sub>F</sub> = 500 mA
Reverse current	I <sub>R</sub>	_	_	500	μΑ	V <sub>R</sub> = 30 V
Thermal resistance	Rth(j-a)	_	200	_	°C/W	Glass epoxy board *1

Notes: 1. Glass epoxy board



3. TURP is the structure which radiates heat to a substrate, please perform mounting to a substrate by reflow.

### **Main Characteristic**

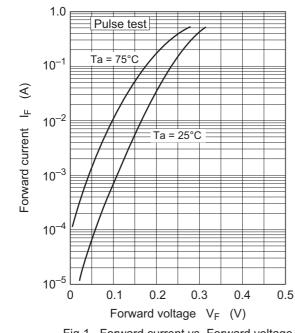


Fig.1 Forward current vs. Forward voltage

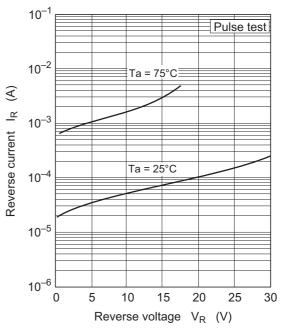
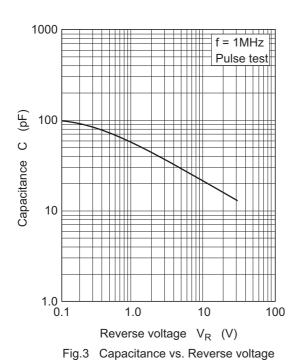
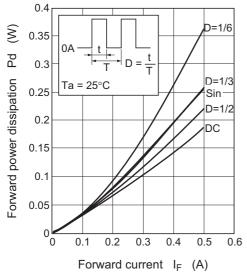


Fig.2 Reverse current vs. Reverse voltage





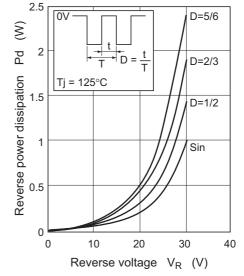


Fig.4 Forward power dissipation vs. Forward current

Fig.5 Reverse power dissipation vs. Reverse voltage

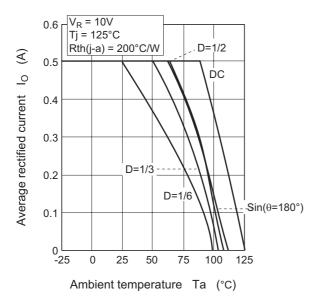
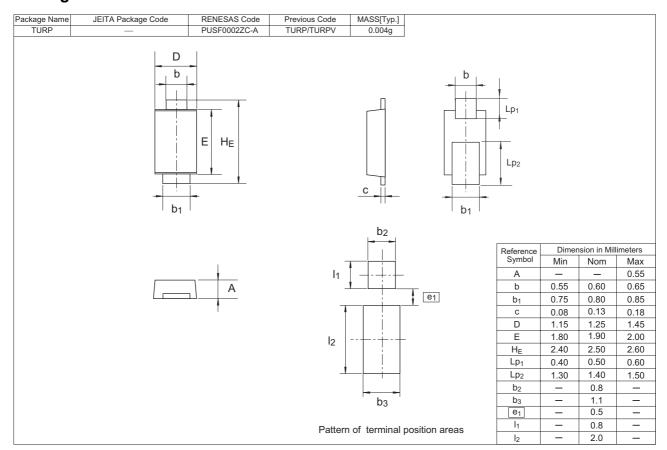


Fig.6 Average rectified current vs. Ambient temperature

# **Package Dimensions**



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