

# H7N0308LD, H7N0308LS, H7N0308LM

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1124-0500

(Previous: ADE-208-1535C)

Rev.5.00

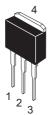
Apr 07, 2006

### **Features**

- Low on-resistance  $R_{DS (on)} = 3.8 \text{ m}\Omega \text{ typ.}$
- Low drive current
- 4.5 V gate drive device can be driven from 5 V source

#### **Outline**

RENESAS Package code: PRSS0004AE-A (Package name: LDPAK (L) )



H7N0308LD

RENESAS Package code: PRSS0004AE-C (Package name: LDPAK (S)-(2) )



H7N0308LM

RENESAS Package code: PRSS0004AE-B (Package name: LDPAK (S)-(1) )



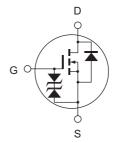
1. Gate

2. Drain

3. Source

4. Drain

H7N0308LS



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	70	Α
Drain peak current	I <sub>D (pulse)</sub> Note 1	280	Α
Body to drain diode reverse drain current	I <sub>DR</sub>	70	А
Channel dissipation	Pch Note 2	100	W
Channel to case thermal impedance	θ ch-c	1.25	°C/W
Channel to ambient thermal impedance	θ ch-a	89	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tc = 25°C

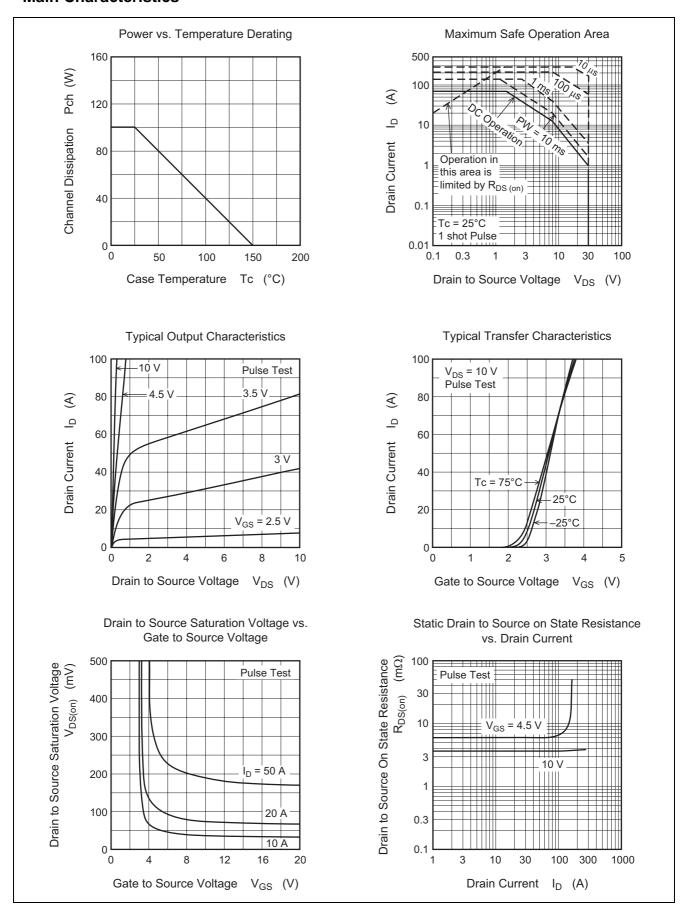
#### **Electrical Characteristics**

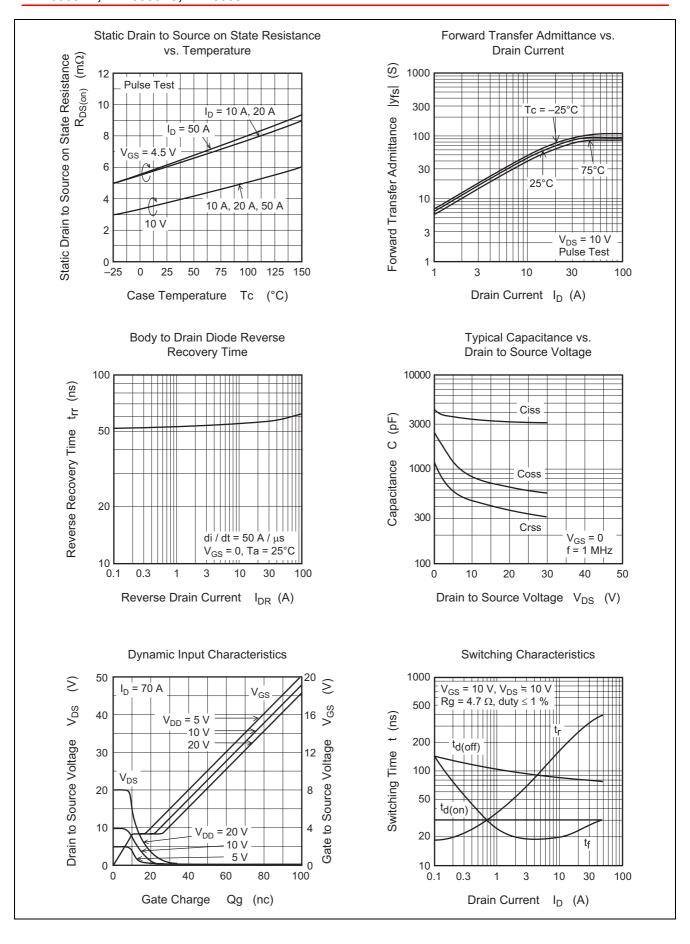
 $(Ta = 25^{\circ}C)$ 

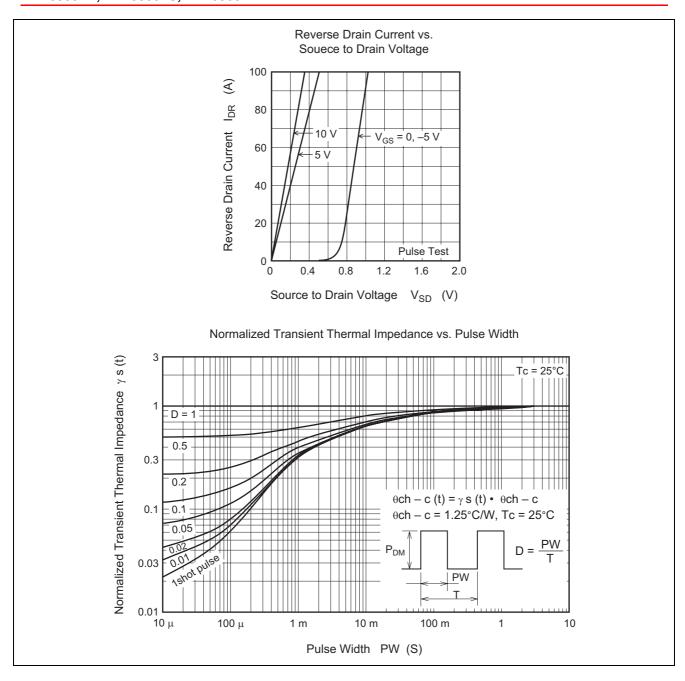
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	30	_		>	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR)</sub> GSS	±20	_		V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	10	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS\ (off)}$	1.0	_	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}^{\text{Note 3}}$
Static drain to source on state	R <sub>DS (on)</sub>	_	3.8	4.8	mΩ	$I_D = 35 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note } 3}$
resistance		_	6.0	8.5	mΩ	$I_D = 35 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	54	90	_	S	$I_D = 35 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note } 3}$
Input capacitance	Ciss	_	3350	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	840	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	480	_	pF	f = 1 MHz
Total gate charge	Qg	_	52	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	11	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	10	_	nC	$I_D = 70 \text{ A}$
Turn-on delay time	t <sub>d (on)</sub>	_	30	_	ns	$V_{GS} = 10 \text{ V}, I_D = 35 \text{ A}$
Rise time	t <sub>r</sub>	_	370	_	ns	$R_L = 0.29 \Omega$
Turn-off delay time	t <sub>d (off)</sub>	_	80	_	ns	$Rg = 4.7 \Omega$
Fall time	t <sub>f</sub>	_	27	_	ns	
Body to drain diode forward voltage	$V_{DF}$	_	0.93	_	V	I <sub>F</sub> = 70 A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery	t <sub>rr</sub>	_	60	_	ns	$I_F = 70 \text{ A}, V_{GS} = 0$
time						di <sub>F</sub> /dt = 50 A/μs

Note: 3. Pulse test

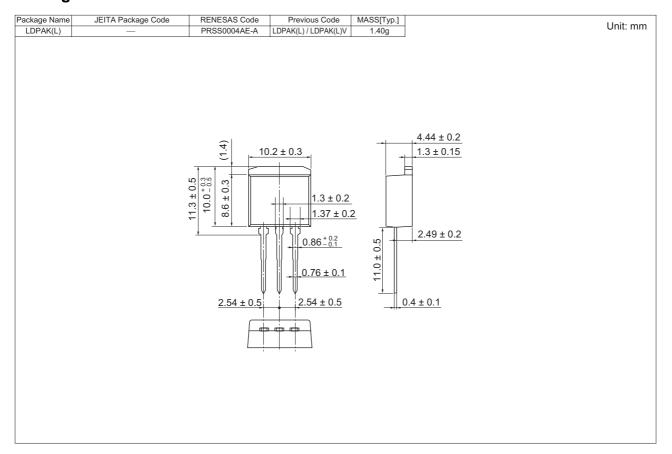
#### **Main Characteristics**

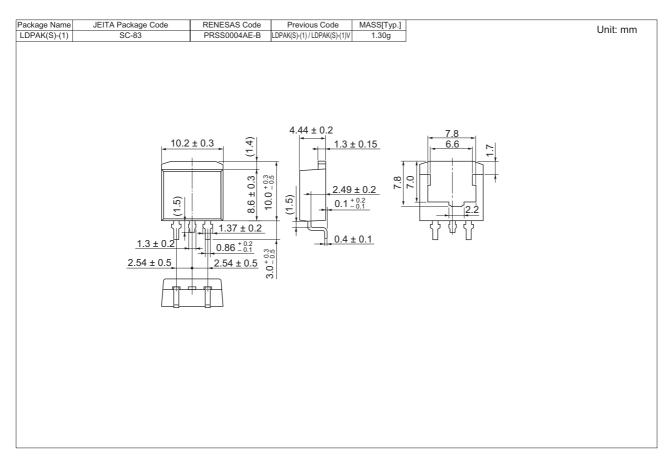


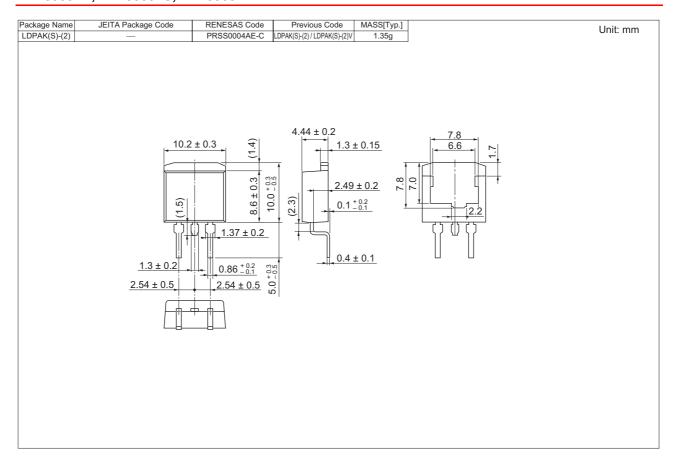




## **Package Dimensions**







# **Ordering Information**

Part Name	Quantity	Shipping Container
H7N0308LD-E	500 pcs	Box (Conductive Sack)
H7N0308LSTL-E	1000 pcs	Taping
H7N0308LMTL-E	1000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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