**Product data sheet** 

# **Product profile**

## 1.1 General description

Hyperfast, epitaxial rectifier diode in a SOD113 (2-lead TO-220F) plastic package.

#### 1.2 Features

- Extremely fast switching
- Low reverse recovery current
- Reduces switching loss in associated **MOSFET**
- Low thermal resistance
- Isolated package

## 1.3 Applications

- Half-bridge or full-bridge switched-mode Continuous Current Mode (CCM) Power power supplies
- Half-bridge lighting ballasts
- Factor Correction (PFC)

#### 1.4 Quick reference data

- $V_{RRM} \le 600 \text{ V}$
- $V_F = 1.54 \text{ V (typ)}$

- I<sub>F(AV)</sub> ≤ 20 A
- $t_{rr} = 19 \text{ ns (typ)}$

# **Pinning information**

Table 1. **Pinning** 

| Pin | Description             | Simplified outline    | Symbol  |
|-----|-------------------------|-----------------------|---|
| 1   | cathode (k)             |                       | . 14  |
| 2   | anode (a)               | mb                    | k <del>-                                   </del> |
| mb  | mounting base; isolated |                       |   |
|     |                         | SOD113 (2-lead TO-22) | OF)   |



# 3. Ordering information

## Table 2. Ordering information

| Type number | Package | Package   |         |  |  |  |  |
|-------------|---------|---|---------|--|--|--|--|
|             | Name    | Description   | Version |  |  |  |  |
| BYC20X-600  | TO-220F | plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 2-lead TO-220 'full pack' | SOD113  |  |  |  |  |

# 4. Limiting values

#### Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol             | Parameter                           | Conditions  | Min | Max  | Unit |
|--------------------|-------------------------------------|---|-----|------|------|
| $V_{RRM}$          | repetitive peak reverse voltage     |   | -   | 600  | V    |
| $V_{RWM}$          | crest working reverse voltage       |   | -   | 600  | V    |
| $V_R$              | reverse voltage                     | square waveform; $\delta$ = 1.0; $T_h \le 100~^{\circ}C$              | -   | 500  | V    |
| I <sub>F(AV)</sub> | average forward current             | square waveform; $\delta$ = 0.5; $T_h \leq$ 25 $^{\circ}C$            | -   | 20   | Α    |
| I <sub>FRM</sub>   | repetitive peak forward current     | square waveform; $\delta$ = 0.5; $T_h \leq$ 25 °C; $t_p$ = 25 $\mu s$ | -   | 40   | Α    |
| I <sub>FSM</sub>   | non-repetitive peak forward current | t = 10 ms; sinusoidal waveform  | -   | 250  | Α    |
|                    |                                     | t = 8.3 ms; sinusoidal waveform                                       | -   | 274  | Α    |
| T <sub>stg</sub>   | storage temperature                 |   | -40 | +150 | °C   |
| T <sub>i</sub>     | junction temperature                |   | -   | 150  | °C   |

# 5. Thermal characteristics

#### Table 4. Thermal characteristics

| Symbol               | Parameter                                    | Conditions                           | Min | Тур | Max | Unit |
|----------------------|--|--------------------------------------|-----|-----|-----|------|
| $R_{th(j-h)}$        | thermal resistance from junction to heatsink | with heatsink compound; see Figure 1 | -   | -   | 2.6 | K/W  |
| R <sub>th(j-a)</sub> | thermal resistance from junction to ambient  | in free air                          | -   | 55  | -   | K/W  |

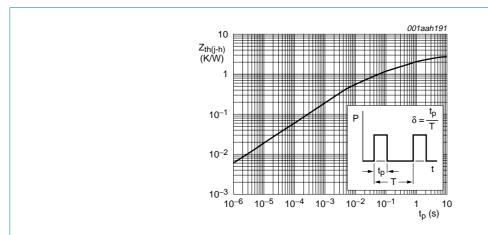


Fig 1. Transient thermal impedance from junction to heatsink as a function of pulse width

# 6. Isolation characteristics

## Table 5. Isolation limiting values and characteristics

 $T_h = 25 \,^{\circ}C$  unless otherwise specified.

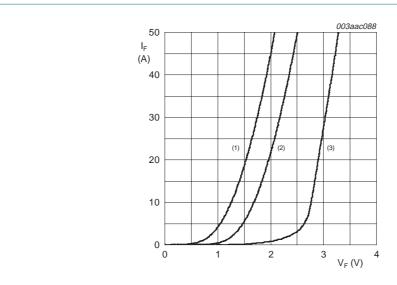
| Symbol            | Parameter             | Conditions  | Min | Тур | Max  | Unit |
|-------------------|-----------------------|---|-----|-----|------|------|
| $V_{isol(RMS)}$   | RMS isolation voltage | from all terminals to external heatsink;<br>f = 50  Hz to 60 Hz; sinusoidal waveform;<br>relative humidity $\leq 65 \%$ ; clean and dust free | -   | -   | 2500 | V    |
| C <sub>isol</sub> | isolation capacitance | from pin 1 (cathode) to external heatsink;<br>f = 1 MHz   | -   | 10  | -    | pF   |

# **Characteristics**

Table 6. **Characteristics** 

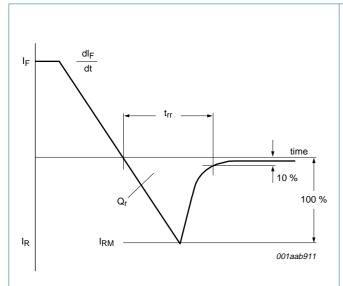
 $T_i = 25 \,^{\circ}C$  unless otherwise specified.

| Symbol          | Parameter                     | Conditions  | Min | Тур  | Max  | Unit |
|-----------------|-------------------------------|---|-----|------|------|------|
| Static char     | acteristics                   |   |     |      |      |      |
| V <sub>F</sub>  | forward voltage               | $I_F = 20 \text{ A}; T_j = 150 ^{\circ}\text{C}; \text{ see } \frac{\text{Figure 2}}{}$ | -   | 1.54 | 1.97 | V    |
|                 |                               | $I_F = 40 \text{ A}$ ; $T_j = 150 ^{\circ}\text{C}$ ; see Figure 2                      | -   | 1.95 | 2.34 | V    |
|                 |                               | I <sub>F</sub> = 20 A; see <u>Figure 2</u>  | -   | 1.89 | 2.9  | V    |
| $I_R$           | reverse current               | V <sub>R</sub> = 600 V  | -   | 16   | 200  | μΑ   |
|                 |                               | $V_R = 500 \text{ V}; T_j = 100 ^{\circ}\text{C}$                                       | -   | 1.6  | 3.0  | mΑ   |
| Dynamic c       | haracteristics                |   |     |      |      |      |
| t <sub>rr</sub> | reverse recovery time         | $I_F = 1$ A to $V_R = 30$ V; $dI_F/dt = 50$ A/ $\mu$ s; see Figure 3                    | -   | 35   | 55   | ns   |
|                 |                               | $I_F$ = 20 A to $V_R$ = 400 V; $dI_F/dt$ = 500 A/ $\mu$ s; see Figure 3                 |     |      |      |      |
|                 |                               | T <sub>j</sub> = 25 °C  | -   | 19   | -    | ns   |
|                 |                               | T <sub>j</sub> = 100 °C   | -   | 32   | 40   | ns   |
| I <sub>RM</sub> | peak reverse recovery current | $I_F$ = 20 A to $V_R$ = 400 V; $T_j$ = 125 °C; see Figure 3                             |     |      |      |      |
|                 |                               | $dI_F/dt = 50 A/\mu s$  | -   | 3.0  | 7.5  | Α    |
|                 |                               | $dI_F/dt = 500 A/\mu s$   | -   | 9.5  | 12   | Α    |
| $V_{FR}$        | forward recovery voltage      | $I_F = 20 \text{ A}$ ; $dI_F/dt = 100 \text{ A/}\mu\text{s}$ ; see Figure 4             | -   | 8    | 11   | V    |



- (1)  $T_j = 150 \,^{\circ}\text{C}$ ; typical values
- (2)  $T_j = 150 \,^{\circ}\text{C}$ ; maximum values
- (3)  $T_j = 25$  °C; maximum values

Fig 2. Forward current as a function of forward voltage



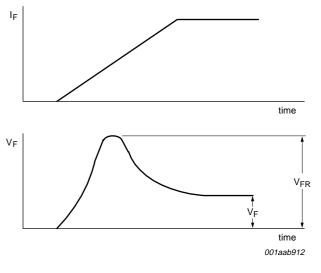
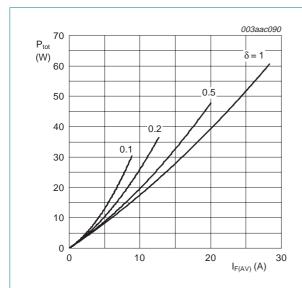


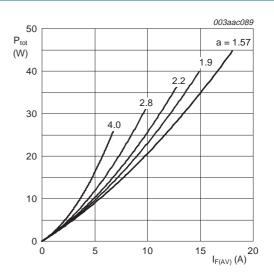
Fig 3. Reverse recovery definitions

Fig 4. Forward recovery definitions



 $I_{F(AV)} = I_{F(RMS)} \times \sqrt{\delta}$ 

Fig 5. Forward power dissipation as a function of average forward current; square waveform; maximum values



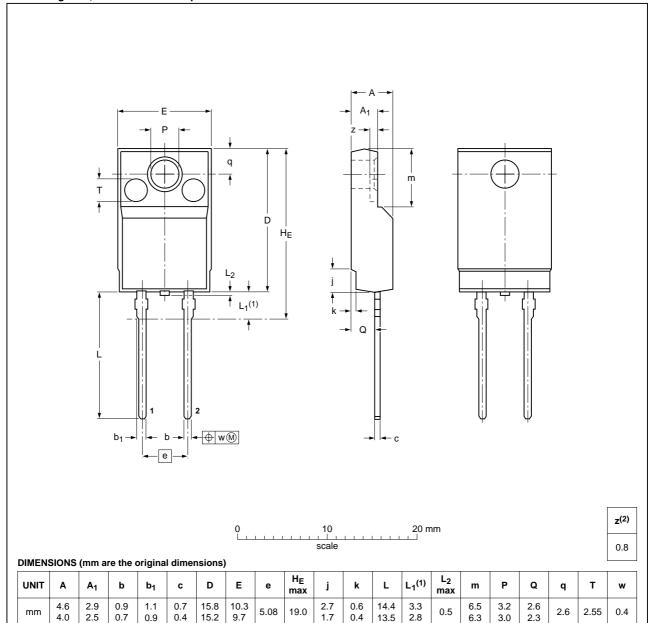
 $a = form factor = I_{F(RMS)} / I_{F(AV)}$ 

Fig 6. Forward power dissipation as a function of average forward current; sinusoidal waveform; maximum values

# **Package outline**

Plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 2-lead TO-220 'full pack'

SOD113



- Notes 1. Terminals are uncontrolled within zone L<sub>1</sub>.
- 2. z is depth of T.

| OUTLINE |     | REFER          | ENCES | EUROPEAN   | ISSUE DATE                      |  |
|---------|-----|----------------|-------|------------|---------------------------------|--|
| VERSION | IEC | JEDEC          | JEITA | PROJECTION | ISSUE DATE                      |  |
| SOD113  |     | 2-lead TO-220F |       |            | <del>02-04-09</del><br>07-06-18 |  |

Fig 7. Package outline SOD113 (2-lead TO-220F)

BYC20X-600\_1 © NXP B.V. 2007. All rights reserved.



# 9. Revision history

## Table 7. Revision history

| Document ID  | Release date | Data sheet status  | Change notice | Supersedes |
|--------------|--------------|--------------------|---------------|------------|
| BYC20X-600_1 | 20071129     | Product data sheet | -             | -          |

## 10. Legal information

#### 10.1 Data sheet status

| Document status[1][2]          | Product status[3] | Definition  |
|--------------------------------|-------------------|---|
| Objective [short] data sheet   | Development       | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification     | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production        | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

#### 10.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

#### 10.3 Disclaimers

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

Terms and conditions of sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <a href="http://www.nxp.com/profile/terms">http://www.nxp.com/profile/terms</a>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

#### 10.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

#### 11. Contact information

For additional information, please visit: http://www.nxp.com

For sales office addresses, send an email to: salesaddresses@nxp.com

## 12. Contents

| 1    | Product profile           |
|------|---------------------------|
| 1.1  | General description       |
| 1.2  | Features                  |
| 1.3  | Applications              |
| 1.4  | Quick reference data      |
| 2    | Pinning information 1     |
| 3    | Ordering information      |
| 4    | Limiting values 2         |
| 5    | Thermal characteristics 3 |
| 6    | Isolation characteristics |
| 7    | Characteristics 4         |
| 8    | Package outline           |
| 9    | Revision history 8        |
| 10   | Legal information 9       |
| 10.1 | Data sheet status         |
| 10.2 | Definitions9              |
| 10.3 | Disclaimers               |
| 10.4 | Trademarks9               |
| 11   | Contact information 9     |
| 12   | Contents 10               |

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

