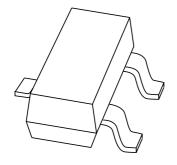
DISCRETE SEMICONDUCTORS

DATA SHEET



PLVA2600A series Low-voltage avalanche regulator double diodes

Product data sheet Supersedes data of 1999 May 10 2001 Oct 15



Low-voltage avalanche regulator double diodes

PLVA2600A series

FEATURES

- Very low dynamic impedance at low currents: approximately ¹/₂₀ of conventional series
- · Hard breakdown knee
- Low noise: approximately ¹/₁₀ of conventional series
- Total power dissipation: max. 250 mW
- Small tolerances of V_Z
- Working voltage range: nom. 5.0 to 6.8 V
- Non-repetitive peak reverse power dissipation: max. 30 W.

APPLICATIONS

- Low current, low power, low noise applications
- . CMOS RAM back-up circuits
- Voltage stabilizers
- Voltage limiters
- Smoke detector relays.

DESCRIPTION

The PLVA2600A series consists of two high performance voltage regulator diodes with common anodes, in small SOT23 plastic SMD packages.

The series consists of PLVA2650A to PLVA2668A.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
PLVA2650A	*9J
PLVA2653A	*9K
PLVA2656A	*9L
PLVA2659A	*9M
PLVA2662A	*9N
PLVA2665A	*90
PLVA2668A	*9P

Note

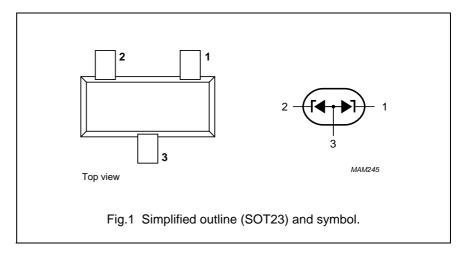
1. * = p: Made in Hong Kong.

* = t : Made in Malaysia.

* = W: Made in China.

PINNING

PIN	DESCRIPTION	
1	cathode (k1)	
2	cathode (k2)	
3	common anode	



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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _F	continuous forward current		-	250	mA
I _{ZRM}	repetitive peak working current	$t_p = 100 \ \mu s; \ \delta = 10\%$	_	250	mA
P _{ZSM}	non-repetitive peak reverse power dissipation	$t_p = 100 \ \mu s; \ T_j = 150 \ ^{\circ}C$	-	30	W
P _{tot}	total power dissipation	single diode loaded; T _{amb} = 25 °C; note 1	-	250	mW
		double diode loaded; T _{amb} = 25 °C; note 1	-	180	mW
T _{stg}	storage temperature		-65	+150	°C
T _i	junction temperature		_	150	°C

Note

^{1.} Device mounted on an FR4 printed circuit-board.

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ELECTRICAL CHARACTERISTICS

 $T_i = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	forward voltage	I _F = 10 mA	_	_	0.9	V
Vz	working voltage	I _Z = 250 μA				
	PLVA2650A		4.80	5.00	5.20	V
	PLVA2653A		5.10	5.30	5.50	V
	PLVA2656A		5.40	5.60	5.80	V
	PLVA2659A		5.70	5.90	6.10	V
	PLVA2662A		6.00	6.20	6.40	V
	PLVA2665A		6.30	6.50	6.70	V
	PLVA2668A		6.60	6.80	7.00	V
	working voltage	I _Z = 10 μA				
	PLVA2650A		_	4.30	_	V
	PLVA2653A		_	5.20	_	V
	PLVA2656A		_	5.51	_	V
	PLVA2659A		_	5.85	_	V
	PLVA2662A		_	6.19	_	V
	PLVA2665A		_	6.49	_	V
	PLVA2668A		_	6.80	_	V
R _Z	dynamic resistance	1 kHz superimposed;				
	PLVA2650A	I_{ZAC} is 10% of I_{ZDC} ; $I_Z = 250 \mu A$	_	_	700	Ω
	PLVA2653A		_	_	250	Ω
	PLVA2656A to PLVA2668A		_	_	100	Ω
Sz	temperature coefficient	I _Z = 250 μA				
	PLVA2650A		_	0.20	_	mV/K
	PLVA2653A		_	1.60	_	mV/K
	PLVA2656A		_	1.90	_	mV/K
	PLVA2659A		_	2.40	_	mV/K
	PLVA2662A		_	2.65	_	mV/K
	PLVA2665A		_	2.90	_	mV/K
	PLVA2668A		_	3.40	_	mV/K
I _R	reverse current	V _R = 80%; V _Z nominal				
	PLVA2650A		_	_	20000	nA
	PLVA2653A		_	_	5000	nA
	PLVA2656A		_	_	1000	nA
	PLVA2659A		_	_	500	nA
	PLVA2662A		_	_	100	nA
	PLVA2665A		_	_	50	nA
	PLVA2668A		_	_	10	nA

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SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _R	reverse current	V _R = 50%; V _Z nominal				
	PLVA2650A		_	34	_	nA
	PLVA2653A		_	22	_	nA
	PLVA2656A		_	1.1	_	nA
	PLVA2659A		_	0.9	_	nA
	PLVA2662A		_	0.9	_	nA
	PLVA2665A		_	0.9	_	nA
	PLVA2668A		_	0.8	_	nA
	reverse current	V _R = 90%; V _Z nominal				
	PLVA2650A		_	21	_	μΑ
	PLVA2653A		_	3.5	_	μΑ
	PLVA2656A		_	1.3	_	μΑ
	PLVA2659A		_	1.0	_	μΑ
	PLVA2662A		_	0.05	_	μΑ
	PLVA2665A		_	0.04	_	μΑ
	PLVA2668A		_	0.006	_	μΑ
ΔV_Z	line regulation					
	PLVA2659A to PLVA2668A	$I_{LO} = 10 \mu A; I_{Hi} = 1 mA$	_	_	0.1	V
	PLVA2656A	$I_{LO} = 50 \mu A; I_{Hi} = 1 mA$	_	_	0.1	V
	PLVA2650A	$I_{LO} = 100 \mu A; I_{Hi} = 1 mA$	_	_	0.4	V
	PLVA2653A	$I_{LO} = 100 \mu A; I_{Hi} = 1 mA$	_	_	0.2	V
V _n	noise voltage density	$f = 1 \text{ kHz}; B = 1 \text{ kHz}; I_Z = 250 \mu A$	_	_	1.0	μV
						$\frac{\mu V}{\sqrt{Hz}}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-tp}	thermal resistance from junction to tie-point		360	K/W
R _{th j-a}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Device mounted on an FR4 printed circuit-board.

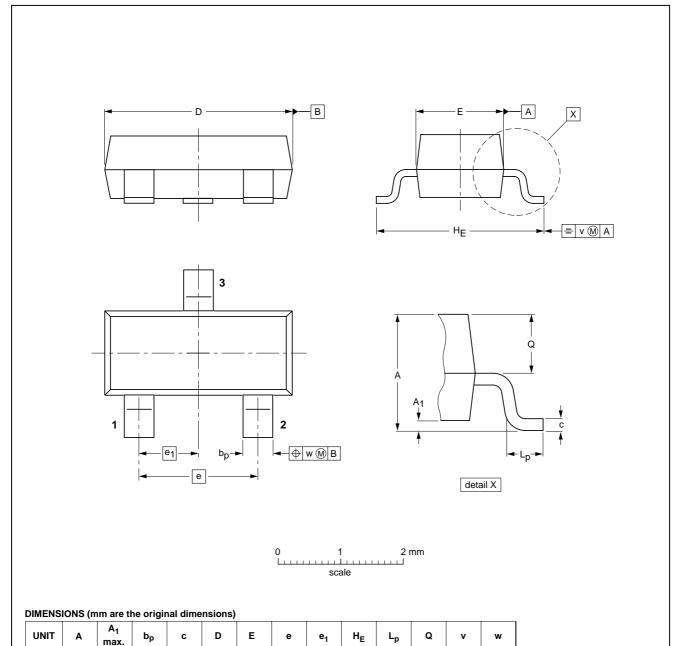
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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	1330E DATE
SOT23		TO-236AB				97-02-28 99-09-13

1.9

0.45

0.55

0.1

2001 Oct 15 6

0.48

0.38

0.15

1.1

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DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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