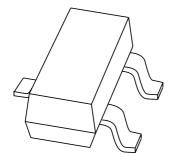
DISCRETE SEMICONDUCTORS

DATA SHEET



PLVA6xxA series Low-voltage avalanche regulator diodes

Product data sheet Supersedes data of 1999 May 25 2004 Jan 14



Low-voltage avalanche regulator diodes

PLVA6xxA series

FEATURES

- Very low dynamic impedance at low currents: approximately ¹/₂₀ of conventional series
- · Hard breakdown knee
- Low noise: approximately ¹/₁₀ of conventional series
- Total power dissipation: max. 250 mW
- Small tolerances of VZ
- Working voltage range: nominal 5.00 to 6.80 V
- Non-repetitive peak reverse power dissipation: maximal 30 W.

APPLICATIONS

- Low current, low power, low noise applications
- · CMOS RAM back-up circuits
- Voltage stabilizers
- · Voltage limiters
- Smoke detector relays.

DESCRIPTION

High performance voltage regulator diodes in small SOT23 plastic SMD packages.

The series consists of PLVA650A to PLVA668A.

MARKING

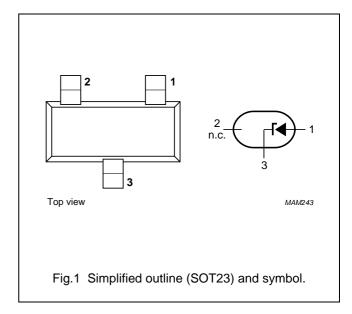
TYPE NUMBER	MARKING CODE(1)
PLVA650A	*9A
PLVA653A	*9B
PLVA656A	*9C
PLVA659A	*9D
PLVA662A	*9E
PLVA665A	*9F
PLVA668A	*9G

Note

- 1. * = p: Made in Hong Kong.
 - * = t: Made in Malaysia.
 - * = W: Made in China.

PINNING

PIN	DESCRIPTION	
1	anode	
2	not connected	
3	cathode	



Low-voltage avalanche regulator diodes

PLVA6xxA series

ORDERING INFORMATION

TYPE		PACKAGE			
NUMBER	NAME	DESCRIPTION VERSION			
PLVA6xxA	_	plastic surface mounted package; 3 leads	SOT23		

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _F	continuous forward current		_	250	mA
I _{ZRM}	repetitive peak working current	$t_p = 100 \ \mu s; \ \delta = 10\%$	_	250	mA
P _{ZSM}	non-repetitive peak reverse power dissipation	$t_p = 100 \ \mu s; T_j = 150 \ ^{\circ}C$	_	30	W
P _{tot}	total power dissipation	T _{amb} = 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C

Note

1. Device mounted on an FR4 printed circuit-board.

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PLVA6xxA series

ELECTRICAL CHARACTERISTICS

 T_j = 25 °C; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	forward voltage	I _F = 10 mA	_	_	0.9	V
Vz	working voltage	I _Z = 250 μA				
	PLVA650A		4.80	5.00	5.20	V
	PLVA653A		5.10	5.30	5.50	V
	PLVA656A		5.40	5.60	5.80	V
	PLVA659A		5.70	5.90	6.10	V
	PLVA662A		6.00	6.20	6.40	V
	PLVA665A		6.30	6.50	6.70	V
	PLVA668A		6.60	6.80	7.00	V
V _Z	working voltage	I _Z = 10 μA				
	PLVA650A		_	4.30	_	V
	PLVA653A		_	5.20	_	V
	PLVA656A		_	5.51	_	V
	PLVA659A		_	5.85	_	V
	PLVA662A		_	6.19	_	V
	PLVA665A		_	6.49	_	V
	PLVA668A		_	6.80	_	V
R _Z	dynamic resistance	1 kHz superimposed;				
	PLVA650A	I_{ZAC} is 10% of I_{ZDC} ; $I_Z = 250 \mu A$	_	_	700	Ω
	PLVA653A		_	_	250	Ω
	PLVA656A to PLVA668A		_	_	100	Ω
Sz	temperature coefficient	I _Z = 250 μA				
	PLVA650A		_	0.20	_	mV/K
	PLVA653A		_	1.60	_	mV/K
	PLVA656A		_	1.90	_	mV/K
	PLVA659A		_	2.40	_	mV/K
	PLVA662A		_	2.65	_	mV/K
	PLVA665A		_	2.90	_	mV/K
	PLVA668A		_	3.40	_	mV/K
I _R	reverse current	$V_R = 80\% V_Z$ nominal				
	PLVA650A		_	_	20000	nA
	PLVA653A		_	_	5000	nA
	PLVA656A		_	_	1000	nA
	PLVA659A		_	_	500	nA
	PLVA662A		_	_	100	nA
	PLVA665A		_	_	50	nA
	PLVA668A		_	_	10	nA

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Low-voltage avalanche regulator diodes

PLVA6xxA series

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _R	reverse current	V _R = 50% V _Z nominal				
	PLVA650A		_	34	_	nA
	PLVA653A		_	22	_	nA
	PLVA656A		_	1.1	_	nA
	PLVA659A		_	0.9	_	nA
	PLVA662A		_	0.9	_	nA
	PLVA665A		_	0.9	_	nA
	PLVA668A		_	8.0	_	nA
I _R	reverse current	V _R = 90% V _Z nominal				
	PLVA650A		_	21	_	μΑ
	PLVA653A		_	3.5	_	μΑ
	PLVA656A		_	1.3	_	μΑ
	PLVA659A		_	1.0	_	μΑ
	PLVA662A		_	0.05	_	μΑ
	PLVA665A		_	0.04	_	μΑ
	PLVA668A		_	0.006	_	μΑ
ΔV_Z	line regulation					
	PLVA659A to PLVA668A	$I_{LO} = 10 \mu A; I_{HI} = 1 mA$	_	_	0.1	V
	PLVA656A	$I_{LO} = 50 \mu A; I_{HI} = 1 mA$	_	_	0.1	V
	PLVA650A	$I_{LO} = 100 \mu A; I_{HI} = 1 \text{ mA}$	_	_	0.4	V
	PLVA653A	$I_{LO} = 100 \mu A; I_{HI} = 1 \text{ mA}$	_	_	0.2	V
V _n	noise voltage density	$f = 1 \text{ kHz}; B = 1 \text{ kHz}; I_Z = 250 \mu A$	_	_	1.0	μV
						$\frac{\mu V}{\sqrt{Hz}}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-tp)}	thermal resistance from junction to tie-point		330	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Device mounted on an FR4 printed circuit-board.

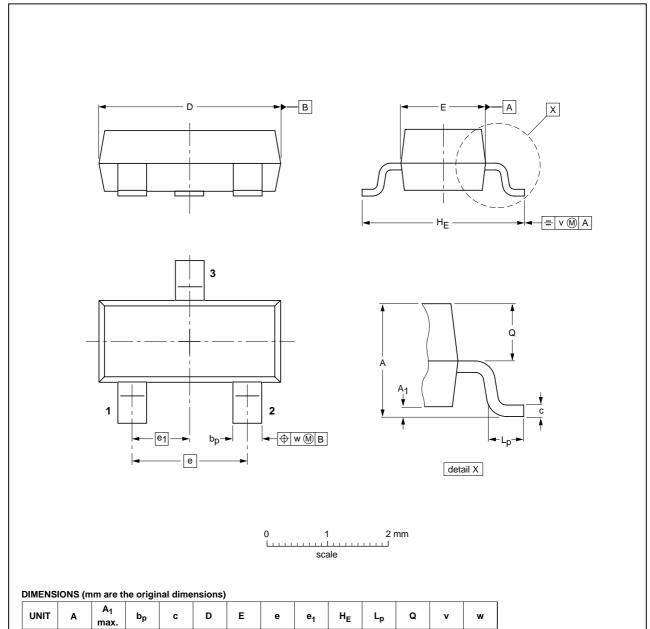
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PLVA6xxA series

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

0.45

0.55

0.1

2004 Jan 14 6

0.38

0.9

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PLVA6xxA series

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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Customer notification

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