

## Single BiCMOS rail-to-rail micropower comparator

### Features

- Rail-to-rail inputs
- Open drain output
- Supply operation from 2.7 to 10 V
- Typical supply current: 6  $\mu$ A at 5 V
- Response time of 0.5  $\mu$ s at 5 V
- Low input current
- ESD protection: 2 kV (HBM), 200 V (MM)
- Available in tiny SOT23-5 package

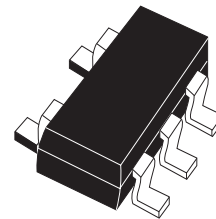
### Applications

- Battery-powered systems
- Notebooks and PDAs
- PCMCIA cards
- Cellular and mobile communications
- Alarms and security systems
- Replacement of amplifiers used in comparator configurations for improved performance.

### Description

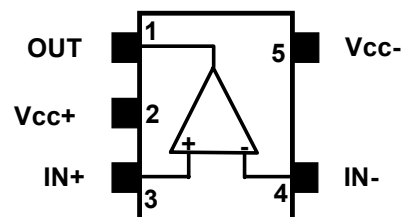
The TS7221 is a micropower comparator featuring a rail-to-rail input performance in a tiny SOT23-5 package. This comparator is ideally suited to space and weight-critical applications. It is fully specified at 2.7-, 5- and 10-V operation over industrial temperature ranges (-40°C to +85°C).

The TS7221 features an open-drain output stage. The speed-to-power ratio makes this device ultra-versatile for a wide range of applications.



**L**  
**SOT23-5L**

**TS7221 open drain output**



# 1 Absolute maximum ratings

**Table 1. Absolute maximum ratings**

Symbol	Parameter	Value	Unit
$V_{CC}$	Supply voltage	12	V
$V_{ID}$	Differential input voltage	$(V_{CC}^-) - 0.3$ to $(V_{CC}^+) + 0.3$	V
$V_{IN}$	Input voltage <sup>(1)</sup>	$(V_{CC}^-) - 0.3$ to $(V_{CC}^+) + 0.3$	V
$V_{OUT}$	Output voltage	12	V
$I_{IN}$	Current at input pins <sup>(1)</sup>	$\pm 5$	mA
$I_{OUT}$	Current at output pin	$\pm 30$	mA
$R_{thja}$	Thermal resistance junction to ambient <sup>(2)</sup> SOT23-5	250	°C/W
$R_{thjc}$	Thermal resistance junction to case <sup>(2)</sup> SOT23-5	81	°C/W
$T_{Lead}$	Lead temperature (soldering 10 seconds)	260	°C
$T_{stg}$	Storage temperature	-65 to +150	°C
$T_J$	Junction temperature	150	°C
ESD	Human body model (HBM) <sup>(3)</sup>	2000	V
	Machine model (MM) <sup>(4)</sup>	200	

1. The magnitude of input voltages must never exceed 0.3 V beyond the supply voltage.
2. Short-circuits can cause excessive heating. These values are typical.
3. Human body model: a 100 pF capacitor is charged to the specified voltage, then discharged through a 1.5 k $\Omega$  resistor between two pins of the device. This is done for all couples of connected pin combinations while the other pins are floating.
4. Machine model: a 200 pF capacitor is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5  $\Omega$ ). This is done for all couples of connected pin combinations while the other pins are floating.

**Table 2. Operating conditions**

Symbol	Parameter	Value	Unit
$V_{CC}$	Supply voltage	2.7 to 10	V
$T_{amb}$	Ambient temperature	-40 to +85	°C
$V_{icm}$	Common mode input voltage range	$(V_{CC}^-) - 0.3$ to $(V_{CC}^+) + 0.3$	V

## 2 Electrical characteristics

**Table 3. Electrical characteristics at  $V_{CC}^+ = 2.7\text{ V}$ ,  $T_{amb} = 25^\circ\text{C}$  (unless otherwise specified)<sup>(1)</sup>**

Symbol	Parameter	Min.	Typ.	Max.	Unit
$V_{IO}$	Input offset voltage (full common mode range) – TS7221A at $T_{min} \leq T_{amb} \leq T_{max}$ – TS7221B at $T_{min} \leq T_{amb} \leq T_{max}$			7 10 15 18	mV
$\Delta V_{IO}$	Input offset voltage drift with temperature		6		$\mu\text{V}/^\circ\text{C}$
$I_{IB}$	Input bias current <sup>(2)</sup> at $T_{min} \leq T_{amb} \leq T_{max}$		1	300 600	pA
$I_{IO}$	Input offset current <sup>(2)</sup> at $T_{min} \leq T_{amb} \leq T_{max}$		1	150 300	pA
CMRR	Common-mode rejection ratio ( $0 < V_{icm} < 2.7\text{ V}$ )		65		dB
PSRR	Power supply rejection ratio ( $2.7 < V_{CC} < 10\text{ V}$ )		80		dB
$A_{VD}$	Voltage gain <sup>(3)</sup>		240		dB
$V_{icm}$	Input common mode voltage range at $T_{min} \leq T_{amb} \leq T_{max}$	-0.3 0.0		3 2.7	V
$I_{OH}$	High level output voltage ( $I_N^+ = 0.5\text{ V}$ , $I_N^- = 0\text{ V}$ and $O_U = 10\text{ V}$ )		0.1	500	nA
$V_{OL}$	Low level output voltage, $I_{sink} = 5\text{ mA}$ at $T_{min} \leq T_{amb} \leq T_{max}$		0.2	0.35 0.45	V
$I_{CC}$	Supply current Output low Output high		6 8	12 14	$\mu\text{A}$
$T_{PLH}$	Response time low to high ( $V_{ic} = 1.35\text{ V}$ , $C_L = 50\text{ pF}$ , $R_L = 10\text{ k}\Omega$ ) Overdrive = 10 mV Overdrive = 100 mV		1.5 0.6		$\mu\text{s}$
$T_{PHL}$	Response time high to low ( $V_{ic} = 1.35\text{ V}$ , $C_L = 50\text{ pF}$ , $R_L = 10\text{ k}\Omega$ ) Overdrive = 10 mV Overdrive = 100 mV		1.5 0.5		$\mu\text{s}$
$T_F$	Fall time $C_L = 50\text{ pF}$ , $R_L = 5\text{ k}\Omega$ , overdrive = 10 mV		0.3		$\mu\text{s}$
$T_R$	Rise time $C_L = 50\text{ pF}$ , $R_L = 5\text{ k}\Omega$ , overdrive = 10 mV		0.3		$\mu\text{s}$

1. Limits are 100% production-tested at  $+25^\circ\text{C}$ . Behavior at temperature range limits is guaranteed through correlation and by design.
2. Maximum values include unavoidable inaccuracies of industrial testing.
3. Design evaluation.

**Table 4. Electrical characteristics for  $V_{CC}^+ = 5\text{ V}$ ,  $T_{amb} = 25^\circ\text{C}$  (unless otherwise specified)<sup>(1)</sup>**

Symbol	Parameter	Min.	Typ.	Max.	Unit
$V_{IO}$	Input offset voltage (full common mode range) – TS7221A at $T_{min} \leq T_{amb} \leq T_{max}$ – TS7221B $T_{min} \leq T_{amb} \leq T_{max}$			7 10 15 18	mV
$\Delta V_{IO}$	Input offset voltage drift with temperature		6		$\mu\text{V}/^\circ\text{C}$
$I_{IB}$	Input bias current <sup>(2)</sup> at $T_{min} \leq T_{amb} \leq T_{max}$		1	300 600	pA
$I_{IO}$	Input offset current <sup>(2)</sup> at $T_{min} \leq T_{amb} \leq T_{max}$		1	150 300	pA
CMRR	Common-mode rejection ratio ( $0 < V_{icm} < 5\text{ V}$ )		70		dB
PSRR	Power supply rejection ratio ( $2.7 < V_{CC} < 10\text{ V}$ )		80		dB
$A_{VD}$	Voltage gain <sup>(3)</sup>		240		dB
$V_{icm}$	Input common mode voltage range at $T_{min} \leq T_{amb} \leq T_{max}$	-0.3 0.0		5.3 5.0	V
$I_{OH}$	High level output voltage ( $I_N^+ = 0.5\text{ V}$ , $I_N^- = 0\text{ V}$ and $OUT = 10\text{ V}$ )		0.1	500	nA
$V_{OL}$	Low level output voltage, $I_{sink} = 5\text{ mA}$ at $T_{min} \leq T_{amb} \leq T_{max}$		0.2	0.40 0.55	V
$I_{CC}$	Supply current Output low Output high		6 8	12 14	$\mu\text{A}$
$T_{PLH}$	Response time low to high ( $V_{ic} = 2.5\text{ V}$ , $C_L = 50\text{ pF}$ , $R_L = 10\text{ k}\Omega$ ) Overdrive = 10 mV Overdrive = 100 mV		2 0.5		$\mu\text{s}$
$T_{PHL}$	Response time high to low ( $V_{ic} = 2.5\text{ V}$ , $C_L = 50\text{ pF}$ , $R_L = 10\text{ k}\Omega$ ) Overdrive = 10 mV Overdrive = 100 mV		2 0.4		$\mu\text{s}$
$T_F$	Fall time $C_L = 50\text{ pF}$ , $R_L = 5\text{ k}\Omega$ , overdrive = 10 mV		0.3		$\mu\text{s}$
$T_R$	Rise time $C_L = 50\text{ pF}$ , $R_L = 5\text{ k}\Omega$ , overdrive = 10 mV		0.3		$\mu\text{s}$

1. Limits are 100% production-tested at  $+25^\circ\text{C}$ . Behavior at temperature range limits is guaranteed through correlation and by design.
2. Maximum values include unavoidable inaccuracies of industrial testing.
3. Design evaluation.

**Table 5. Electrical characteristics for  $V_{CC} = 10\text{ V}$ ,  $T_{amb} = 25^\circ\text{C}$  (unless otherwise specified)<sup>(1)</sup>**

Symbol	Parameter	Min.	Typ.	Max.	Unit
$V_{IO}$	Input offset voltage (full common mode range) – TS7221A at $T_{min} \leq T_{amb} \leq T_{max}$ – TS7221B $T_{min} \leq T_{amb} \leq T_{max}$			7 10 15 18	mV
$\Delta V_{IO}$	Input offset voltage drift with temperature		6		$\mu\text{V}/^\circ\text{C}$
$I_{IB}$	Input bias current <sup>(2)</sup> at $T_{min} \leq T_{amb} \leq T_{max}$		1	300 600	pA
$I_{IO}$	Input offset current <sup>(2)</sup> at $T_{min} \leq T_{amb} \leq T_{max}$		1	150 300	pA
CMRR	Common-mode rejection ratio ( $0 < V_{icm} < 10\text{ V}$ )		75		dB
PSRR	Power supply rejection ratio ( $2.7 < V_{CC} < 10\text{ V}$ )		80		dB
$A_{VD}$	Voltage gain <sup>(3)</sup>		240		dB
$V_{ICM}$	Input common mode voltage range at $T_{min} \leq T_{amb} \leq T_{max}$	-0.3 0.0		10.3 10.0	V
$I_{OH}$	High level output voltage ( $I_N^+ = 0.5\text{ V}$ , $I_N^- = 0\text{ V}$ and $OUT = 10\text{ V}$ )		0.1	500	nA
$V_{OL}$	Low level output voltage, $I_{sink} = 5\text{ mA}$ at $T_{min} \leq T_{amb} \leq T_{max}$		0.2	0.40 0.55	V
$I_{CC}$	Supply current Output low Output high		7 10	14 16	$\mu\text{A}$
$T_{PLH}$	Response time low to high ( $V_{ic} = 5\text{ V}$ , $C_L = 50\text{ pF}$ , $R_L = 10\text{ k}\Omega$ ) Overdrive = 10 mV Overdrive = 100 mV		3 0.5		$\mu\text{s}$
$T_{PHL}$	Response time high to low ( $V_{ic} = 5\text{ V}$ , $C_L = 50\text{ pF}$ , $R_L = 10\text{ k}\Omega$ ) Overdrive = 10 mV Overdrive = 100 mV		4 0.4		$\mu\text{s}$
$T_F$	Fall time $C_L = 50\text{ pF}$ , $R_L = 5\text{ k}\Omega$ , overdrive = 10 mV		0.3		$\mu\text{s}$
$T_R$	Rise time $C_L = 50\text{ pF}$ , $R_L = 5\text{ k}\Omega$ , overdrive = 10 mV		0.3		$\mu\text{s}$

1. Limits are 100% production-tested at  $+25^\circ\text{C}$ . Behavior at temperature range limits is guaranteed through correlation and by design.

2. Maximum values include unavoidable inaccuracies of industrial testing.

3. Design evaluation.

Figure 1. Supply current vs. supply voltage (output low)

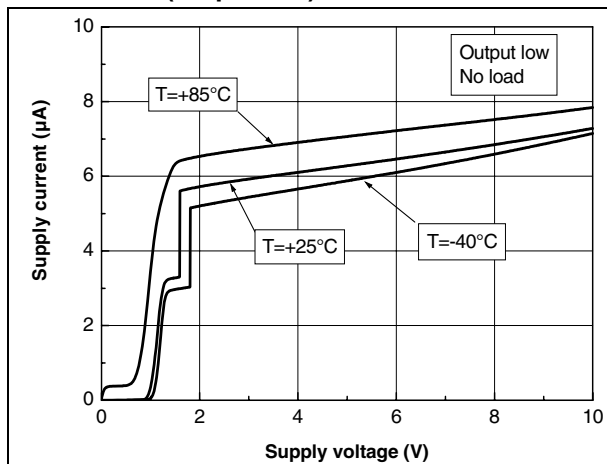


Figure 2. Supply current vs. supply voltage (output high)

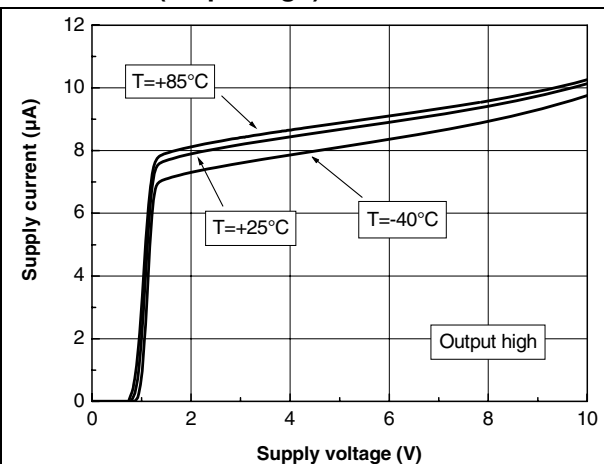
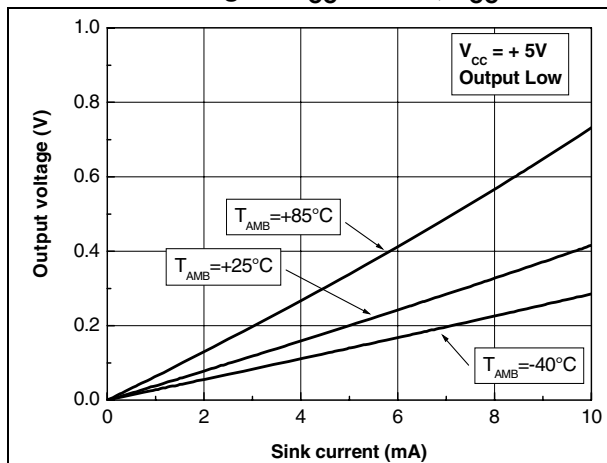
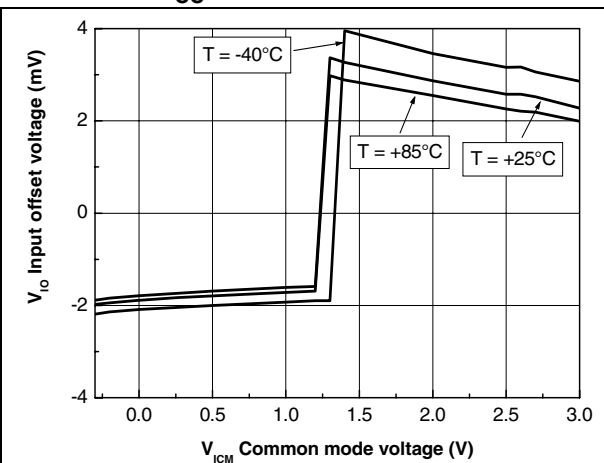
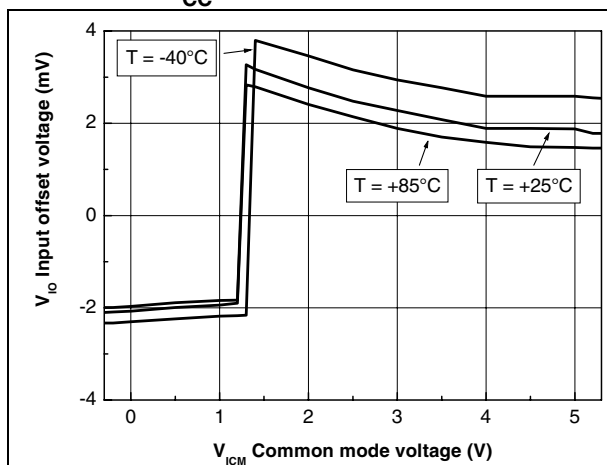
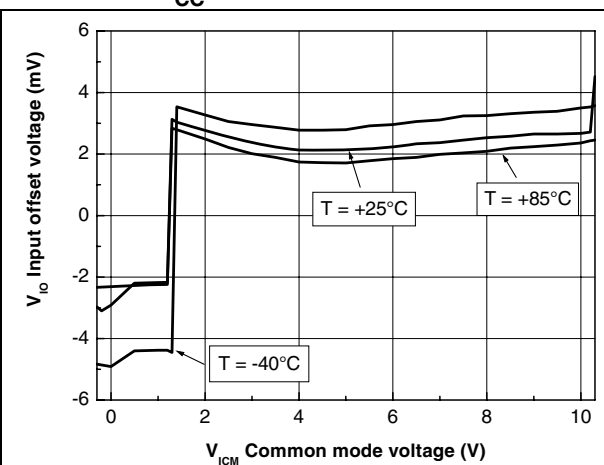
Figure 3. Output sinking current vs. output voltage at  $V_{CC} = +2.7\text{ V}$ ,  $V_{CC} = +5\text{ V}$ Figure 4.  $V_{IO}$  vs.  $V_{ICM}$  and temperature at  $V_{CC} = 2.7\text{ V}$ Figure 5.  $V_{IO}$  vs.  $V_{ICM}$  and temperature at  $V_{CC} = 5\text{ V}$ Figure 6.  $V_{IO}$  vs.  $V_{ICM}$  and temperature at  $V_{CC} = 10\text{ V}$ 

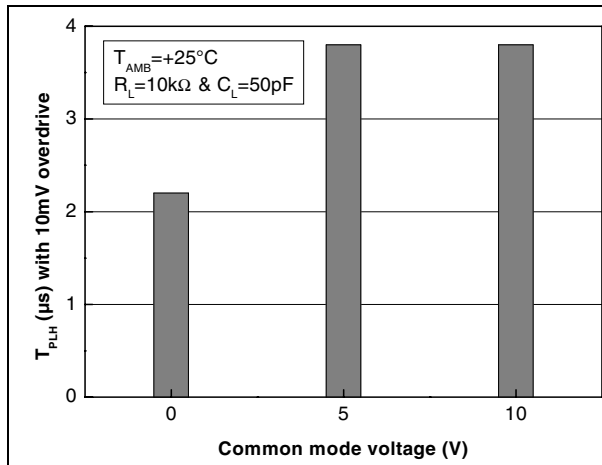
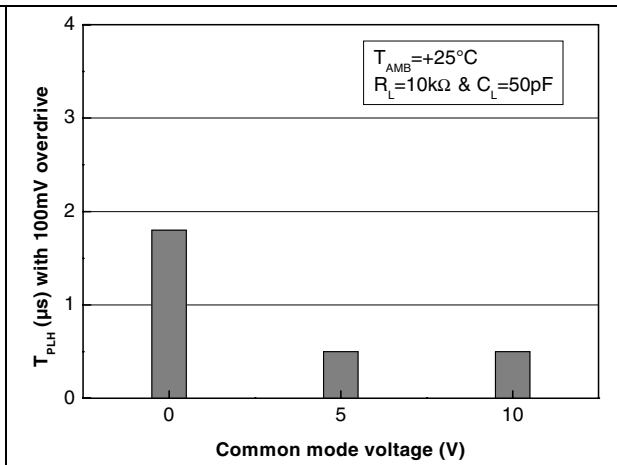
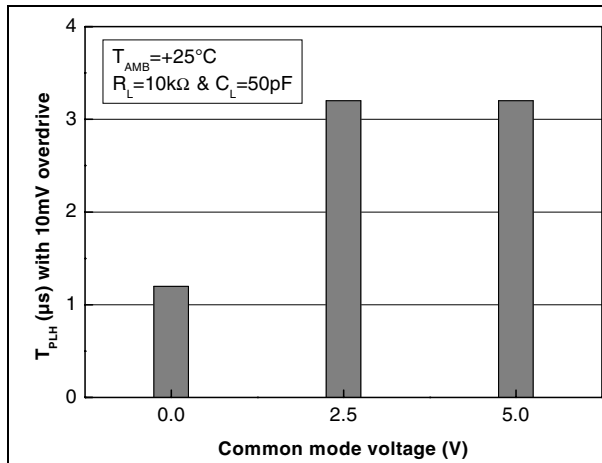
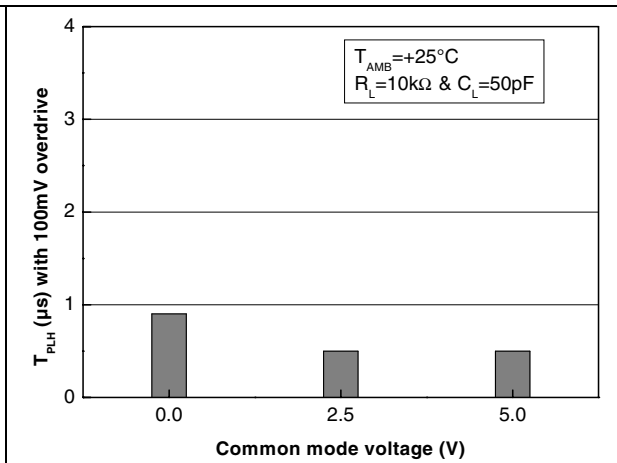
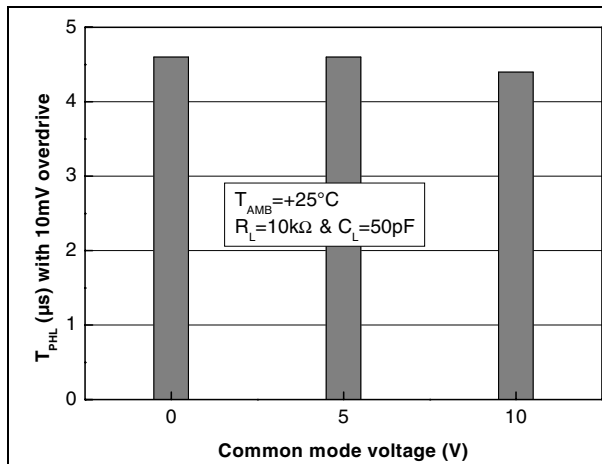
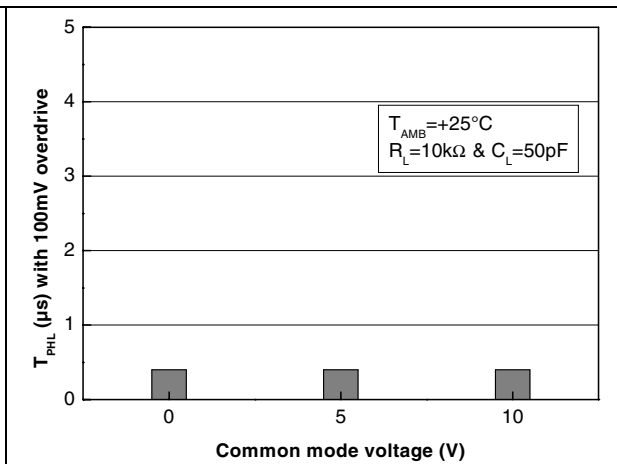
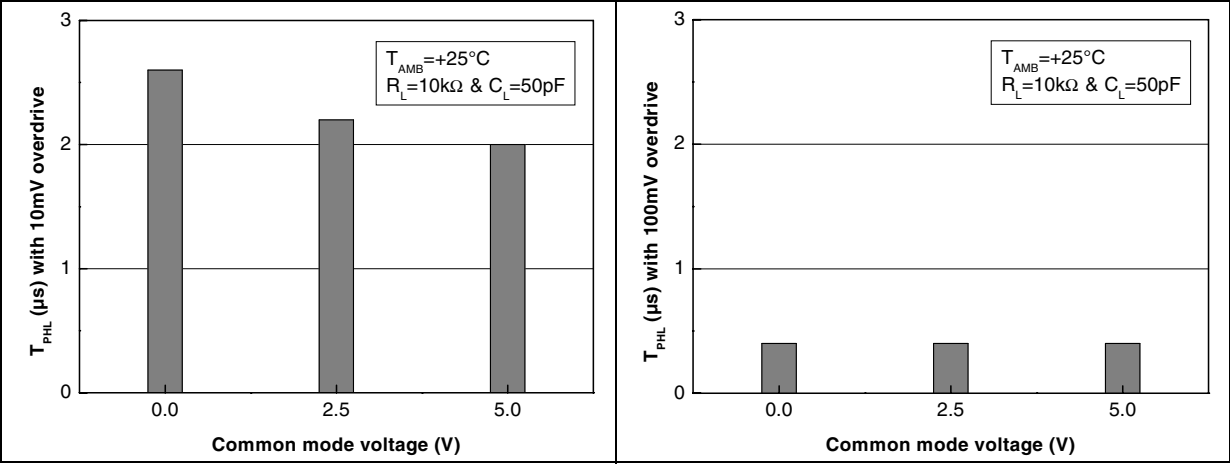
Figure 7.  $T_{PLH}$  vs  $V_{icm}$  at  $V_{CC} = 10\text{ V}$  and 10 mV overdriveFigure 8.  $T_{PLH}$  vs  $V_{icm}$  at  $V_{CC} = 10\text{ V}$  and 100 mV overdriveFigure 9.  $T_{PLH}$  vs  $V_{icm}$  at  $V_{CC} = 5\text{ V}$  and 10 mV overdriveFigure 10.  $T_{PLH}$  vs  $V_{icm}$  at  $V_{CC} = 5\text{ V}$  and 100 mV overdriveFigure 11.  $T_{PHL}$  vs  $V_{icm}$  at  $V_{CC} = 10\text{ V}$  and 10 mV overdriveFigure 12.  $T_{PHL}$  vs  $V_{icm}$  at  $V_{CC} = 10\text{ V}$  and 100 mV overdrive

Figure 13.  $T_{PHL}$  vs  $V_{icm}$  at  $V_{CC} = 5\text{ V}$  and 10 mV overdrive      Figure 14.  $T_{PHL}$  vs  $V_{icm}$  at  $V_{CC} = 5\text{ V}$  and 100 mV overdrive





### 3      **Package information**

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK<sup>®</sup> is an ST trademark.

### 3.1 SOT23-5 package information

Figure 15. SOT23-5L package mechanical drawing

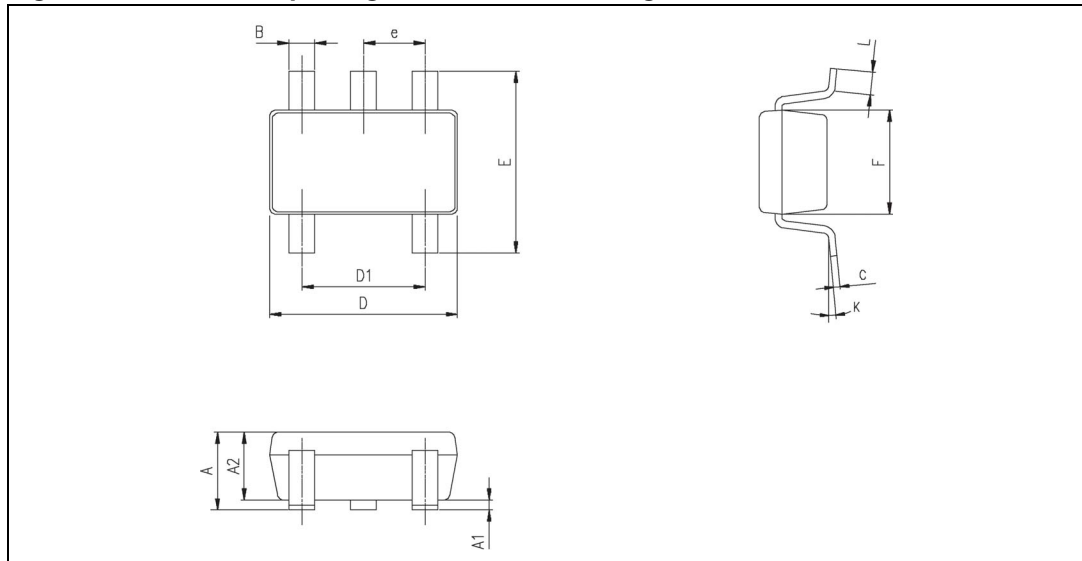


Table 6. SOT23-5L package mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.90	1.20	1.45	0.035	0.047	0.057
A1			0.15			0.006
A2	0.90	1.05	1.30	0.035	0.041	0.051
B	0.35	0.40	0.50	0.013	0.015	0.019
C	0.09	0.15	0.20	0.003	0.006	0.008
D	2.80	2.90	3.00	0.110	0.114	0.118
D1		1.90			0.075	
e		0.95			0.037	
E	2.60	2.80	3.00	0.102	0.110	0.118
F	1.50	1.60	1.75	0.059	0.063	0.069
L	0.10	0.35	0.60	0.004	0.013	0.023
K	0 degrees		10 degrees			

## 4 Ordering information

**Table 7. Order codes**

Order code	Temperature range	Package	Packing	Marking
TS7221AILT	-40°C, +85°C	SOT23-5L	Tape & reel	K518
TS7221BILT				K519

## 5 Revision history

**Table 8. Document revision history**

Date	Revision	Changes
01-Dec-2002	1	Initial release
01-Sep-2005	2	Update of datasheet presentation and format. Change of $T_{\text{lead}}$ temperature in <a href="#">Table 1 on page 2</a> , to reflect change to Pb-free package. Corrections to $V_{\text{icm}}$ upper rail parameters in <a href="#">Electrical characteristics</a> tables. Addition of Pb-free information in <a href="#">Section 3: Package information on page 9</a> . Correction to package mechanical data given in <a href="#">Figure 15 on page 10</a> .
26-Mar-2007	3	Added automotive grade part numbers in <a href="#">Section 4: Ordering information on page 11</a> .
05-Jul-2007	4	Corrected automotive grade part numbers in <a href="#">Table 7: Order codes</a> .
27-Mar-2009	5	Added notes for ESD in <a href="#">Table 1: Absolute maximum ratings</a> . Added $R_{\text{thja}}$ and $R_{\text{thjc}}$ parameters in <a href="#">Table 1: Absolute maximum ratings</a> . Removed power dissipation parameter ( $P_D$ ) in <a href="#">Table 1: Absolute maximum ratings</a> . Updated package information in <a href="#">Section 3.1</a> . Removed automotive grade part numbers in <a href="#">Table 7: Order codes</a> .

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