

Filtered video buffers for STB and DVD devices

Features

- Y, C, CVBS inputs with 7 MHz filters
- Y, Cr, Cb inputs with 7 MHz filters
- 6 dB gains
- Capabilities of integrated output buffers: single load (150 Ω) for Y/Cr/Cb signals
double load (75 Ω) for Y, C and CVBS signals
- DC coupled outputs for CVBS and YCrCb signals, DC or AC coupled output for chroma signal
- Bottom clamp on Y and CVBS, bias clamp on C, sync clamps on Cr and Cb
- Crosstalk: 55 dB (typ.)
- Separate stand-by modes on Y/C/CVBS and on Y/Cr/Cb signals
- Switchable Y+C adder for decoders without CVBS outputs

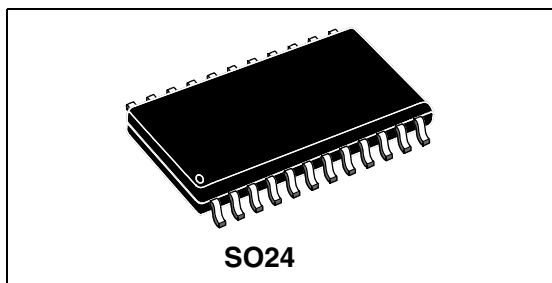


Table 1. Device summary

Order code	Packaging
STV6436S	Tray

Description

The STV6436 is a filtered video output interface for STB and DVD applications.

After removing D/A conversion noises using integrated low pass filters, the STV6436 adapts in amplitude and impedance the video signals coming from the digital decoder for transmission, via 75 Ω adapted cables, to the TV set, VCR and auxiliary devices.

The STV6436 is powered by a 5 V supply.

The STV6436 is fully compatible with STi55xx digital decoders.

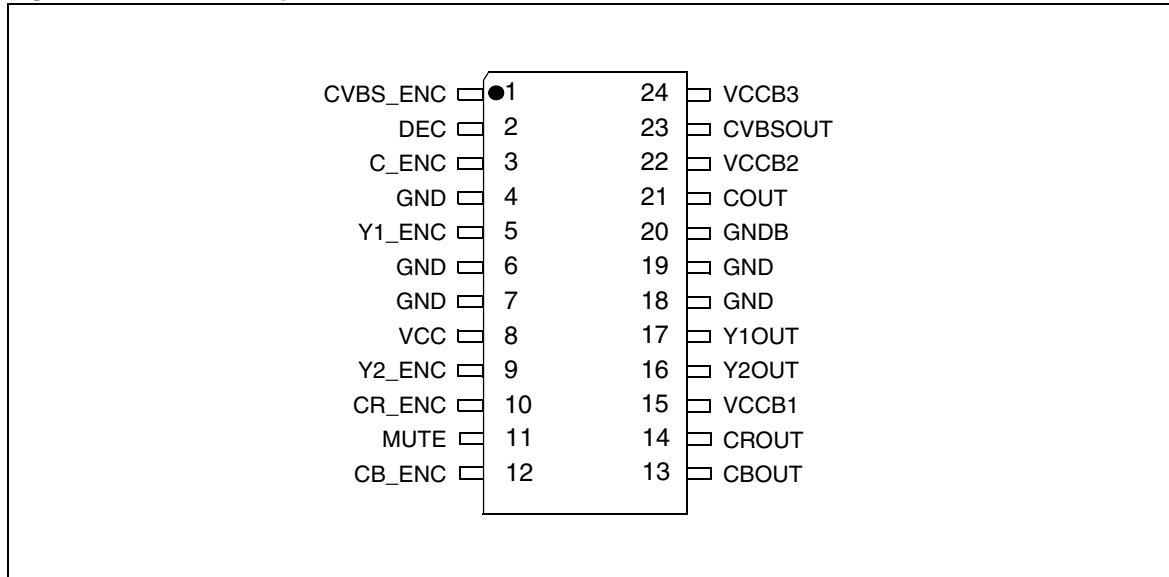
The STV6436 is mounted in a SO24 package (STV6436S).

Contents

1	General information	3
1.1	I/O pin description	3
2	Electrical characteristics	5
2.1	Absolute maximum ratings	5
2.2	Thermal data	5
2.3	Recommended operating conditions	5
2.4	Video section (Y1, Y2 and CVBS signals)	6
2.5	Chroma section	7
2.6	Cb/Cr section	8
2.7	Mute section	9
3	Input/output groups	10
4	Application diagram	14
5	Package mechanical data	15
5.1	Environmentally-friendly packages	16
6	Revision history	17

1 General information

Figure 1. STV6436S pinout



1.1 I/O pin description

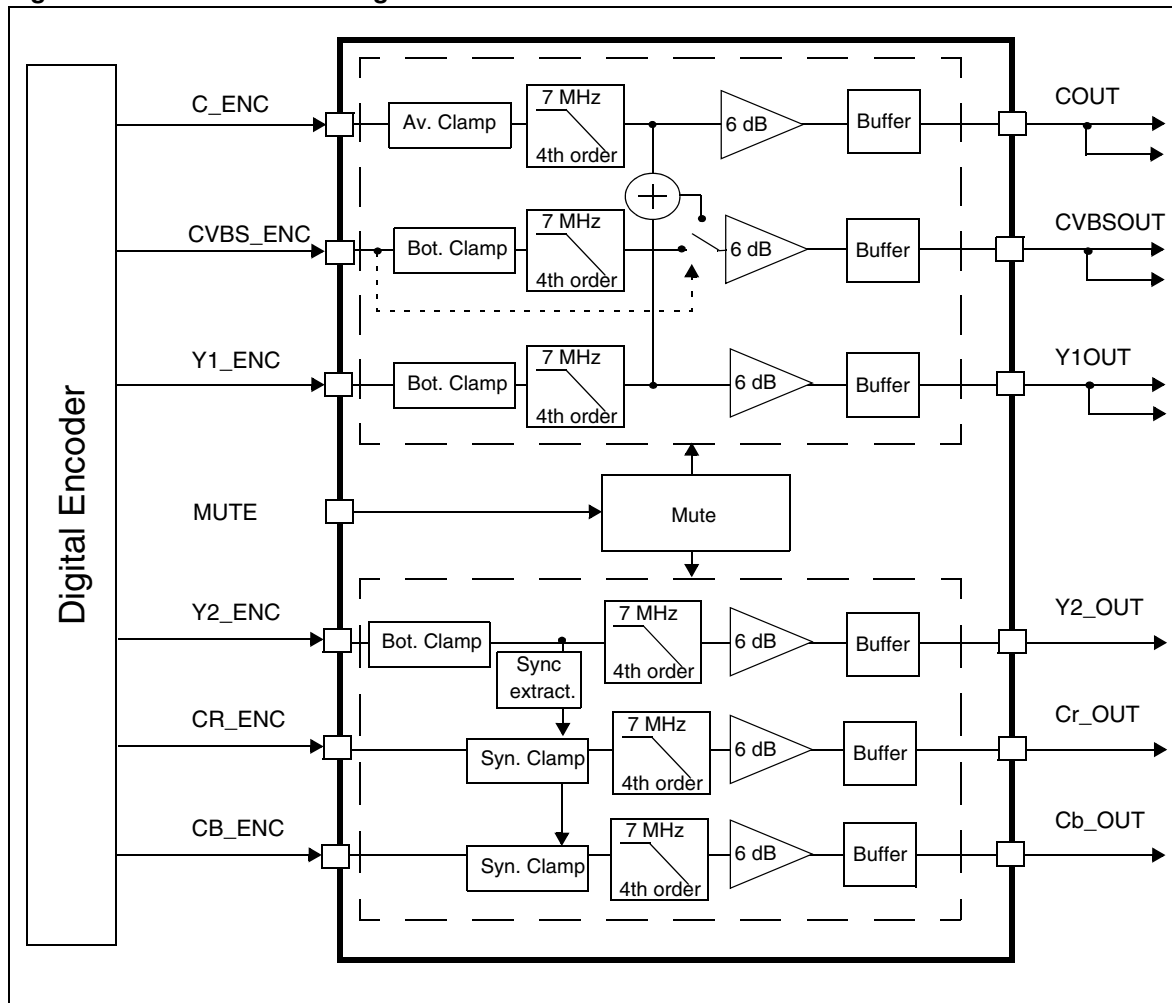
Table 2. Pin description

STV6436S	Name	Function
1	CVBS_ENC	CVBS input from encoder or internal CVBS switch command
2	DEC	Decoupling capacitor
3	C_ENC	Chroma input from encoder
4	GND	Ground
5	Y1_ENC	Y input from encoder
6	GND	Ground
7	GND	Ground
8	VCC	+5 V supply
9	Y2_ENC	Large-band Y input from encoder
10	CR_ENC	Large-band Cr input from encoder
11	MUTE	4-state command for mute
12	CB_ENC	Large-band Cb input from encoder
13	CBOUT	Cb output
14	CROUT	Cr output
15	VCCB1	+5 V supply for output buffers

Table 2. Pin description (continued)

STV6436S	Name	Function
16	Y2OUT	Y2 output
17	Y1OUT	Y1 output
18	GND	Ground
19	GND	Ground
20	GNDB	Ground for buffers
21	COUT	Chroma output
22	VCCB2	+5 V supply for output buffers
23	CVBSOUT	CVBS output
24	VCCB3	+5 V supply for output buffers

Figure 2. STV6436 block diagram



2 Electrical characteristics

2.1 Absolute maximum ratings

Table 3. Absolute maximum rating

Symbol	Parameter		Value	Unit
V_{CC} , V_{CCB}	Supply voltage		6	V
V	Voltage at all pins to ground		-0.6 to V_{CC}	V
V_{ESD}	ESD susceptibility	Human body model: 100 pF discharged through 1.5 k Ω serial resistor	± 4	kV

2.2 Thermal data

Table 4. Thermal data

Symbol	Parameter	Value	Unit
R_{thJA}	Junction-to-ambient thermal resistance	70	$^{\circ}\text{C}/\text{W}$
T_J	Maximum recommended junction temperature	130	$^{\circ}\text{C}$
T_{OPER}	Operating ambient temperature	0 to +70	$^{\circ}\text{C}$
T_{STG}	Storage temperature	-55 to +150	$^{\circ}\text{C}$

2.3 Recommended operating conditions

Test conditions: $T_{AMB} = 25^{\circ}\text{C}$, $V_{CC} = 5\text{ V}$; $V_{CCB} = 5\text{ V}$; $R_{GENERATOR} = 75\ \Omega$, $R_{L_{OUT}} = 75\ \Omega$ for Y1OUT, CVBSOUT and COUT $R_{L_{OUT}} = 150\ \Omega$ for Y2OUT, CBOU and CROUT, unless otherwise specified.

Table 5. Recommended operating conditions

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
Supply voltages						
V_{CC}	Operating supply voltage		4.75	5.00	5.25	V
V_{CCB}	Buffer supply voltage		4.75	5.00	5.25	V
Active (channels ON)						
I_{CC1}	Supply current ($V_{CC} + V_{CCB}$)	No load, MUTE pin to VCC pin (5 V) All channels active		50	65	mA
I_{CC2}	Supply current ($V_{CC} + V_{CCB}$)	No load, MUTE pin = 1.5 V (not connected) Y1/C/CVBS active		30		mA

Table 5. Recommended operating conditions (continued)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_{CC3}	Supply current ($V_{CC} + V_{CCB}$)	No load, MUTE pin = 3 V Y2/Cr/Cb active		30		mA
Standby (all channels OFF)						
I_{CCSTB}	Total supply current	No load, MUTE pin to 0 V		4		mA

2.4 Video section (Y1, Y2 and CVBS signals)

Test conditions: $T_{AMB} = 25^{\circ}\text{C}$, $V_{CC} = 5\text{ V}$; $V_{CCB} = 5\text{ V}$; $R_{GENERATOR} = 75\ \Omega$, $R_{LOUT} = 75\ \Omega$ for Y1 and CVBS outputs and $R_{LOUT} = 150\ \Omega$ for Y2 output, unless otherwise specified.

Table 6. Video section (Y1, Y2 and CVBS signals)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_{DCIN}	DC input level, bottom clamp input	Bottom level, Y1 and CVBS inputs		2		V
I_{CLAMP}	Clamping current, bottom clamp input	at $V_{DCIN} - 400\text{ mV}$	1	2		mA
I_{LEAK}	Input leakage current, bottom clamp input	$V_{IN} = V_{DCIN} + 1\text{ V}$		1	10	μA
$V_{DCIN_YSY_NC}$	DC input level	Y2 input, YCrCb mode, black level		2.3		V
C_{IN}	Input capacitance			2		pF
V_{IN}	Maximum input signal	$V_{CCV} = 5\text{ V}$			1.5	V_{PP}
DYN	Dynamic output signal	$V_{CCV} = 5\text{ V}$			3	V_{PP}
Y1F1	-1 dB bandwidth (flatness) of Y1 and CVBS	1H signal	4.0	4.5		MHz
Y2F1	-1 dB bandwidth (flatness) of Y2	1H signal	4.0	4.5		MHz
Y1F3	-3 dB bandwidth of Y1 and CVBS	1H signal		7		MHz
Y2F3	-3 dB bandwidth of Y2	1H signal		7		MHz
Y1SBR	Stopband rejection	27 MHz versus 100 kHz		- 40		dB
Y2SBR	Stopband rejection	27 MHz versus 100 kHz		- 40		dB
Flatness	Spread of gain in video bands	$V_{IN} = 1\ V_{PP}$ Band = 15 kHz to 5 MHz for Y1, Y2 and CVBS			± 0.5	dB
VCTo	Crosstalk isolation of Y1 (or Y2 or CVBS) from C and Cr Cb channels	$V_{IN} = 0.5\ V_{PP}$ at $f = 3.58\text{ MHz}$, on either C_{IN_ENC} or CR_{IN_ENC} or CB_{IN_ENC} input, $R_{LOAD} = 150\ \Omega$		55		dB
R_{OUT}	Output resistance			5	10	Ω
GY	Gain on Y1, Y2 and CVBS channels	$V_{IN} = 1\ V_{PP}$ at $f = 1\text{ MHz}$	5.5	6	6.5	dB

Table 6. Video section (Y1, Y2 and CVBS signals) (continued)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
DC _{YOUT}	DC output voltage (Y1 and Y2)	Video signal bottom sync pulse at IC output pins		0.5		V
DC _{CVBSOUT}	DC output voltage (CVBS)	Video signal bottom sync pulse at IC output pin		1.0		V
DPHI	Differential phase	V _{IN} = 1 V _{PP} at f = 3.58 MHz		0.2	3	deg.
DG	Differential gain	V _{IN} = 1 V _{PP} at f = 3.58 MHz		0.3	3	%
LNL	Luminance non-linearity			0.5	3	%
VSN7	Video S/N ratio: Y1, C and CVBS channels (7 MHz filter)	NTC-7 weighting 4.2 MHz Lowpass		70		dB
Dtpd7	Group delay variation from flatness	7 MHz filter		20		nS

2.5 Chroma section

Test conditions: T_{AMB} = 25°C, V_{CC} = 5 V; V_{CCB} = 5 V; R_{GENERATOR} = 75 Ω and R_{LOAD} = 75 Ω unless otherwise specified.

Table 7. Chroma section

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V _{DCIN}	DC input level			3		V
R _{IN}	Input resistance		30	50		kΩ
C _{IN}	Input capacitance			2		pF
V _{IN}	Max input signal				1	V _{PP}
DYN	Dynamic output signal				2	V _{PP}
DC _{COOUT}	DC output voltage (COOUT)	Without signal		1.5		V
CF1	-1 dB bandwidth (flatness)		4	4.5		MHz
CF3	-3 dB bandwidth			7		MHz
CSBR	Stopband rejection	27 MHz versus 100 kHz		- 40		dB
Flatness	Spread of gain in video bands	V _{IN} = 1 V _{PP} Band = 15 kHz to 5 MHz for Y1 and CVBS			±0.5	dB
CCTo	Crosstalk isolation of C from Y1, Y2 and CVBS channels	V _{IN} = 1 V _{PP} at f = 3.58 MHz, on Y1 or Y2 or CVBS inputs, R _{LOAD} = 150Ω		55		dB
R _{OUT}	Output resistance			5	10	Ω
GC	Gain on C channel	V _{IN} = 1 V _{PP} at f = 1 MHz	5.5	6	6.5	dB

Table 7. Chroma section (continued)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
CToYdel	Chroma to luma delay, source Y1/C	$V_{IN} = 1 V_{PP}$ at $f = 3.58$ MHz			20	ns
YCadd	Voltage to be applied at CVBS_ENC input for Y+C adder selection			V_{CC}	V_{CC}	V

2.6 Cb/Cr section

Test conditions: $T_{AMB} = 25^{\circ}\text{C}$, $V_{CC} = 5$ V; $V_{CCB} = 5$ V; $R_{GENERATOR} = 75 \Omega$ and $R_{LOAD} = 150 \Omega$, unless otherwise specified.

Table 8. Cb/Cr section

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{DCIN_SYN_C}$	DC input level	Sync clamp input (Cr,Cb) Sync signal on Y input		3.0		V
$I_{CLAMP_SYN_C}$	Clamping current, sync clamp	Sync clamp input (Cr,Cb) at $V_{DCIN} - 400$ mV		100		μA
C_{IN}	Input capacitance			2		pF
V_{IN}	Max input signal				1	V_{PP}
DYN	Dynamic output signal				2	V_{PP}
$DC_{CrCbOUT}$	DC output voltage (Cr and Cb outputs)	Black level sync signal on Y2 input		1.5		V
PF1	-1 dB bandwidth (flatness)		4.0	4.5		MHz
PF3	-3 dB bandwidth			7		MHz
PSBR	Stopband rejection	27 MHz versus 100 kHz		- 40		dB
Flatness	Spread of gain in video bands	$V_{IN} = 1 V_{PP}$ Band = 15 kHz to 5 MHz			± 0.5	dB
PCTo	Crosstalk isolation of Cr or Cb from Y1, Y2 and CVBS channels	$V_{IN} = 1 V_{PP}$ at $f = 3.58$ MHz, on Y1 or Y2 or CVBS input, $R_{LOAD} = 150\Omega$		55		dB
R_{OUT}	Output resistance			5	10	Ω
GP	Gain on Cr and Cb channels	$V_{IN} = 1 V_{PP}$ at $f = 1$ MHz	5.5	6	6.5	dB

2.7 Mute section

Test conditions: $T_{AMB} = 25^{\circ}\text{C}$, $V_{CC} = 5\text{ V}$; $V_{CCB} = 5\text{ V}$; $R_{GENERATOR} = 75\ \Omega$ and $R_{LOUT} = 75\ \Omega$ unless otherwise specified.

Table 9. Mute section

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_{00}	MUTE voltage for Y1/C/CVBS muted and Y2/Cr/Cb muted	Pin MUTE to GND or logical 0	0		1.1	V
V_{01}	MUTE voltage for Y1/C/CVBS active and Y2/Cr/Cb muted	Pin MUTE opened (not connected) ⁽¹⁾	1.3		1.7	V
V_{10}	MUTE voltage for Y1/C/CVBS muted and Y2/Cr/Cb active	Pin MUTE connected by 22 k Ω to VCC or at 3.3 V ($I_{IN} < 140\ \mu\text{A}$)	1.9		4	V
V_{11}	MUTE voltage for Y1/C/CVBS active and Y2/Cr/Cb active	Pin MUTE to VCC (5 V)	4.2		Vcc	V

1. When the MUTE pin is left open, its voltage is defined by an internal voltage divide performed by a 42 k Ω resistor to Vcc and 18 k Ω resistor to GND.

3 Input/output groups

Figure 3. Bottom clamped video input (Y1_ENC)

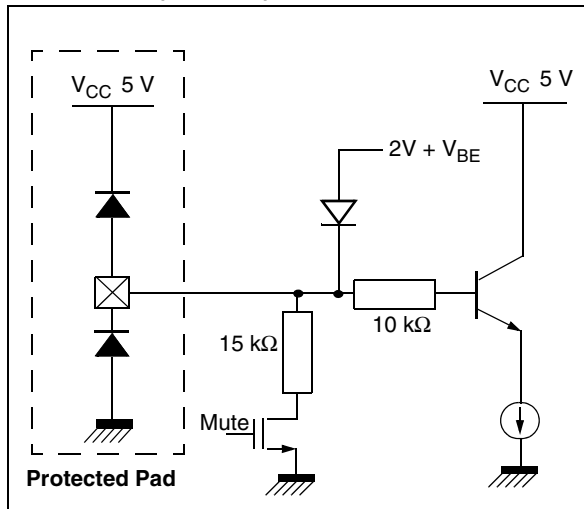


Figure 4. Average clamped video input (C_ENC)

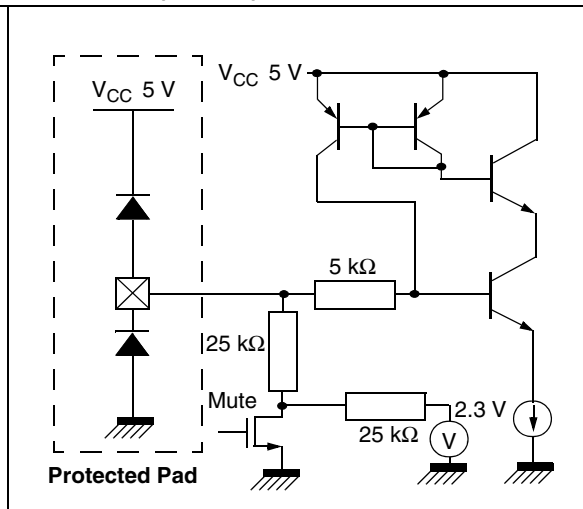


Figure 5. Video outputs (CVBSOUT, Y1OUT, Y2OUT, CROUT and CROUT)

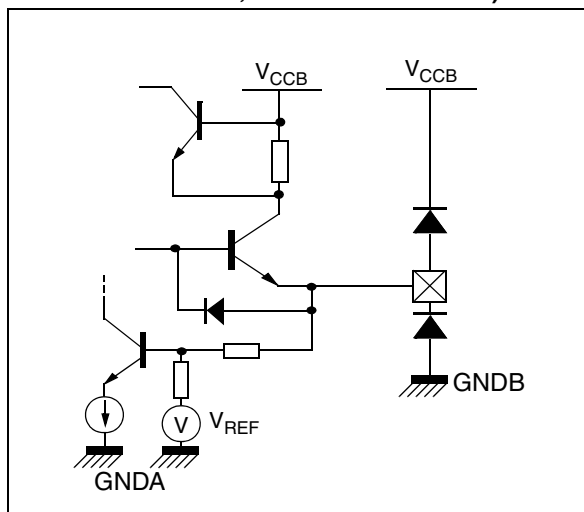


Figure 6. C video output (COUT)

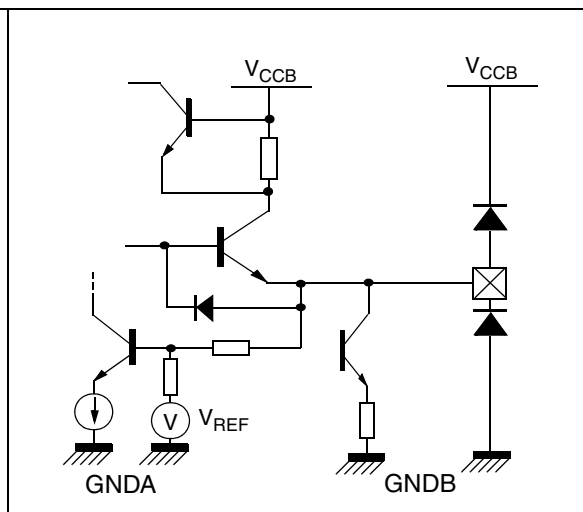


Figure 7. Black level clamped video input (Y2_ENC)

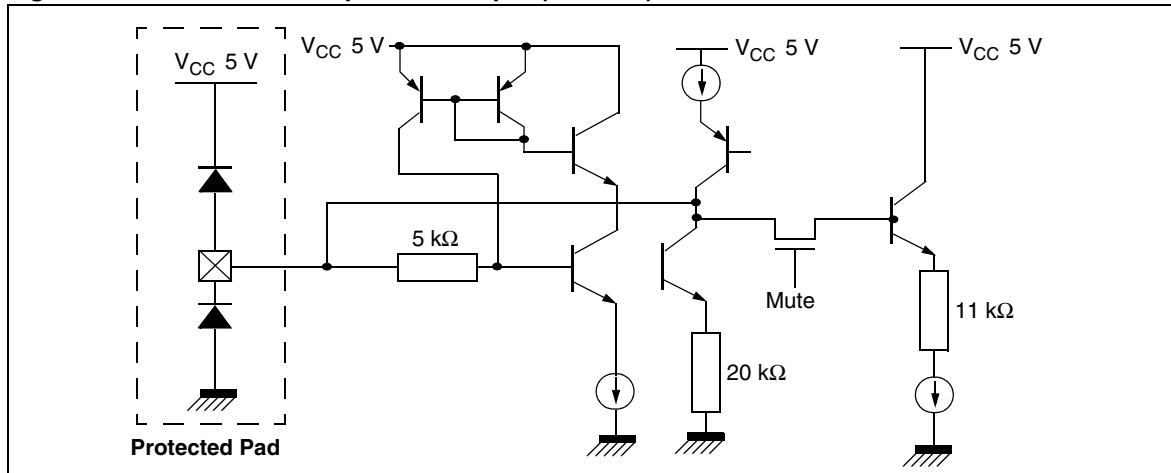


Figure 8. Cb/Cr inputs (CR_ENC and CB_ENC)

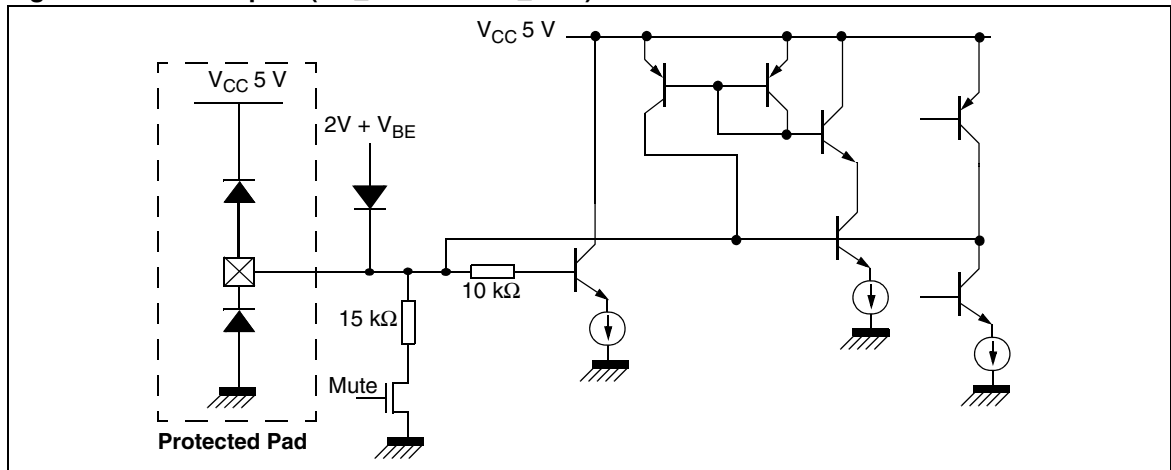


Figure 9. Decoupling capacitor (DEC)

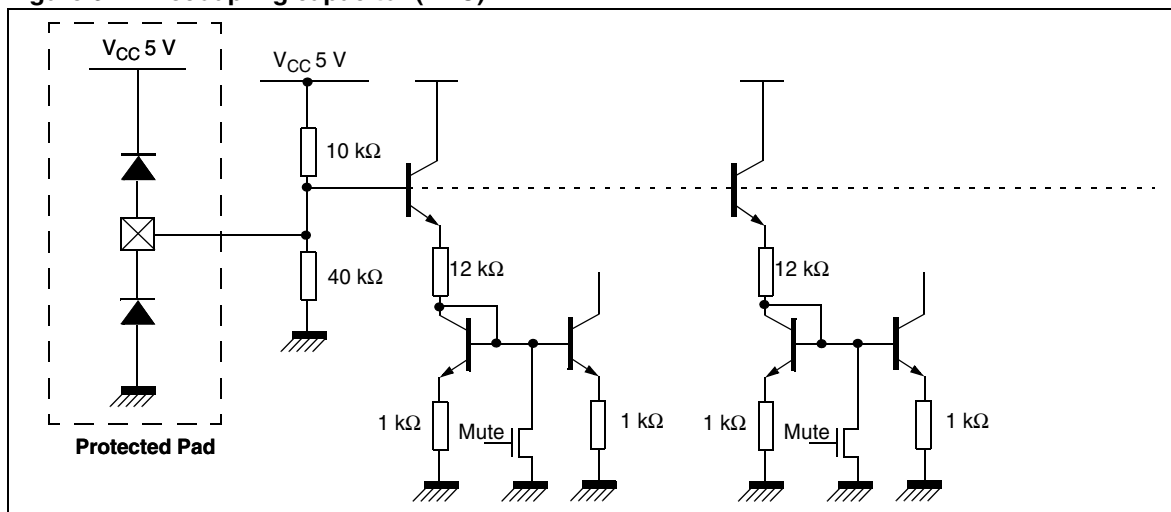


Figure 10. Mute (MUTE)

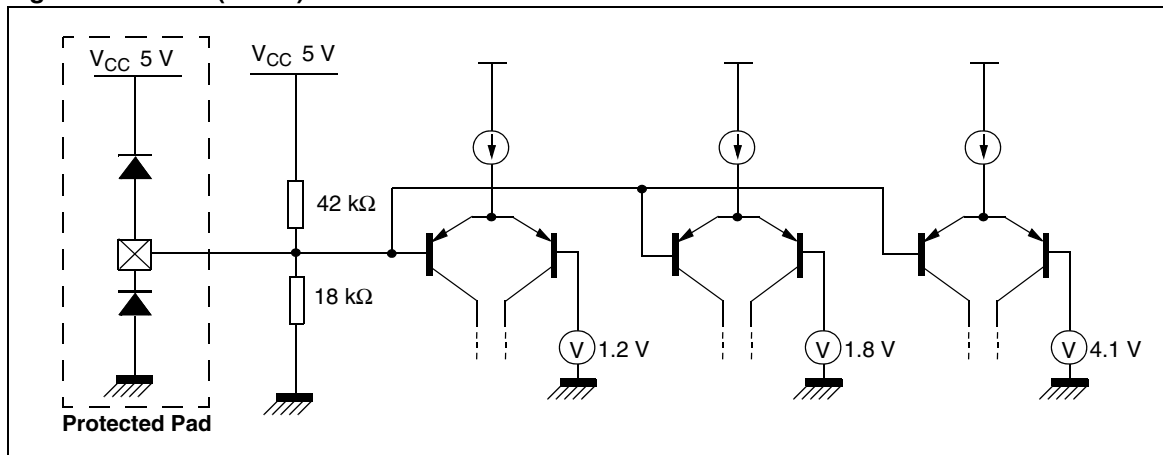


Figure 11. CVBS input (CVBS_ENC)

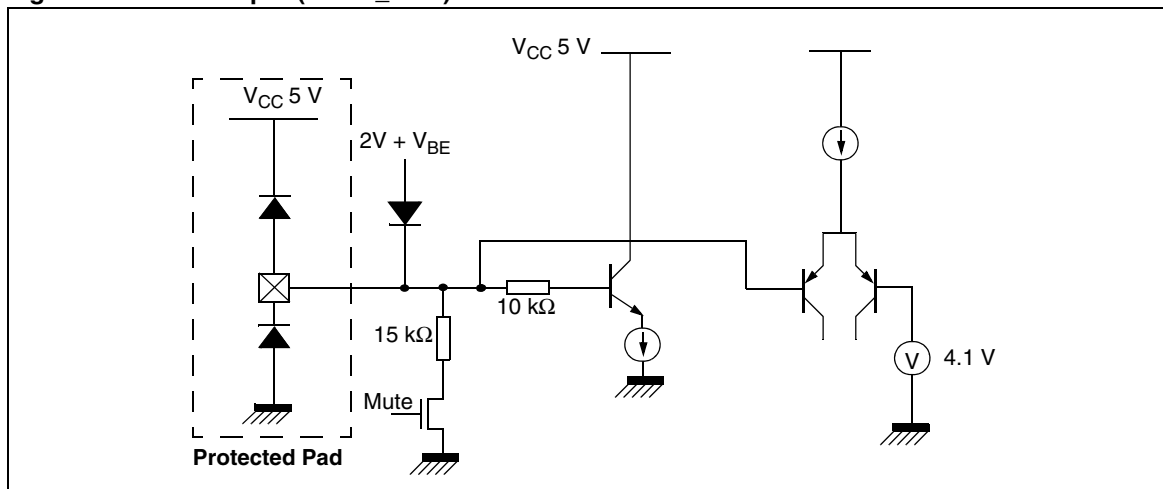


Figure 12. Power supply connection

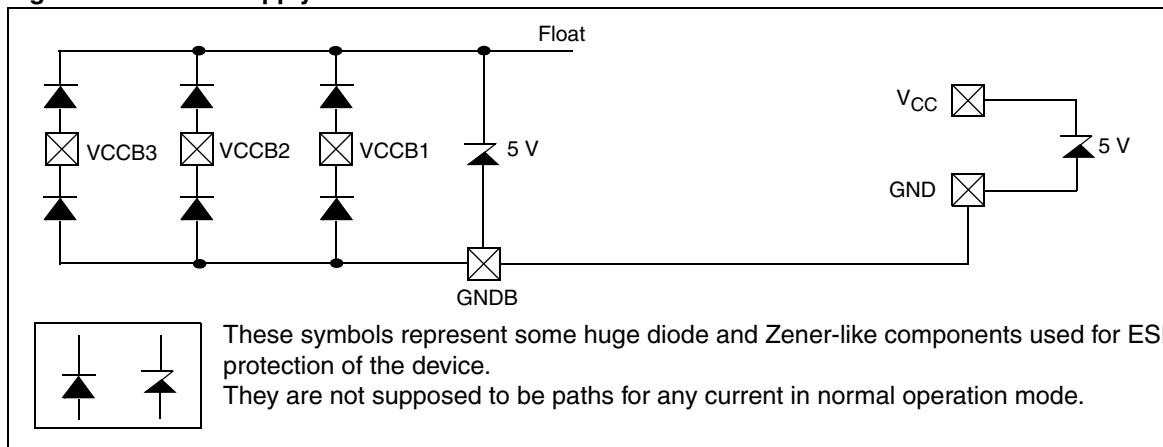


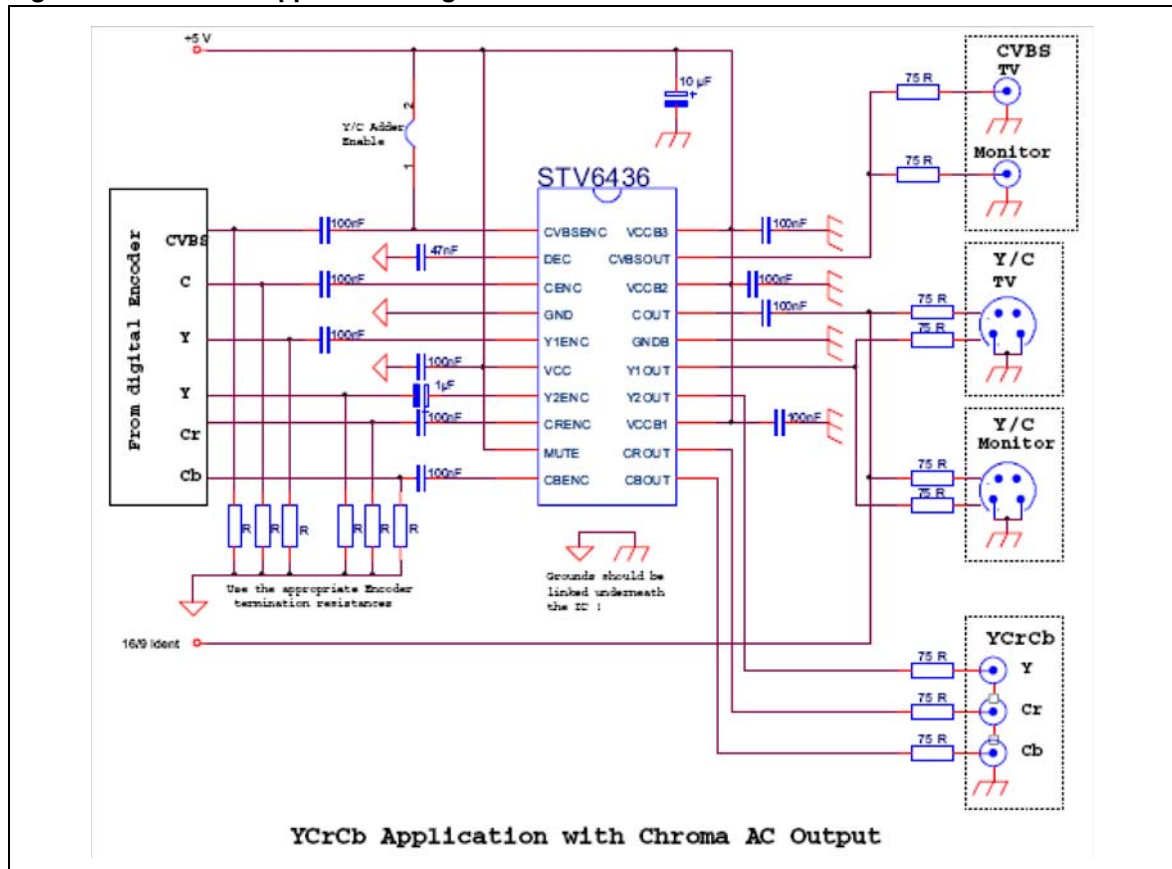
Table 10. Power supply connections

Supply	Description
VCCB1	Y2OUT, CROUT and CBOUT supply
VCCB2	Y1OUT and COUT supply
VCCB3	CVBSOUT Supply
GNDB	Output buffer ground
VCC	Input stages, filters and 6-dB amplifier supply
GND	Input stages, filters and 6-dB amplifier ground

4 Application diagram

Note: The application diagram presented here is an example only and is subject to change without notice. The real application diagram will depend on application conditions and constraints.

Figure 13. STV6436 application diagram



5 Package mechanical data

Figure 14. 24-pin plastic small outline package (SO24)

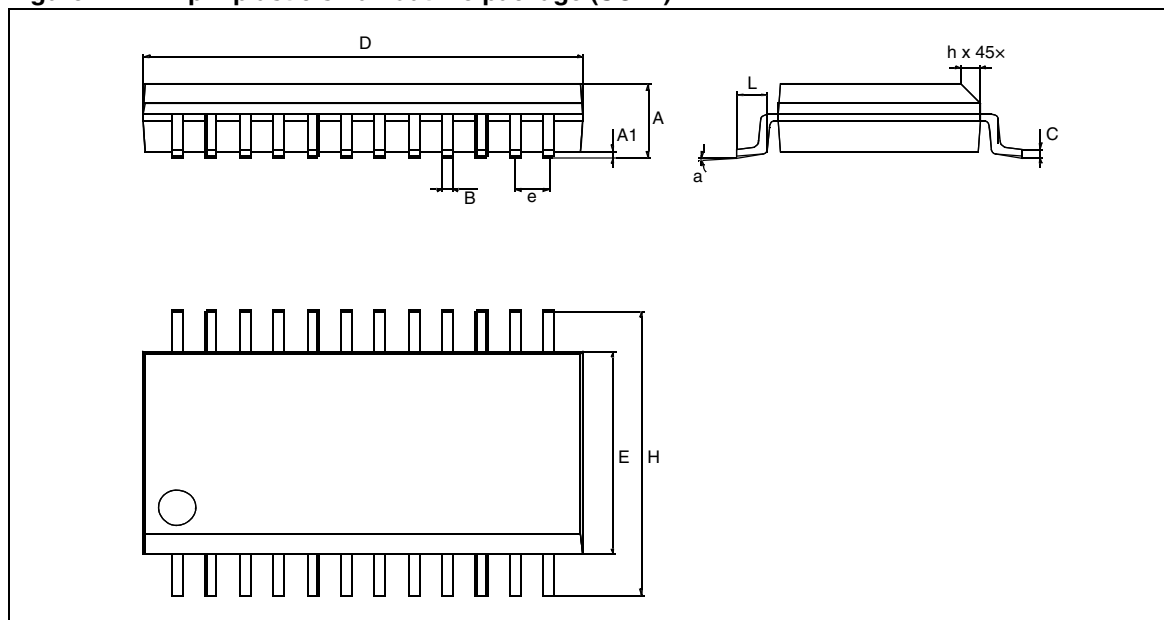


Table 11. 24-pin dimensions

Dim.	mm			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.35		2.65	0.093		0.104
A1	0.10		0.30	0.004		0.012
B	0.33		0.51	0.013		0.020
C	0.23		0.32	0.009		0.013
D	15.20		15.60	0.599		0.614
E	7.40		7.60	0.291		0.299
e		1.27			0.050	
H	10.00		10.65	0.394		0.419
h	0.25		0.75	0.010		0.030
α	0°		8°	0°		8°
L	0.40		1.27	0.016		0.050

5.1 Environmentally-friendly packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance.

ECOPACK specifications, grade definitions and product status are available at: www.st.com.
ECOPACK is an ST trademark.

6 Revision history

Table 12. Document revision history

Date	Revision	Changes
21-Jun-2002	0.1	First Issue
24-May-2005	1	Removed DIP20 package information
30-Apr-2007	1.1	Reformatted to new corporate template. Addition of disclaimer for Figure 13: STV6436 application diagram
02-Apr-2009	2	Preliminary banner removed, Section 5.1: Environmentally-friendly packages added

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2009 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com