TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

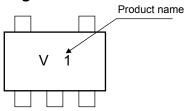
TC7SA00F,TC7SA00FU

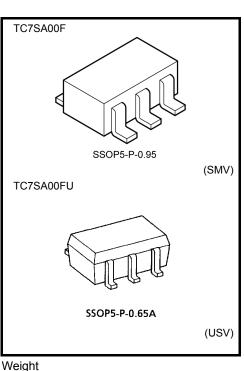
2-Input NAND Gate

Features

- Low voltage operation $: V_{CC} = 1.8 \text{ to } 3.6 \text{ V}$
- High speed operation $: t_{pd} = 2.8 \text{ ns} (max) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
 - : t_{pd} = 3.7 ns (max) (V_{CC} = 2.3 to 2.7 V)
 - : t_{pd} = 7.4 ns (max) (V_{CC} = 1.8 V)
- High output current : $I_{OH}/I_{OL} = \pm 24$ mA (min) (V_{CC} = 3.0 V) : $I_{OH}/I_{OL} = \pm 18$ mA (min) (V_{CC} = 2.3 V) : $I_{OH}/I_{OL} = \pm 6$ mA (min) (V_{CC} = 1.8 V)
- 3.6-V tolerant inputs.
- 3.6-V power down protection output.
- TC74VCX00FT equivalent.

Marking





 weight

 SSOP5-P-0.95
 : 0.016 g (typ.)

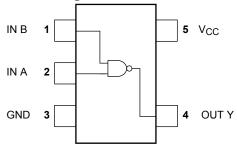
 SSOP5-P-0.65A
 : 0.006 g (typ.)

. 0.000 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 4.6	V
DC output voltage	Vour	-0.5 to 4.6 (Note 1)	V
DC oulput voltage	Vout	-0.5 to V _{CC} +0.5 (Note 2)	v
Input diode current	I _{IK}	-50	mA
Output diode current	I _{OK}	-50 (Note 3)	mA
DC output current	IOUT	±50	mA
Power dissipation	PD	200	mW
DC V _{CC} /ground current	I _{CC}	±100	mA
Storage temperature range	T _{stg}	−65 to 150	°C

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:
$$V_{CC} = 0 V$$

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

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IEC Logic Symbol



А	В	Y
L	L	Н
L	Н	Н
Н	L	Н

Н

L

Truth Table

Н

Operating Ranges

Characteristics	Symbol	Rating	Unit
supply voltage	V _{CC}	1.8 to 3.6	V
Supply Vollage	VCC	1.2 to 3.6 (Note 4)	v
Input voltage	V _{IN}	–0.3 to 3.6	V
Output voltage	V _{OUT}	0 to 3.6 (Note 5)	V
Oulput voltage		0 to V _{CC} (Note 6)	v
		± 24 (Note 7)	
Output current	I _{OH} /I _{OL}	± 18 (Note 8)	mA
		± 6 (Note 9)	
Operating temperature range	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 10)	ns/V

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or low state

Note 7: $V_{CC}=3.0 \mbox{ to } 3.6 \mbox{ V}$

Note 8: $V_{CC} = 2.3$ to 2.7 V

Note 9: $V_{CC} = 1.8 V$

Note 10: V_{IN} = 0.8 to 2.0 V, V_{CC} = 3.0 V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C, 2.7 V < V_{CC} \leq 3.6 V)

Characteristics Symbol		Symbol T		Condition		Min	Мах	Unit
			onation	$V_{CC}(V)$		Max	Offic	
Input voltage	High level	VIH			2.7 to 3.6	2.0	_	v
input voltage	Low level	V _{IL}			2.7 to 3.6	_	0.8	v
			I _{OH} = -100 μA	2.7 to 3.6	V _{CC} - 0.2	_		
	High level	V _{OH}	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OH} = -12 mA	2.7	2.2	_	
Output voltage	0.1		I _{OH} = -18 mA	3.0	2.4	_]	
				I _{OH} =24 mA	3.0	2.2	_	v
		V _{OL}	V _{IN} = V _{IH}	I _{OL} = 100 μA	2.7 to 3.6	-	0.2	-
	L avv lavval			I _{OL} = 12 mA	2.7	_	0.4	
	Low level			I _{OL} = 18 mA	3.0	-	0.4	
				I _{OL} = 24 mA	3.0	_	0.55	
Input leakage curr	ent	l _{IN}	$V_{IN} = 0$ to 3.6 V	·	2.7 to 3.6	_	±5.0	μA
Power off leakage	current	IOFF	V_{IN} , $V_{OUT} = 0$ to	3.6 V	0	_	10.0	μA
Quiescent supply current			V _{IN} = V _{CC} or GNI	V _{IN} = V _{CC} or GND		_	20.0	
		Icc	$V_{CC} \leq (V_{IN}, V_{OUT}) \leq 3.6 \text{ V}$		2.7 to 3.6	_	±20.0	μA
Increase in I _{CC} pe	r input	∆l _{CC}	$V_{IH} = V_{CC} - 0.6$ \	/	2.7 to 3.6	_	750	

DC Characteristics (Ta = –40 to 85°C, 2.3 V \leq V_{CC} \leq 2.7 V)

Chara	cteristics	Symbol	Test C	condition	V _{CC} (V)	Min	Max	Unit
Input voltage	High level	VIH	-		2.3 to 2.7	1.6	_	V
input voltage	Low level	VIL	-		2.3 to 2.7		0.7	v
High level Output voltage			I _{OH} = -100 μA	2.3 to 2.7	V _{CC} - 0.2	_		
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -6 mA	2.3	2.0	_		
	_			I _{OH} = -12 mA	2.3	1.8	_	
			I _{OH} = -18 mA	2.3	1.7	_	V	
		V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 100 μA	2.3 to 2.7		0.2	-
	Low level			I _{OL} = 12 mA	2.3		0.4	
				I _{OL} = 18 mA	2.3		0.6	
Input leakage curr	ent	I _{IN}	$V_{IN} = 0$ to 3.6 V		2.3 to 2.7		±5.0	μA
Power off leakage current		IOFF	V_{IN} , $V_{OUT} = 0$ to 3	3.6 V	0		10.0	μA
		ICC	$V_{IN} = V_{CC}$ or GND		2.3 to 2.7		20.0	
	Quiescent supply current		$V_{CC} \leq (V_{IN}, V_{OUT})$	-) ≦ 3.6 V	2.3 to 2.7		±20.0	μA

DC Characteristics (Ta = -40 to 85°C, 1.8 V \leq V_{CC} < 2.3 V)

Charac	cteristics	Symbol	Test Condition			Min	Max	Unit	
					V _{CC} (V)				
Input voltage	High level	VIH	-		1.8 to 2.3	V _{CC} × 0.7	—	v	
input voltage	Low level	VIL	-		1.8 to 2.3	_	$V_{CC} \times 0.2$	v	
High level	Vон	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -100 μA	1.8	V _{CC} - 0.2	_			
Output voltage					I _{OH} = -6 mA	1.8	1.4	_	v
		V _{OL}		I _{OL} = 100 μA	1.8		0.2		
LOW	Low level		$V_{IN} = V_{IH}$	I _{OL} = 6 mA	1.8	_	0.3		
Input leakage curre	ent	l _{IN}	$V_{IN} = 0$ to 3.6 V	-	1.8	_	±5.0	μA	
Power off leakage	current	IOFF	V_{IN} , $V_{OUT} = 0$ to 3	3.6 V	0		10.0	μA	
Quiescent supply current			V _{IN} = V _{CC} or GND		1.8	_	20.0		
Quiescent supply (Junent	Icc	$V_{CC} \leq (V_{IN}, V_{OUT}) \leq 3.6 \text{ V}$		1.8		±20.0	μA	

AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0 \text{ ns}$, $C_L = 30 \text{ pF}$, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
			1.8	1.5	7.4	
Propagation delay time	t _{pLH} t _{pHL}	Figure 1, Figure 2	$\textbf{2.5}\pm\textbf{0.2}$	1.0	3.7	ns
	γμ⊔Γ		$\textbf{3.3}\pm\textbf{0.3}$	0.8	2.8	

For $C_L = 50 \text{ pF}$, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0 \text{ ns}$, $C_L = 30 \text{ pF}$)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 11)	1.8	0.25	
Quiet output maximum dynamic VOL	V _{OLP}	V _{IN} = 2.5 V, V _{IL} = 0 V	(Note 11)	2.5	0.6	V
		$V_{IN} = 3.3 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 11)	3.3	0.8	
		V _{IN} = 1.8 V, V _{IL} = 0 V	(Note 11)	1.8	-0.25	
Quiet output minimum dynamic V_{OL}	V _{OLV}	$V_{IN} = 2.5 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 11)	2.5	-0.6	V
		$V_{IN} = 3.3 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 11)	3.3	-0.8	
		V _{IN} = 1.8 V, V _{IL} = 0 V	(Note 11)	1.8	1.5	
Quiet output minimum dynamic V_{OH}	V _{OHV}	$V_{IN} = 2.5 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 11)	2.5	1.9	V
		$V_{IN} = 3.3 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 11)	3.3	2.2	

Note 11: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

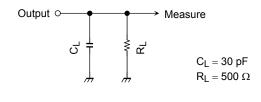
Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	—		1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 12)	1.8, 2.5, 3.3	20	pF

Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

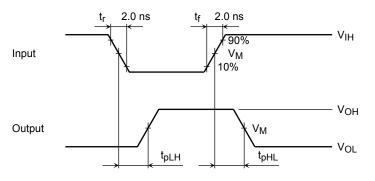
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

AC Test Circuit

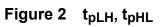




AC Waveforms



Symbol	V _{CC}						
Symbol	$3.3\pm0.3~V$	$2.5\pm0.2\;V$	1.8 V				
VIH	2.7 V	V _{CC}	V _{CC}				
VM	1.5 V	V _{CC} /2	V _{CC} /2				

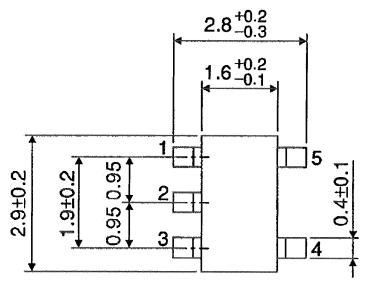


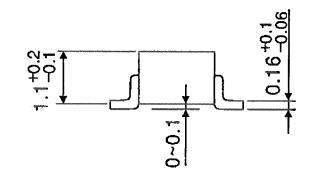
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Package Dimensions

SSOP5-P-0.95

Unit : mm



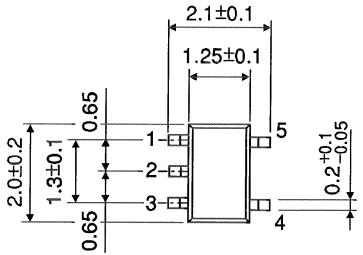


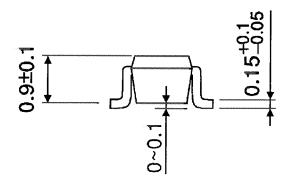
Weight: 0.016 g (typ.)

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Package Dimensions

Unit : mm





Weight: 0.006 g (typ.)

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