300 mA CMOS LDO Regulator

Description

The CAT6218 is a 300 mA CMOS low dropout regulator that provides fast response time during load current and line voltage changes.

The quick-start feature allows the use of an external bypass capacitor to reduce the overall output noise without affecting the turn-on time of just $150 \ \mu s$.

With zero shutdown current and low ground current of $55 \,\mu A$ typical, the CAT6218 is ideal for battery–operated devices with supply voltages from 2.3 V to 5.5 V. An internal under voltage lockout circuit disables the output at supply voltages under 2.1 V typical.

The CAT6218 offers 1% initial accuracy and low dropout voltage, 180 mV typical at 300 mA. Stable operation is provided with a small value ceramic capacitor, reducing required board space and component cost.

Other features include fold-back current limit and thermal protection.

The device is available in the low profile (1 mm max height) 5-lead thin SOT23 package.

Features

- Guaranteed 300 mA Output Current
- Low Dropout Voltage of 180 mV Typical at 300 mA
- Stable with 1 µF Ceramic Output Capacitor
- External 10 nF Bypass Capacitor for Low Noise
- Quick-start Feature
- No-load Ground Current of 55 µA Typical
- Full-load Ground Current of 80 µA Typical
- $\pm 1.0\%$ Initial Accuracy (V_{OUT} ≥ 2.0 V)
- $\pm 2.0\%$ Accuracy Over Temperature (V_{OUT} ≥ 2.0 V)
- "Zero" Current Shutdown Mode
- Fold-back Current Limit and Under-voltage Lockout
- Thermal Protection
- Thin SOT23-5 Package
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Cellular Phones
- Battery-powered Devices
- Consumer Electronics



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MARKING DIAGRAM



RU= CAT6218 Device Code

Y = Production Year (last digit)

M = Production Month: 1 – 9, A, B, C

PIN FUNCTION

Pin #	Name	Function		
1	VIN	Supply voltage input.		
2	GND	Ground reference.		
3	EN	Enable input (active high); a 2.5 $M\Omega$ pull-down resistor is provided.		
4	BYP	Optional bypass capacitor connec- tion for noise reduction and PSRR enhancing.		
5	VOUT	LDO Output Voltage.		

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.



Figure 1. Typical Application Circuit

Pin Function

VIN is the supply pin for the LDO. A small 1 μ F ceramic bypass capacitor is required between the V_{IN} pin and ground near the device. When using longer connections to the power supply, C_{IN} value can be increased without limit. The operating input voltage range is from 2.3 V to 5.5 V.

EN is the enable control logic (active high) for the regulator output. It has a 2.5 M Ω pull-down resistor, which assures that if EN pin is left open, the circuit is disabled.

VOUT is the LDO regulator output. A small 1 μ F ceramic bypass capacitor is required between the V_{OUT} pin and ground for stability. For better transient response, its value can be increased to 4.7 μ F.

Table 1. ABSOLUTE MAXIMUM RATINGS



Figure 2. CAT6218 Functional Block Diagram

The capacitor should be located near the device. ESR domain is 5 m Ω to 500 m Ω . V_{OUT} can deliver a maximum guaranteed current of 300 mA. For input-to-output voltages higher than 1 V, a continuous 300 mA output current might turn-on the thermal protection. A 250 Ω internal shutdown switch discharges the output capacitor in the no-load condition.

GND is the ground reference for the LDO. The pin must be connected to the ground plane on the PCB.

BYP is the reference bypass pin. An optional $0.01 \,\mu\text{F}$ capacitor can be connected between BYP pin and GND to reduce the output noise and enhance the PSRR at high frequency.

Parameter	Rating	Unit
V _{IN}	0 to 6.5	V
V _{EN} , V _{OUT}	–0.3 to V _{IN} + 0.3	V
Junction Temperature, T _J	+150	°C
Power Dissipation, P _D	Internally Limited (Note 1)	mW
Storage Temperature Range, T _S	-65 to +150	°C
Lead Temperature (soldering, 5 sec.)	260	°C
ESD Rating (Human Body Model)	3	kV

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 2. RECOMMENDED OPERATING CONDITIONS (Note 2)

Parameter	Range	Unit
V _{IN}	2.3 to 5.5	V
V _{EN}	0 to V _{IN}	V
Junction Temperature Range, T _J	-40 to +125	°C
Package Thermal Resistance (SOT23–5), θ_{JA}	235	°C/W

NOTE: Typical application circuit with external components is shown above.

The maximum allowable power dissipation at any T_A (ambient temperature) is P_{Dmax} = (T_{Jmax} - T_A)/θ_{JA}. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.

2. The device is not guaranteed to work outside its operating rating.

Table 3. ELECTRICAL OPERATING CHARACTERISTICS (Note 3)

 $(V_{IN} = V_{OUT} + 1.0 \text{ V}, V_{EN} = \text{High}, I_{OUT} = 100 \ \mu\text{A}, C_{IN} = C_{OUT} = 1 \ \mu\text{F}$, ambient temperature of 25°C (over recommended operating conditions unless specified otherwise). **Bold numbers** apply for the entire junction temperature range.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OUT-ACC}	Output Voltage Accuracy	Initial accuracy for $V_{OUT} \ge 2.0 V$	-1.0		+1.0	%
		(Note 6)	-2.0		+2.0	
TC _{OUT}	Output Voltage Temp. Coefficient			40		ppm/°C
V _{R-LINE}	Line Regulation	V _{IN} = V _{OUT} + 1.0 V to 5.5 V	-0.2	±0.1	+0.2	%/V
			-0.4		+0.4	
V _{R-LOAD}	Load Regulation	I _{OUT} = 100 μA to 300 mA		0.7	1.2	%
					1.5	
V _{DROP}	Dropout Voltage (Note 4)	I _{OUT} = 300 mA		180	250	mV
					300	
I _{GND}	Ground Current	I _{OUT} = 0 μA		55	75	μΑ
					90	
		I _{OUT} = 300 mA		80		
I _{GND-SD}	Shutdown Ground Current	V _{EN} < 0.4 V			1	μΑ
					2	
PSRR	Power Supply Rejection Ratio	f = 1 kHz, C _{BYP} = 10 nF		64		dB
		f = 20 kHz, C _{BYP} = 10 nF		54		
I _{SC}	Output short circuit current limit	V _{OUT} = 0 V		180		mA
T _{ON}	Turn–On Time	C _{BYP} = 10 nF		150		μs
e _N	Output Noise Voltage (Note 5)	BW = 10 Hz to 100 kHz		45		μVrms
R _{OUT-SH}	Shutdown Switch Resistance			250		Ω
R _{EN}	Enable pull-down resistor			2.5		MΩ
V _{UVLO}	Under-voltage lock out (UVLO) threshold			2.1		V
ESR	C _{OUT} equivalent series resistance		5		500	mΩ

ENABLE INPUT

V _{HI}	Logic High Level	V _{IN} = 2.3 to 5.5 V	1.8			V
		V_{IN} = 2.3 to 5.5 V, 0°C to +125°C junction temperature	1.6			
V_{LO}	Logic Low Level	V _{IN} = 2.3 to 5.5 V			0.4	V
I _{EN}	Enable Input Current	V _{EN} = 0.4 V		0.15	1	μΑ
		$V_{EN} = V_{IN}$		1.5	4	

THERMAL PROTECTION

T _{SD}	Thermal Shutdown		160	°C
T _{HYS}	Thermal Hysteresis		10	°C

3. Specification for 2.80 V output version unless specified otherwise.

4. Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value measured at 1 V differential. During test, the input voltage stays always above the minimum 2.3 V.

5. Specification for 1.8 V output version.

6. For V_{OUT} < 2.0 V, the initial accuracy is ±2% and across temperature ±3%.









Figure 5. Load Regulation



Figure 7. Ground Current vs. Load Current





Figure 6. Output Voltage vs. Temperature



Figure 8. Ground Current vs. Temperature

TYPICAL CHARACTERISTICS (shown for 2.80 V output option)

(V_{IN} = 3.85 V, I_{OUT} = 100 μ A, C_{IN} = C_{OUT} = 1 μ F, C_{BYP} = 10 nF, T_A = 25°C unless otherwise specified.)



Figure 9. Output Voltage vs. Load Current



Figure 11. Dropout vs. Temperature (300 mA Load)



Figure 13. Enable Threshold vs. Input Voltage



Figure 10. Ground Current vs. Input Voltage



Figure 12. Dropout vs. Load Current



Figure 14. PSRR vs. Frequency (10 mA Load)





Figure 15. PSRR (30 mA Load)



Figure 16. PSRR (200 mA Load)

 $\label{eq:transformation} \begin{array}{l} \textbf{TRANSIENT CHARACTERISTICS} \ (shown for 2.80 \ V \ output \ option) \\ (V_{IN} = 3.85 \ V, \ I_{OUT} = 100 \ \mu\text{A}, \ C_{IN} = C_{OUT} = 1 \ \mu\text{F}, \ C_{BYP} = 10 \ n\text{F}, \ T_A = 25^\circ\text{C} \ unless \ otherwise \ specified.) \end{array}$



(3.85 V to 4.85 V)



Note: All transient characteristics are generated using the evaluation board CAT621XEVAL1.

PACKAGE DIMENSIONS

TSOT-23, 5 LEAD CASE 419AE-01 ISSUE O



SYMBOL MIN NOM MAX А 1.00 A1 0.01 0.05 0.10 A2 0.80 0.87 0.90 0.30 0.45 b с 0.12 0.15 0.20 2.90 BSC D Е 2.80 BSC E1 1.60 BSC 0.95 TYP е L 0.30 0.40 0.50 L1 0.60 REF L2 0.25 BSC 0° 8° θ

TOP VIEW



SIDE VIEW

Notes:

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-193.



END VIEW

Example of Ordering Information (Note 9)



ORDERING INFORMATION

Orderable Part Number	V _{OUT} Voltage	Package	Shipping
CAT6218-150TDGT3	1.50 V	TSOT-23	3,000 / Tape & Reel
CAT6218-180TDGT3	1.80 V	TSOT-23	3,000 / Tape & Reel
CAT6218-240TDGT3	2.40 V	TSOT-23	3,000 / Tape & Reel
CAT6218-250TDGT3	2.50 V	TSOT-23	3,000 / Tape & Reel
CAT6218-270TDGT3	2.70 V	TSOT-23	3,000 / Tape & Reel
CAT6218-280TDGT3	2.80 V	TSOT-23	3,000 / Tape & Reel
CAT6218-285TDGT3 (Note 10)	2.85 V	TSOT-23	3,000 / Tape & Reel
CAT6218-300TDGT3	3.00 V	TSOT-23	3,000 / Tape & Reel
CAT6218-320TDGT3 (Note 10)	3.20 V	TSOT-23	3,000 / Tape & Reel
CAT6218-330TDGT3	3.30 V	TSOT-23	3,000 / Tape & Reel

7. All packages are RoHS-compliant (Lead-free, Halogen-free).

8. The standard finish is NiPdAu.

9. The device used in the above example is a CAT6218–180TDGT3 (V_{OUT} = 1.8 V, in a TSOT–23 package, NiPdAu, Tape & Reel, 3,000/Reel). 10. For other voltage options, please contact your nearest ON Semiconductor Sales office.

11. For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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