# Single 2-Input OR Gate, TTL Level

# LSTTL-Compatible Inputs

The NLU1GT32 MiniGate<sup>™</sup> is an advanced CMOS high-speed 2-input OR gate in ultra-small footprint.

The device input is compatible with TTL-type input thresholds and the output has a full 5.0 V CMOS level output swing.

The NLU1GT32 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

### Features

- High Speed:  $t_{PD} = 3.7 \text{ ns} (Typ) @ V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation:  $I_{CC} = 2 \mu A$  (Max) at  $T_A = 25^{\circ}C$
- TTL-Compatible Input:  $V_{IL} = 0.8 \text{ V}$ ;  $V_{IH} = 2.0 \text{ V}$
- CMOS–Compatible Output: V<sub>OH</sub> > 0.8 V<sub>CC</sub>; V<sub>OL</sub> < 0.1 V<sub>CC</sub> @ Load
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Ultra-Small Packages
- These are Pb-Free Devices

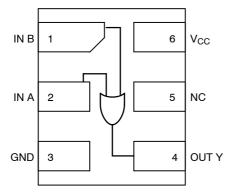


Figure 1. Pinout (Top View)

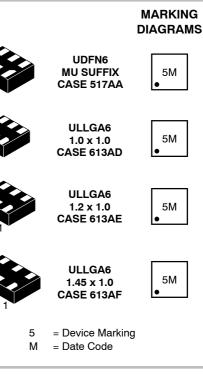


Figure 2. Logic Symbol



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#### PIN ASSIGNMENT

1	IN B
2	IN A
3	GND
4	OUT Y
5	NC
6	V <sub>CC</sub>

#### FUNCTION TABLE

Inp	out	Output
Α	В	Y
L	L	L
L H	H   L	H H
Н	Н	Н

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

### MAXIMUM RATINGS

Symbol	Parameter		Value	Unit
V <sub>CC</sub>	DC Supply Voltage		-0.5 to +7.0	V
V <sub>IN</sub>	DC Input Voltage		-0.5 to +7.0	V
V <sub>OUT</sub>	DC Output Voltage		-0.5 to +7.0	V
I <sub>IK</sub>	DC Input Diode Current	/ <sub>IN</sub> < GND	-20	mA
I <sub>OK</sub>	DC Output Diode Current V <sub>O</sub>	<sub>UT</sub> < GND	±20	mA
Ι <sub>Ο</sub>	DC Output Source/Sink Current		±12.5	mA
I <sub>CC</sub>	DC Supply Current Per Supply Pin		±25	mA
I <sub>GND</sub>	DC Ground Current per Ground Pin		±25	mA
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C
ΤL	Lead Temperature, 1 mm from Case for 10 Seconds		260	°C
Τ <sub>J</sub>	Junction Temperature Under Bias		150	°C
MSL	Moisture Sensitivity		Level 1	
F <sub>R</sub>	Flammability Rating Oxygen Index	c: 28 to 34	UL 94 V-0 @ 0.125 in	
I <sub>LATCHUP</sub>	Latchup Performance Above $V_{CC}$ and Below GND at 125°C (	Note 2)	±500	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.
Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.
Tested to EIA / JESD78.

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter			Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage		1.65	5.5	V
V <sub>IN</sub>	Digital Input Voltage			5.5	V
V <sub>OUT</sub>	Output Voltage			5.5	V
T <sub>A</sub>	Operating Free-Air Temperature		-55	+125	°C
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	$ \begin{array}{l} V_{CC} = 3.3 \ V \pm 0.3 \ V \\ V_{CC} = 5.0 \ V \pm 0.5 \ V \end{array} $	0 0	100 20	ns/V

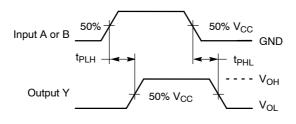
#### DC ELECTRICAL CHARACTERISTICS

			T <sub>A</sub> = 25 °C		T <sub>A</sub> = +85°C		T <sub>A</sub> = −55°C to +125°C				
Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Min	Max	Unit
V <sub>IH</sub>	Low-Level Input Voltage		1.8 3.0 4.5 to 5.5	1.2 1.4 2.0			1.2 1.4 2.0		1.2 1.4 2.0		V
V <sub>IL</sub>	Low-Level Input Voltage		1.8 3.0 4.5 to 5.5			0.3 0.53 0.8		0.3 0.53 0.8		0.3 0.53 0.8	V
V <sub>OH</sub>	High-Level Output Voltage	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -50 \ \mu\text{A}$	3.0 4.5	2.9 4.4	3.0 4.5		2.9 4.4		2.9 4.4		V
		$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -2 \text{ mA}$ $I_{OH} = -4 \text{ mA}$ $I_{OH} = -8 \text{ mA}$	1.8 3.0 4.5	1.40 2.58 3.94			1.38 2.48 3.80		1.37 2.34 3.66		
V <sub>OL</sub>	Low-Level Output Voltage	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OL} = 50 \ \mu\text{A}$	3.0 4.5		0 0	0.1 0.1		0.1 0.1		0.1 0.1	V
		$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OL} = 2 \text{ mA}$ $I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$	1.8 3.0 4.5			0.36 0.36 0.36		0.44 0.44 0.44		0.52 0.52 0.52	
I <sub>IN</sub>	Input Leakage Current	$0 \le V_{IN} \le 5.5 V$	0 to 5.5			±0.1		±1.0		±1.0	μΑ
Icc	Quiescent Supply Current	$0 \le V_{IN} \le V_{CC}$	5.5			2.0		20		40	μΑ
I <sub>CCT</sub>	Quiescent Supply Current	V <sub>IN</sub> = 3.4 V	5.5			1.35		1.50		1.65	mA
I <sub>OPD</sub>	Output Leakage Current	V <sub>OUT</sub> = 5.5 V	0.0			0.5		5.0		10	μΑ

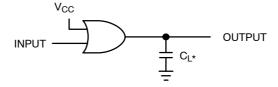
### AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0 \text{ ns}$ )

			Test	т	A = 25 °(	C	<b>T<sub>A</sub></b> = -	⊦85°C	T <sub>A</sub> = - to +1		
Symbol	Parameter	V <sub>CC</sub> (V)	Condition	Min	Тур	Max	Min	Max	Min	Max	Unit
t <sub>PLH</sub> ,	Propagation Delay, Input A or B to Y	1.65 to	C <sub>L</sub> = 15 pF			15.4		16.9		18.7	ns
t <sub>PHL</sub>		1.95	C <sub>L</sub> = 50 pF			23.8		25.2		26.7	
		2.3 to 2.7	C <sub>L</sub> = 15 pF			10.4		11.2		13.2	
			C <sub>L</sub> = 50 pF			14.5		15.5		17.9	
		3.0 to 3.6	C <sub>L</sub> = 15 pF		4.8	7.9		9.5		11.5	
			C <sub>L</sub> = 50 pF		6.1	11.4		13.0		15.5	
		4.5 to 5.5	C <sub>L</sub> = 15 pF		3.7	5.5		6.5		8.0	
			C <sub>L</sub> = 50 pF		4.4	7.5		8.5		10.0	
C <sub>IN</sub>	Input Capacitance				5.5	10		10		10.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 3)	5.0			11						pF

3.  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation  $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$ .  $C_{PD}$  is used to determine the no-load dynamic power consumption:  $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$ .







\*Includes all probe and jig capacitance. A 1–MHz square input wave is recommended for propagation delay tests.

#### Figure 4. Test Circuit

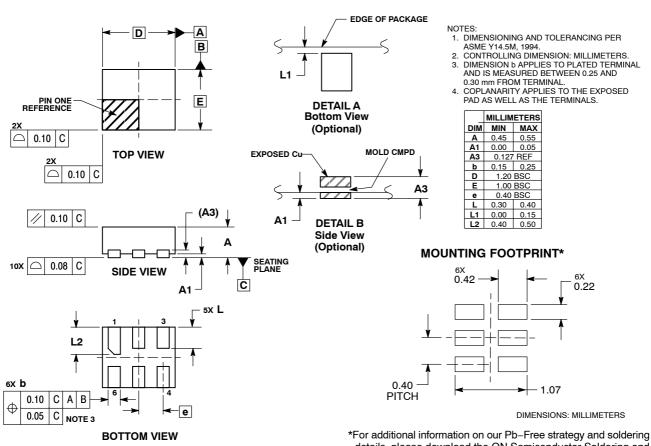
#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NLU1GT32MUTCG	UDFN6 (Pb–Free)	3000 / Tape & Reel
NLU1GT32AMX1TCG	ULLGA6, 1.45 x 1.0, 0.5P (Pb–Free)	3000 / Tape & Reel
NLU1GT32BMX1TCG	ULLGA6, 1.2 x 1.0, 0.4P (Pb–Free)	3000 / Tape & Reel
NLU1GT32CMX1TCG	ULLGA6, 1.0 x 1.0, 0.35P (Pb–Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

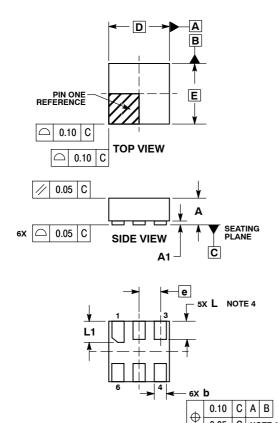
UDFN6, 1.2x1.0, 0.4P CASE 517AA-01 ISSUE B



For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### PACKAGE DIMENSIONS

ULLGA6 1.0x1.0, 0.35P CASE 613AD-01 **ISSUE A** 



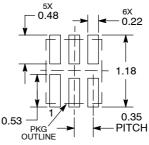
**BOTTOM VIEW** 

0.05 C NOTE 3

- NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 mm FROM THE TERMINAL TIP. 4. A MAXIMUM OF 0.05 PULL BACK OF THE PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.

FACINAUL IS ALLOW					
	MILLIMETERS				
DIM	MIN	MAX			
Α		0.40			
A1	0.00	0.05			
b	0.12	0.22			
D	1.00 BSC				
E	1.00	BSC			
е	0.35	BSC			
L	0.25	0.35			
L1	0.30	0.40			

**MOUNTING FOOTPRINT** SOLDERMASK DEFINED\*

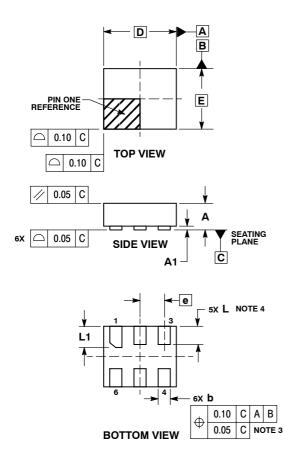


DIMENSIONS: MILLIMETERS

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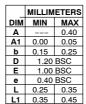
#### PACKAGE DIMENSIONS

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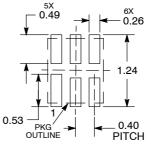


NOTES:

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- DIES: DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 2. З.
- 0.30 mm FROM THE TERMINAL TIP. A MAXIMUM OF 0.05 PULL BACK OF THE 4. PLATED TERMINAL FROM THE EDGE OF THE PACKAGE IS ALLOWED.



#### **MOUNTING FOOTPRINT** SOLDERMASK DEFINED\*

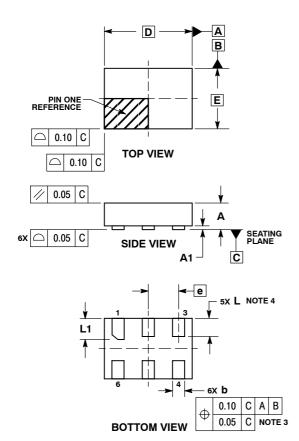


DIMENSIONS: MILLIMETERS

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#### PACKAGE DIMENSIONS

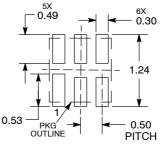
ULLGA6 1.45x1.0, 0.5P CASE 613AF-01 **ISSUE A** 



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	-				
	MILLIMETERS				
DIM	MIN MAX				
Α		0.40			
A1	0.00	0.05			
b	0.15	0.25			
D	1.45 BSC				
E	1.00	BSC			
е	0.50 BSC				
L	0.25	0.35			
L1	0.30	0.40			

**MOUNTING FOOTPRINT** SOLDERMASK DEFINED\*



DIMENSIONS: MILLIMETERS

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