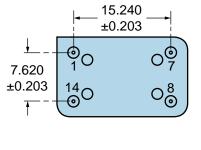


Operating Temperature Range 0°C to Operating Temperature Range 0°C to Supply Voltage 5.0Vd Input Current 40mA Output Voltage Logic High (Voh) 2.4Vd Output Voltage Logic Low (Vol) 0.4Vd Rise/Fall Time 5n Sed Duty Cycle 50 ±1 Load Drive Capability 10TT	ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the ating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, k, and Vibration) m/year Maximum o +70°C dc ±5% A Maximum dc Minimum with TTL Load, Vdd-0.5Vdc with HCMOS Load dc Maximum with TTL Load, 0.5Vdc Maximum with HCMOS Load	
Operating Temperature Range0°C toSupply Voltage5.0VdInput Current40mAOutput Voltage Logic High (Voh)2.4VdOutput Voltage Logic Low (Vol)0.4VdRise/Fall Time5n SecDuty Cycle50 ±1Load Drive Capability10TT	dc ±5% A Maximum dc Minimum with TTL Load, Vdd-0.5Vdc with HCMOS Load	
Supply Voltage 5.0Vd Input Current 40mA Output Voltage Logic High (Voh) 2.4Vd Output Voltage Logic Low (Vol) 0.4Vd Rise/Fall Time 5n Sea Duty Cycle 50 ±1 Load Drive Capability 10TTH	dc ±5% A Maximum dc Minimum with TTL Load, Vdd-0.5Vdc with HCMOS Load	
Input Current40mAOutput Voltage Logic High (Voh)2.4VdOutput Voltage Logic Low (Vol)0.4VdRise/Fall Time5nSedDuty Cycle50 ±1Load Drive Capability10TT	A Maximum dc Minimum with TTL Load, Vdd-0.5Vdc with HCMOS Load	
Output Voltage Logic High (Voh)2.4VdOutput Voltage Logic Low (Vol)0.4VdRise/Fall Time5n SedDuty Cycle50 ±1Load Drive Capability10TT	dc Minimum with TTL Load, Vdd-0.5Vdc with HCMOS Load	
Output Voltage Logic Low (Vol)0.4VdRise/Fall Time5nSedDuty Cycle50 ±1Load Drive Capability10TT		
Rise/Fall Time5nSerDuty Cycle50 ±1Load Drive Capability10TT	dc Maximum with TTL Load, 0.5Vdc Maximum with HCMOS Load	
Duty Cycle 50 ±1 Load Drive Capability 10TTT		
Load Drive Capability 10TT	5nSec Maximum (0.4Vdc to 2.4Vdc w/TTL Load, 20% to 80% of waveform w/HCMOS Load)	
	50 ±10(%) (Measured at 1.4Vdc with TTL Load or at 50% of waveform with HCMOS Load)	
	10TTL Load or 15pF HCMOS Load Maximum	
Output Logic Type CMOS	S	
Control Voltage 2.5Vd	dc ±2.0Vdc	
Frequency Deviation ±50pp	pm Minimum	
Linearity 20% I	20% Maximum	
Transfer Function Positi	Positive Transfer Characteristic	
Absolute Clock Jitter ±100	±100pSec Maximum	
One Sigma Clock Period Jitter ±25ps	Sec Maximum	
Start Up Time 10mS	Sec Maximum	
Storage Temperature Range -55°C	-55°C to +125°C	
ENVIRONMENTAL & MECHANICAL SPECIFICATIONS		

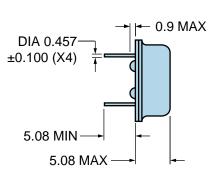
Fine Leak Test	MIL-STD-883, Method 1014, Condition A	
Gross Leak Test	MIL-STD-883, Method 1014, Condition C	
Lead Integrity	MIL-STD-883, Method 2004	
Mechanical Shock	MIL-STD-202, Method 213, Condition C	
Resistance to Soldering Heat	MIL-STD-202, Method 210	
Resistance to Solvents	MIL-STD-202, Method 215	
Solderability	MIL-STD-883, Method 2003	
Temperature Cycling	MIL-STD-883, Method 1010	
Vibration	MIL-STD-883, Method 2007, Condition A	



MECHANICAL DIMENSIONS (all dimensions in millimeters)

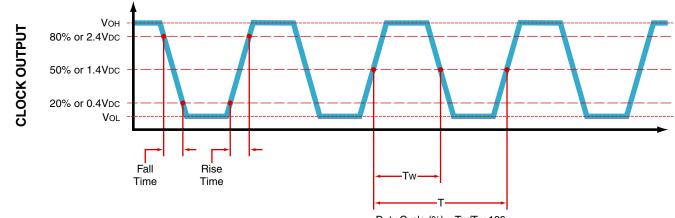






PIN	CONNECTION	
1	Control Voltage	
7	Ground/Case Ground	
8	Output	
14	Supply Voltage	
LINE	MARKING	
1	ECLIPTEK	
2	EC31 EC31=Product Series	
3	27.000M	
4	XXYZZ XX=Ecliptek Manufacturing Code Y=Last Digit of the Year ZZ=Week of the Year	

OUTPUT WAVEFORM



Duty Cycle (%) = Tw/T x 100



Frequency

Counter

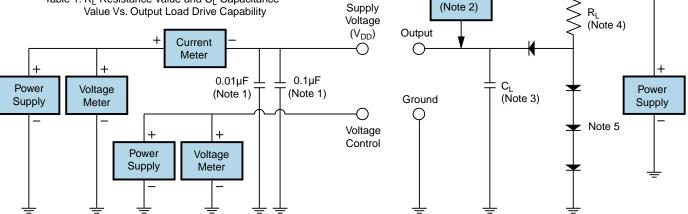
Oscilloscope

Probe

Test Circuit for TTL Output

Output Load Drive Capability	R _L Value (Ohms)	C _L Value (pF)
10TTL	390	15
5TTL	780	15
2TTL	1100	6
10LSTTL	2000	15
1TTL	2200	3

Table 1: R_L Resistance Value and C_L Capacitance



Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and V_{DD} pin is required.

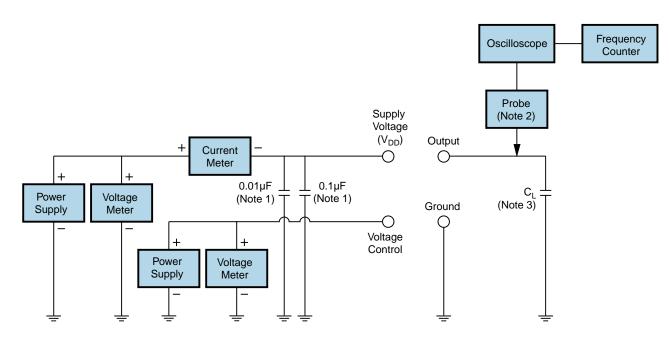
Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value C_{L} includes sum of all probe and fixture capacitance.

Note 4: Resistance value RL is shown in Table 1. See applicable specification sheet for 'Load Drive Capability'. Note 5: All diodes are MMBD7000, MMBD914, or equivalent.



Test Circuit for CMOS Output



Note 1: An external 0.1µF low frequency tantalum bypass capacitor in parallel with a 0.01µF high frequency ceramic bypass capacitor close to the package ground and V_{DD} pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value \dot{C}_L includes sum of all probe and fixture capacitance.

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Recommended Solder Reflow Methods

EC3100-27.000M



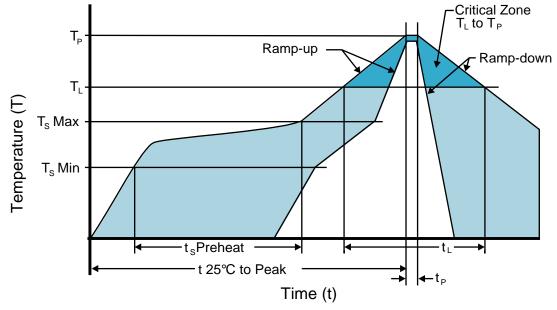
High Temperature Solder Bath (Wave Solder)

T_s MAX to T_L (Ramp-up Rate)	3°C/second Maximum	
Preheat		
- Temperature Minimum (Ts MIN)	150°C	
- Temperature Typical (T _s TYP)	175°C	
- Temperature Maximum (T _s MAX)	200°C	
- Time (t _s MIN)	60 - 180 Seconds	
Ramp-up Rate (T⊾ to T _P)	3°C/second Maximum	
Time Maintained Above:		
- Temperature (T∟)	217°C	
- Time (t∟)	60 - 150 Seconds	
Peak Temperature (T _P)	260°C Maximum for 10 Seconds Maximum	
Target Peak Temperature (T _P Target)	250°C +0/-5°C	
Time within 5°C of actual peak (t _p)	20 - 40 seconds	
Ramp-down Rate	6°C/second Maximum	
Time 25°C to Peak Temperature (t)	8 minutes Maximum	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to back of PCB board and device leads only. Do not use this method for product with the Gull Wing option.	

ECLIPTEK CORPORATION

Recommended Solder Reflow Methods

EC3100-27.000M



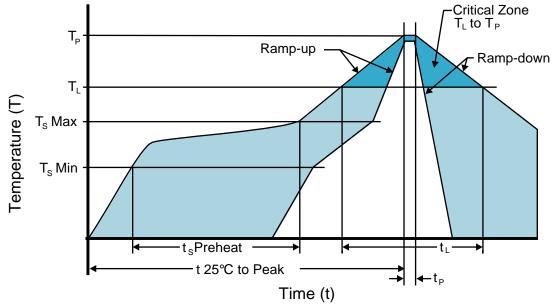
Low Temperature Infrared/Convection 185°C

T MAX to T (Romn un Roto)	5°C/accord Maximum	
T_s MAX to T_L (Ramp-up Rate)	5°C/second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	N/A	
- Temperature Typical (T _s TYP)	150°C	
- Temperature Maximum (T _s MAX)	N/A	
- Time (t _s MIN)	60 - 120 Seconds	
Ramp-up Rate (T _L to T _P)	5°C/second Maximum	
Time Maintained Above:		
- Temperature (T∟)	150°C	
- Time (t∟)	200 Seconds Maximum	
Peak Temperature (T _P)	185°C Maximum	
Target Peak Temperature (T _P Target)	185°C Maximum 2 Times	
Time within 5°C of actual peak (t _p)	10 seconds Maximum 2 Times	
Ramp-down Rate	5°C/second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device. Use this method only for product with the Gull Wing option.	

ECLIPTEK CORPORATION

Recommended Solder Reflow Methods

EC3100-27.000M



Low Temperature Solder Bath (Wave Solder)

T _s MAX to T _L (Ramp-up Rate)	5°C/second Maximum	
Preheat		
- Temperature Minimum (T _s MIN)	N/A	
- Temperature Typical (T _s TYP)	150°C	
- Temperature Maximum (T _s MAX)	N/A	
- Time (t _s MIN)	30 - 60 Seconds	
Ramp-up Rate (T _L to T _P)	5°C/second Maximum	
Time Maintained Above:		
- Temperature (T∟)	150°C	
- Time (t∟)	200 Seconds Maximum	
Peak Temperature (T _P)	245°C Maximum	
Target Peak Temperature (T _P Target)	245°C Maximum 1 Time / 235°C Maximum 2 Times	
Time within 5°C of actual peak (t _p)	5 seconds Maximum 1 Time / 15 seconds Maximum 2 Times	
Ramp-down Rate	5°C/second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to back of PCB board and device leads only. Do not use this method for product with the Gull Wing option.	

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum. (Temperatures listed are applied to device leads only. This method can be utilized with both Gull Wing and Non-Gull Wing devices.)

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum. (Temperatures listed are applied to device leads only. This method can be utilized with both Gull Wing and Non-Gull Wing devices.)