





EH59 25 ET T TS -33.33M

Series — RoHS Compliant (Pb-free) 1.8V 4 Pad 2.0mm x 2.5mm Ceramic SMD LVCMOS Oscillator

Frequency Tolerance/Stability — ±25ppm Maximum

Operating Temperature Range --40°C to +85°C

Nominal Frequency 33.333MHz

Pin 1 Connection
Tri-State (High Impedance)

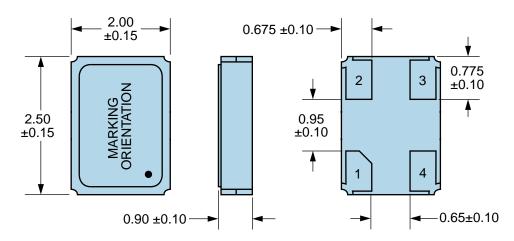
- Duty Cycle 50 ±5(%)

Operating Tem 260°C Reflow, sping at 25°C   perating Temperature Range   -40°C to +85°C   pupply Voltage   1.8Vdc ±5%   4mA Maximum   50°C vold Mi 4ma Maximum   4ma Maximum   4ma Maximum   50°C vold Mi 4maximum   50°C vold Mi 50°C to +85°C   4ma Maximum   50°C vold Mi 50°C vo	
Operating Tem 260°C Reflow, sping at 25°C   perating Temperature Range   -40°C to +85°C   pupply Voltage   1.8Vdc ±5%   4mA Maximum   50°C Vdd Mi 4ma Maximum   4ma Maximum   4ma Maximum   4ma Maximum   4ma Maximum   5nSec Maximum   50°C to +85°C   4ma Maximum   5nSec Ma	perature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°, Shock, and Vibration) aximum
perating Temperature Range  1.8Vdc ±5%  1.8Vdc ±5%  put Current  4mA Maximum  10tput Voltage Logic High (Voh)  10% of Vdd Mi  10se/Fall Time	
put Current  4mA Maximum utput Voltage Logic High (Voh) 90% of Vdd Mi utput Voltage Logic Low (Vol) 10% of Vdd Mi ise/Fall Time 6nSec Maximum uty Cycle 50 ±5(%) (Mea bad Drive Capability 15pF Maximum utput Logic Type CMOS n 1 Connection 7ri-State (High 1mpedance)	
put Current  utput Voltage Logic High (Voh)  utput Voltage Logic Low (Vol)  ise/Fall Time  6nSec Maximu  tuty Cycle  50 ±5(%) (Mea  bad Drive Capability  utput Logic Type  n 1 Connection  i-State Input Voltage (Vih and Vil)  4mA Maximum  50% of Vdd Mi  Impedance)	
utput Voltage Logic High (Voh)  utput Voltage Logic Low (Vol)  ise/Fall Time  ond Drive Capability  utput Logic Type  n 1 Connection  ii-State Input Voltage (Vih and Vil)  90% of Vdd Mi 90% of Vdd Mi 90% of Vdd Mi Impedance)	
utput Voltage Logic Low (Vol)  10% of Vdd Maise/Fall Time 6nSec Maximu uty Cycle 50 ±5(%) (Mean Drive Capability 15pF Maximum utput Logic Type CMOS 11 Connection Tri-State (High Impedance)	(No Load)
ise/Fall Time 6nSec Maximu tuty Cycle 50 ±5(%) (Mea bad Drive Capability 15pF Maximun tutput Logic Type CMOS n 1 Connection Tri-State (High 15pState Input Voltage (Vih and Vil) 15pF Maximun 15pF Maxim	nimum (IOH = -8mA)
uty Cycle 50 ±5(%) (Mea pad Drive Capability 15pF Maximum utput Logic Type CMOS 11 Connection Tri-State (High ii-State Input Voltage (Vih and Vil) Impedance)	ximum (IOL = +8mA)
pad Drive Capability  utput Logic Type  n 1 Connection  ii-State Input Voltage (Vih and Vil)  7 Tri-State Impedance)	m (Measured at 20% to 80% of waveform)
utput Logic Type CMOS n 1 Connection Tri-State (High i-State Input Voltage (Vih and Vil) 90% of Vdd Mi	sured at 50% of waveform)
n 1 Connection Tri-State (High i-State Input Voltage (Vih and Vil) 90% of Vdd Mi Impedance)	
ri-State Input Voltage (Vih and Vil) 90% of Vdd Mi Impedance)	
Impedance)	Impedance)
andhy Current 10uA Maximur	nimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High
TOPA MAXIMU	
bsolute Clock Jitter ±125pSec Max	n (Pin 1 = Ground)
art Up Time 10mSec Maxin	
orage Temperature Range -55°C to +125°	imum

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS		
ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500V	
Fine Leak Test	MIL-STD-883, Method 1014, Condition A	
Flammability	UL94-V0	
Gross Leak Test	MIL-STD-883, Method 1014, Condition C	
Mechanical Shock	MIL-STD-883, Method 2002, Condition B	
Moisture Resistance	MIL-STD-883, Method 1004	
Moisture Sensitivity	J-STD-020, MSL 1	
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K	
Resistance to Solvents	MIL-STD-202, Method 215	
Solderability	MIL-STD-883, Method 2003	
Temperature Cycling	MIL-STD-883, Method 1010, Condition B	
Vibration	MIL-STD-883, Method 2007, Condition A	



## **MECHANICAL DIMENSIONS (all dimensions in millimeters)**

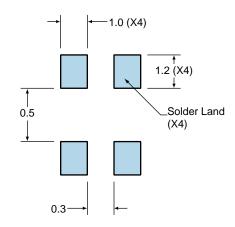


PIN	CONNECTION
1	Tri-State
2	Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	EPO
2	XXXXX XXXXX=Ecliptek Manufacturing Code

### **Suggested Solder Pad Layout**

All Dimensions in Millimeters



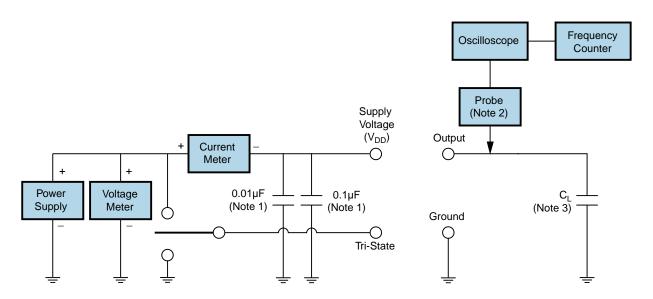
All Tolerances are ±0.1



#### **OUTPUT WAVEFORM & TIMING DIAGRAM**



#### **Test Circuit for CMOS Output**



- Note 1: An external  $0.1\mu\text{F}$  low frequency tantalum bypass capacitor in parallel with a  $0.01\mu\text{F}$  high frequency ceramic bypass capacitor close to the package ground and  $V_{DD}$  pin is required.
- Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.
- Note 3: Capacitance value  $\dot{C}_L$  includes sum of all probe and fixture capacitance.



## **Recommended Solder Reflow Methods**



## **High Temperature Infrared/Convection**

T <sub>s</sub> MAX to T <sub>∟</sub> (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (Ts MIN)	150°C
- Temperature Typical (T <sub>s</sub> TYP)	175°C
- Temperature Maximum (T <sub>s</sub> MAX)	200°C
- Time (t <sub>s</sub> MIN)	60 - 180 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	217°C
- Time (t∟)	60 - 150 Seconds
Peak Temperature (T <sub>P</sub> )	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T <sub>P</sub> Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1



## **Recommended Solder Reflow Methods**



### Low Temperature Infrared/Convection 240°C

T <sub>S</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	N/A
- Temperature Typical (T <sub>S</sub> TYP)	150°C
- Temperature Maximum (T <sub>s</sub> MAX)	N/A
- Time (t <sub>s</sub> MIN)	60 - 120 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	5°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	150°C
- Time (t∟)	200 Seconds Maximum
Peak Temperature (T <sub>P</sub> )	240°C Maximum
Target Peak Temperature (T <sub>P</sub> Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (tp)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

### **Low Temperature Manual Soldering**

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

#### **High Temperature Manual Soldering**

260°C Maximum for 5 seconds Maximum, 2 times Maximum.